



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®2006 = 60.6

Express5800/R120f-1M (Intel Xeon E5-2640 v3)

SPECint_base2006 = 58.7

CPU2006 license: 9006

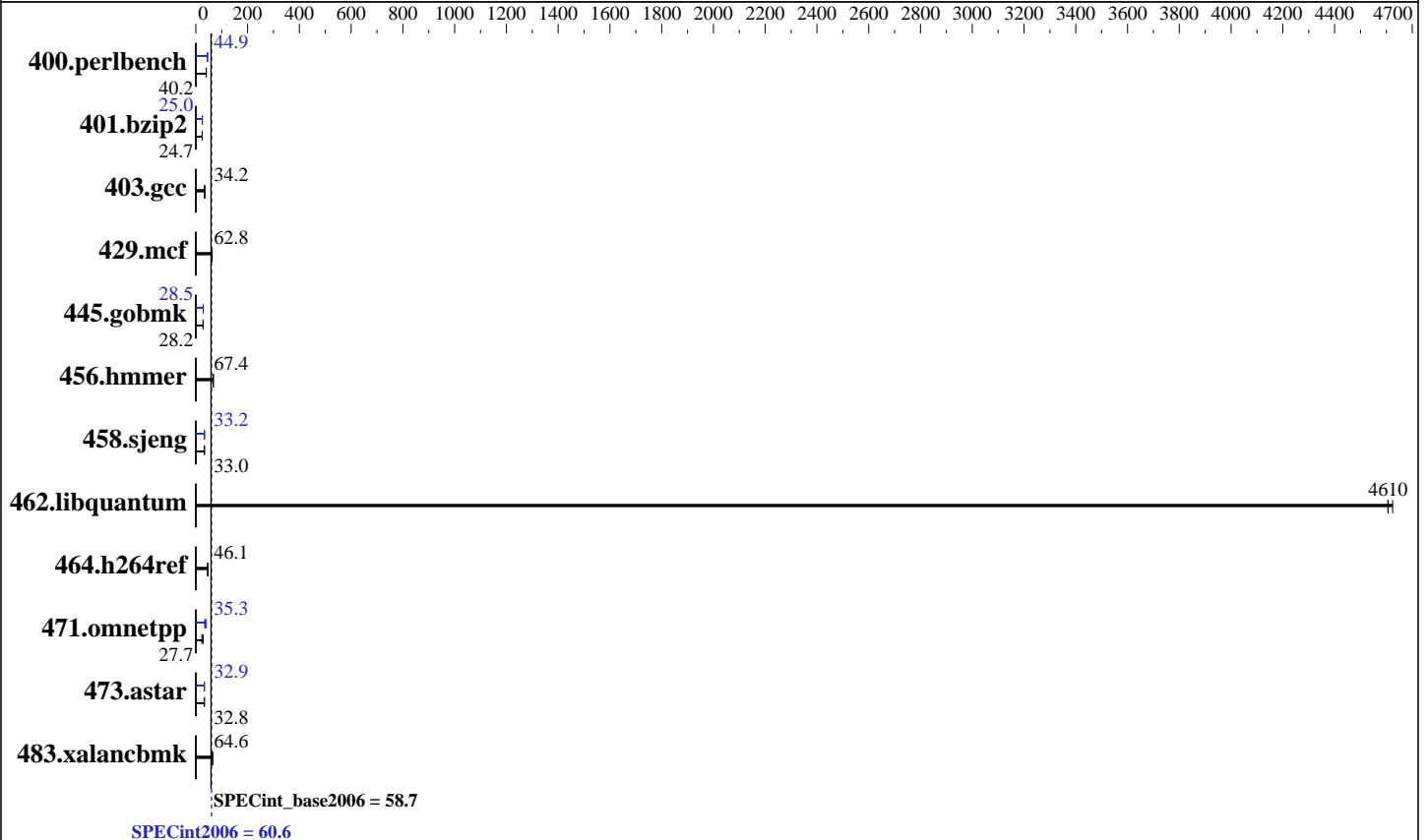
Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2640 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.17.1.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	243	40.3	244	40.1	<u>243</u>	<u>40.2</u>	217	44.9	218	44.9	<u>217</u>	<u>44.9</u>
401.bzip2	390	24.7	391	24.7	<u>391</u>	<u>24.7</u>	<u>386</u>	<u>25.0</u>	385	25.1	388	24.9
403.gcc	235	34.2	<u>235</u>	<u>34.2</u>	236	34.2	235	34.2	<u>235</u>	<u>34.2</u>	236	34.2
429.mcf	146	62.6	145	63.1	<u>145</u>	<u>62.8</u>	146	62.6	145	63.1	<u>145</u>	<u>62.8</u>
445.gobmk	372	28.2	<u>372</u>	<u>28.2</u>	372	28.2	368	28.5	369	28.4	<u>368</u>	<u>28.5</u>
456.hammer	<u>139</u>	<u>67.4</u>	139	67.2	139	67.4	<u>139</u>	<u>67.4</u>	139	67.2	139	67.4
458.sjeng	366	33.0	367	33.0	<u>366</u>	<u>33.0</u>	365	33.2	<u>365</u>	<u>33.2</u>	365	33.2
462.libquantum	4.50	4610	<u>4.50</u>	<u>4610</u>	4.48	4620	4.50	4610	<u>4.50</u>	<u>4610</u>	4.48	4620
464.h264ref	481	46.0	479	46.2	<u>480</u>	<u>46.1</u>	481	46.0	479	46.2	<u>480</u>	<u>46.1</u>
471.omnetpp	270	23.1	224	27.8	<u>226</u>	<u>27.7</u>	<u>177</u>	<u>35.3</u>	154	40.6	181	34.6
473.astar	215	32.7	<u>214</u>	<u>32.8</u>	212	33.1	<u>213</u>	<u>32.9</u>	213	32.9	214	32.9
483.xalancbmk	107	64.7	107	64.6	<u>107</u>	<u>64.6</u>	107	64.7	107	64.6	<u>107</u>	<u>64.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance
Patrol Scrub: Disabled
Hyper-Threading: Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "16"

The Express5800/R120f-1M (Intel Xeon E5-2640 v3) and the Express5800/R120f-2M (Intel Xeon E5-2640 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2640 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
 401.bzip2: `-DSPEC_CPU_LP64`
 403.gcc: `-DSPEC_CPU_LP64`
 429.mcf: `-DSPEC_CPU_LP64`
 456.hmmer: `-DSPEC_CPU_LP64`
 458.sjeng: `-DSPEC_CPU_LP64`
 462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
 464.h264ref: `-DSPEC_CPU_LP64`
 473.astar: `-DSPEC_CPU_LP64`
 483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `basepeak = yes`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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For other inquiries, please contact webmaster@spec.org.

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