



SPEC® CFP2006 Result

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Sugon

SPECfp®2006 = 99.9

Sugon I620-G20 (Intel Xeon E5-2640 v3)

SPECfp_base2006 = 97.3

CPU2006 license: 9046

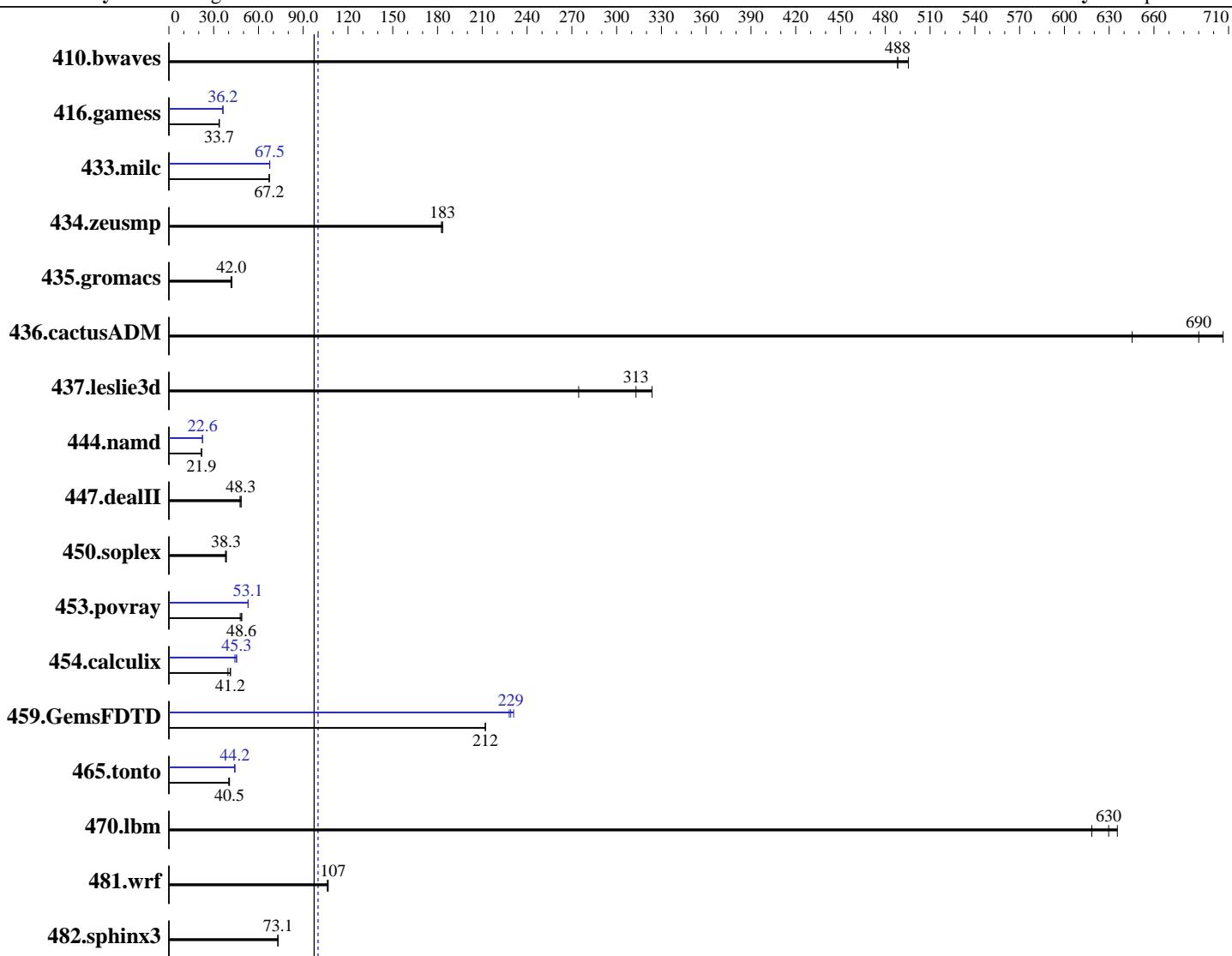
Test date: Sep-2014

Test sponsor: Sugon

Hardware Availability: Sep-2014

Tested by: Sugon

Software Availability: Sep-2013



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Hardware

CPU Name:	Intel Xeon E5-2640 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 2.0 TB SATA 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	27.8	488	27.4	495	<u>27.8</u>	<u>488</u>	27.8	488	27.4	495	<u>27.8</u>	<u>488</u>
416.gamess	581	33.7	<u>580</u>	<u>33.7</u>	580	33.7	541	36.2	<u>541</u>	<u>36.2</u>	542	36.1
433.milc	136	67.3	<u>137</u>	<u>67.2</u>	137	67.2	136	67.5	<u>136</u>	<u>67.5</u>	136	67.6
434.zeusmp	49.8	183	49.6	183	<u>49.6</u>	<u>183</u>	49.8	183	49.6	183	<u>49.6</u>	<u>183</u>
435.gromacs	172	41.6	<u>170</u>	<u>42.0</u>	169	42.3	172	41.6	<u>170</u>	<u>42.0</u>	169	42.3
436.cactusADM	18.5	645	16.9	706	<u>17.3</u>	<u>690</u>	18.5	645	16.9	706	<u>17.3</u>	<u>690</u>
437.leslie3d	34.2	275	<u>30.0</u>	<u>313</u>	29.0	324	34.2	275	<u>30.0</u>	<u>313</u>	29.0	324
444.namd	367	21.9	367	21.9	<u>367</u>	<u>21.9</u>	356	<u>22.6</u>	355	22.6	356	22.6
447.dealII	<u>237</u>	<u>48.3</u>	236	48.4	240	47.7	<u>237</u>	<u>48.3</u>	236	48.4	240	47.7
450.soplex	<u>218</u>	<u>38.3</u>	217	38.5	220	37.9	<u>218</u>	<u>38.3</u>	217	38.5	220	37.9
453.povray	<u>109</u>	<u>48.6</u>	111	47.8	109	48.8	100	53.2	100	53.0	<u>100</u>	<u>53.1</u>
454.calculix	200	41.2	<u>200</u>	<u>41.2</u>	209	39.6	182	45.4	<u>182</u>	<u>45.3</u>	187	44.2
459.GemsFDTD	50.0	212	<u>50.0</u>	<u>212</u>	50.0	212	45.9	231	<u>46.3</u>	<u>229</u>	46.5	228
465.tonto	<u>243</u>	<u>40.5</u>	242	40.6	246	40.1	223	44.1	<u>223</u>	<u>44.2</u>	223	44.2
470.lbm	22.2	618	<u>21.8</u>	<u>630</u>	21.6	635	22.2	618	<u>21.8</u>	<u>630</u>	21.6	635
481.wrf	105	107	105	106	<u>105</u>	<u>107</u>	105	107	105	106	<u>105</u>	<u>107</u>
482.sphinx3	<u>267</u>	<u>73.1</u>	266	73.2	267	73.0	<u>267</u>	<u>73.1</u>	266	73.2	267	73.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Enforce POR set to Enable

Early Snoop set to disabled

COD set to disable

Power Technology set to performance

Sysinfo program /home/cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on localhost Mon Sep 29 16:39:12 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2640 v3 @ 2.60GHz
        2 "physical id"s (chips)
        32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 8
    siblings   : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 KB
```

```
From /proc/meminfo
    MemTotal:      264479556 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 29 10:06
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext4  1.8T  411G  1.3T  24%  /
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. 067 08/13/2014
```

```
Memory:
16x 16 GB
8x NO DIMM NO DIMM
16x Samsung M393A2G40DB0-CPB 16 GB 1866 MHz 2 rank
```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Submitted_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Wed Nov 5 04:16:18 EST 2014

Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Sun Nov 23 21:26:15 EST 2014

Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Wed Nov 26 05:11:52 EST 2014

Submission: cpu2006-20141105-32761.sub

Submitted_by: Tian Yuwan <tianyw@sugon.com>

Submitted: Thu Nov 27 00:57:34 EST 2014

Submission: cpu2006-20141105-32761.sub

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -inline-calloc -opt-malloc-options=3 -auto -unroll14
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revA.20141203.xml>

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For other inquiries, please contact webmaster@spec.org.

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