



# SPEC® CINT2006 Result

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## Lenovo Group Limited

SPECint®\_rate2006 = 564

IBM System x iDataPlex dx360 M4  
(Intel Xeon E5-2650L v2, 1.70 GHz)

SPECint\_rate\_base2006 = 543

CPU2006 license: 9017

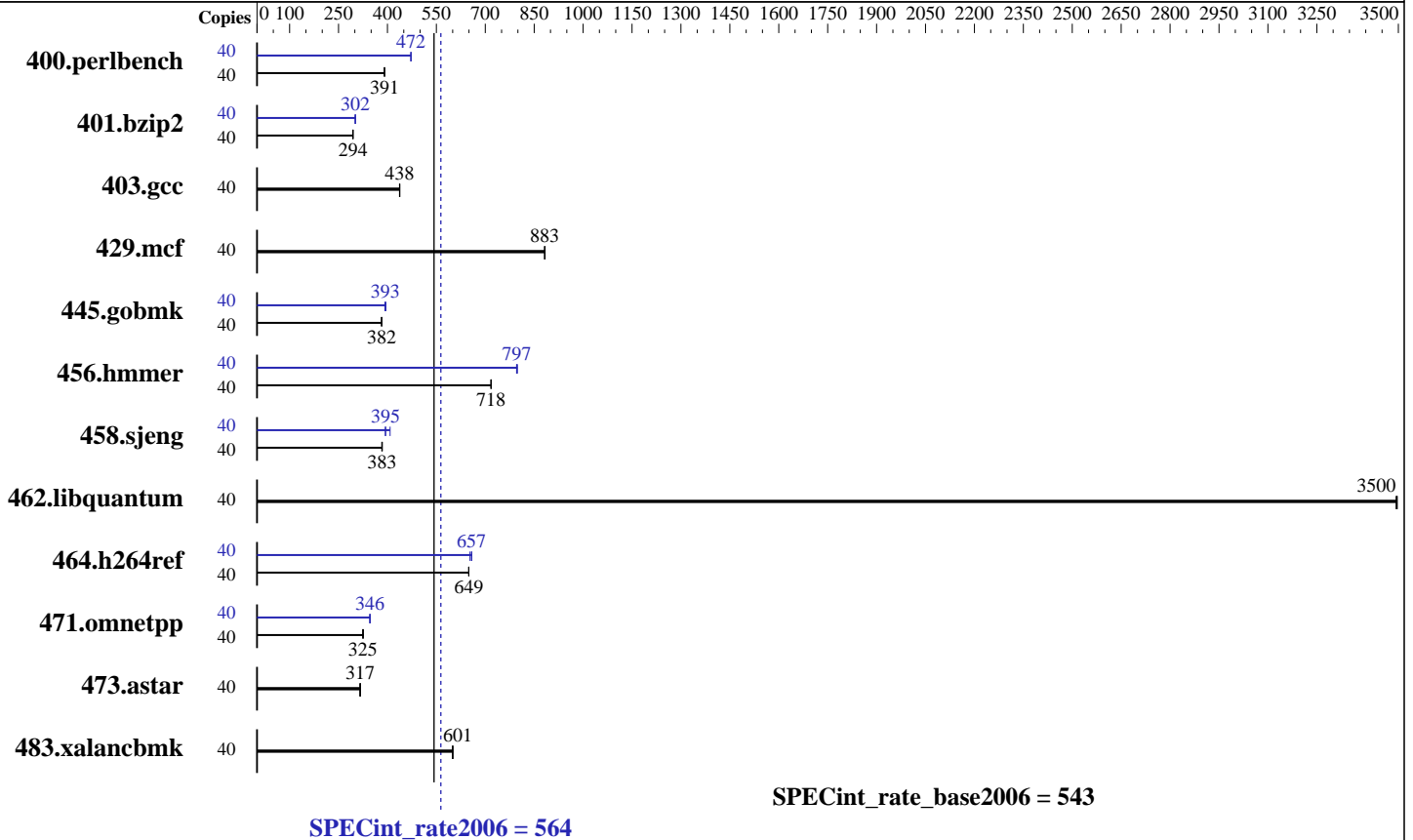
Test date: Oct-2014

Test sponsor: Lenovo Group Limited

Hardware Availability: Dec-2013

Tested by: IBM Corporation

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2650L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	1001	390	998	392	<b>1000</b>	<b>391</b>	40	828	472	828	472	<b>828</b>	<b>472</b>
401.bzip2	40	<b>1311</b>	<b>294</b>	1315	294	1310	295	40	<b>1279</b>	<b>302</b>	1278	302	1284	301
403.gcc	40	735	438	<b>736</b>	<b>438</b>	737	437	40	735	438	<b>736</b>	<b>438</b>	737	437
429.mcf	40	413	883	<b>413</b>	<b>883</b>	414	881	40	413	883	<b>413</b>	<b>883</b>	414	881
445.gobmk	40	<b>1097</b>	<b>382</b>	1097	382	1099	382	40	1062	395	<b>1066</b>	<b>393</b>	1070	392
456.hammer	40	520	718	<b>520</b>	<b>718</b>	520	718	40	467	799	469	796	<b>468</b>	<b>797</b>
458.sjeng	40	<b>1264</b>	<b>383</b>	1259	384	1264	383	40	1231	393	<b>1226</b>	<b>395</b>	1186	408
462.libquantum	40	237	3500	<b>237</b>	<b>3500</b>	237	3490	40	237	3500	<b>237</b>	<b>3500</b>	237	3490
464.h264ref	40	1364	649	1363	650	<b>1363</b>	<b>649</b>	40	1344	659	1355	653	<b>1346</b>	<b>657</b>
471.omnetpp	40	<b>770</b>	<b>325</b>	767	326	771	324	40	<b>722</b>	<b>346</b>	720	347	723	346
473.astar	40	<b>887</b>	<b>317</b>	891	315	884	318	40	<b>887</b>	<b>317</b>	891	315	884	318
483.xalancbmk	40	459	601	461	599	<b>460</b>	<b>601</b>	40	459	601	461	599	<b>460</b>	<b>601</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode  
Intel Idle Driver disabled with the following Linux kernel parameter in /etc/grub.conf:  
intel\_idle.max\_cstate=0

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-20140116-ic14.0/config/sysinfo.rev6874  
\$Rev: 6874 \$ \$Date:: 2013-11-20 #\$ 654bd3fcf53b06faef0efe54ed011998  
running on dx360M4 Fri Oct 24 16:59:22 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2650L v2 @ 1.70GHz  
2 "physical id"s (chips)

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### Platform Notes (Continued)

40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      264640496 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux dx360M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 24 16:26

SPEC is set to: /home/SPECcpu-20140116-ic14.0

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_td2-lv_home
                ext4      380G  174G  187G  49% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TDE139OUS-1.50]- 02/21/2014

Memory:

16x Samsung M393B2G70QH0-CMA 16 GB 2 rank 1866 MHz, configured at 1600 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPECcpu-20140116-ic14.0/libs/32:/home/SPECcpu-20140116-ic14.0/libs/64:/home/SPECcpu-20140116-ic14.0/sh"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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Software Availability: Sep-2013

## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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