



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 79.9

Huawei CH121 V3 (Intel Xeon E5-2620 v3)

SPECfp_base2006 = 75.1

CPU2006 license: 3175

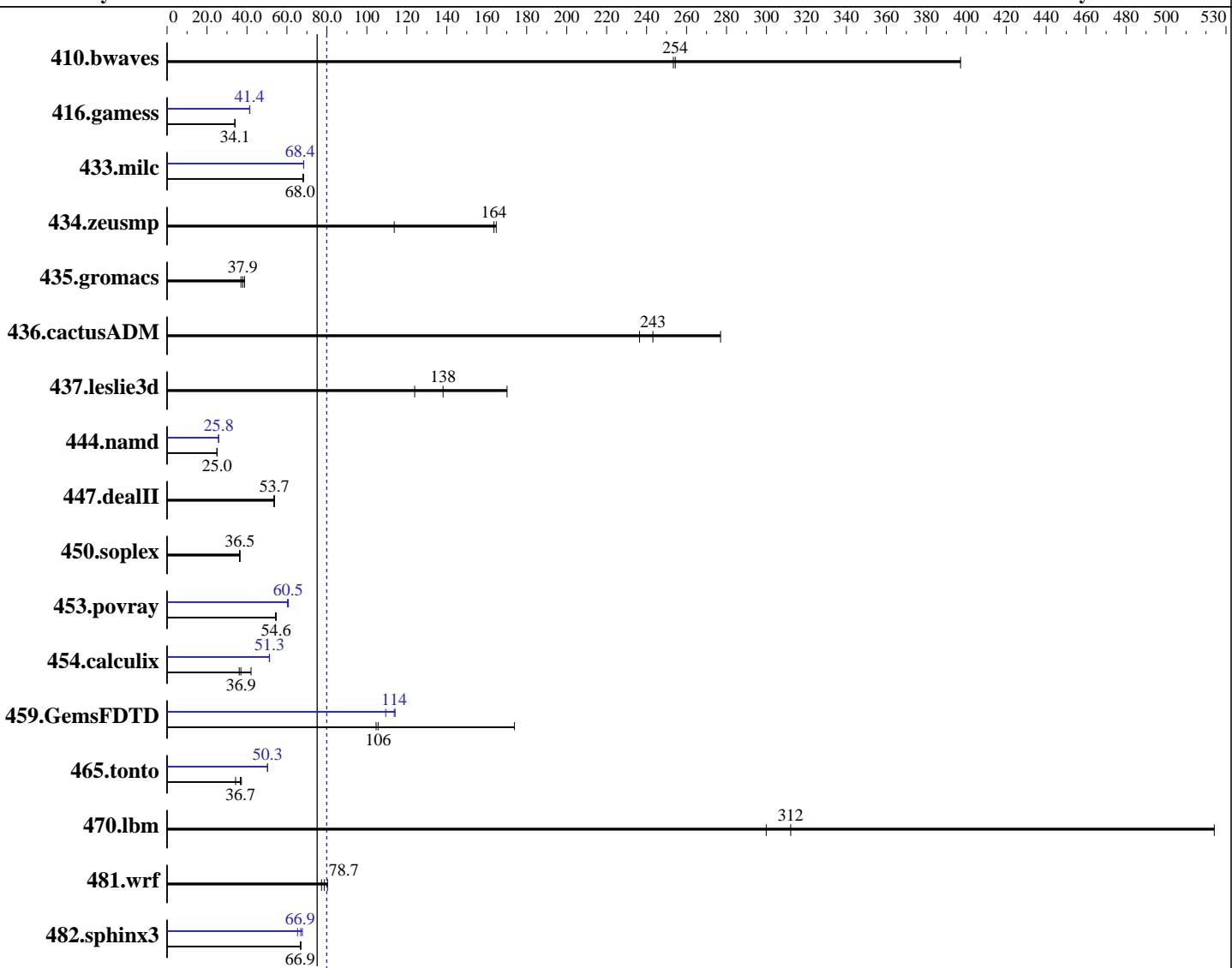
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2620 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
File System: Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R , running at 1866 MHz)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio |
| 410.bwaves | 53.6 | 253 | 53.4 | 254 | 34.2 | 397 | 53.6 | 253 | 53.4 | 254 | 34.2 | 397 |
| 416.gamess | 575 | 34.1 | 575 | 34.1 | 579 | 33.8 | 473 | 41.4 | 473 | 41.4 | 474 | 41.3 |
| 433.milc | 135 | 68.0 | 134 | 68.3 | 135 | 68.0 | 134 | 68.4 | 134 | 68.4 | 134 | 68.3 |
| 434.zeusmp | 80.0 | 114 | 55.2 | 165 | 55.6 | 164 | 80.0 | 114 | 55.2 | 165 | 55.6 | 164 |
| 435.gromacs | 193 | 37.0 | 184 | 38.8 | 188 | 37.9 | 193 | 37.0 | 184 | 38.8 | 188 | 37.9 |
| 436.cactusADM | 49.1 | 243 | 50.5 | 236 | 43.1 | 277 | 49.1 | 243 | 50.5 | 236 | 43.1 | 277 |
| 437.leslie3d | 68.0 | 138 | 55.2 | 170 | 75.8 | 124 | 68.0 | 138 | 55.2 | 170 | 75.8 | 124 |
| 444.namd | 321 | 25.0 | 321 | 25.0 | 321 | 25.0 | 311 | 25.8 | 311 | 25.8 | 310 | 25.8 |
| 447.dealII | 214 | 53.5 | 213 | 53.7 | 213 | 53.7 | 214 | 53.5 | 213 | 53.7 | 213 | 53.7 |
| 450.soplex | 230 | 36.2 | 229 | 36.5 | 229 | 36.5 | 230 | 36.2 | 229 | 36.5 | 229 | 36.5 |
| 453.povray | 97.4 | 54.6 | 98.0 | 54.3 | 97.4 | 54.6 | 87.6 | 60.7 | 88.3 | 60.2 | 87.9 | 60.5 |
| 454.calculix | 223 | 36.9 | 229 | 36.1 | 196 | 42.0 | 161 | 51.3 | 161 | 51.3 | 161 | 51.2 |
| 459.GemsFDTD | 61.0 | 174 | 101 | 105 | 100 | 106 | 97.0 | 109 | 92.9 | 114 | 93.3 | 114 |
| 465.tonto | 268 | 36.7 | 287 | 34.3 | 265 | 37.2 | 195 | 50.4 | 196 | 50.3 | 196 | 50.3 |
| 470.lbm | 26.2 | 524 | 44.0 | 312 | 45.8 | 300 | 26.2 | 524 | 44.0 | 312 | 45.8 | 300 |
| 481.wrf | 145 | 77.3 | 142 | 78.7 | 139 | 80.3 | 145 | 77.3 | 142 | 78.7 | 139 | 80.3 |
| 482.sphinx3 | 292 | 66.7 | 291 | 67.0 | 291 | 66.9 | 288 | 67.7 | 291 | 66.9 | 298 | 65.3 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to HS

Set Hyper-Threading to Disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on huawei Thu Sep 4 21:17:05 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2620 v3 @ 2.40GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264277800 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux huawei 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 4 09:19
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal      ext4  438G  11G  406G   3% /
```

```
Additional information from dmidecode:
```

```
BIOS Insyde Corp. 1.16 09/02/2014
Memory:
 8x NO DIMM NO DIMM      3 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 1 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3

are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -inline-calloc -opt-malloc-options=3 -auto -unroll14
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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