



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **110**

Huawei RH2288E V2 (Intel Xeon E5-2667 v2)

SPECfp\_base2006 = **105**

CPU2006 license: 3175

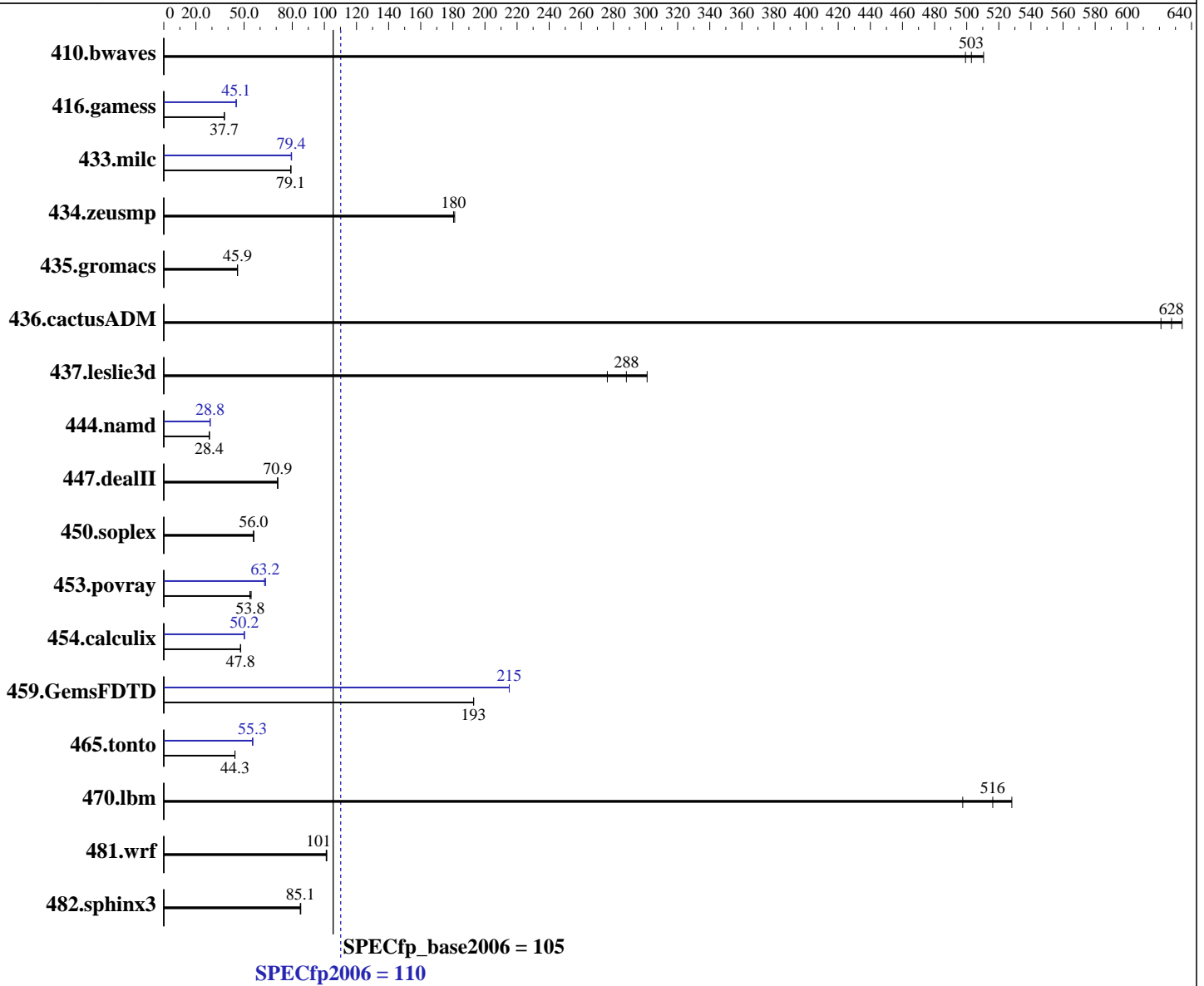
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2667 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SATA, 7200RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	27.2	499	26.6	511	<b><u>27.0</u></b>	<b><u>503</u></b>	27.2	499	26.6	511	<b><u>27.0</u></b>	<b><u>503</u></b>
416.gamess	<b><u>519</u></b>	<b><u>37.7</u></b>	519	37.7	520	37.6	434	45.1	435	45.1	<b><u>434</u></b>	<b><u>45.1</u></b>
433.milc	116	79.0	116	79.2	<b><u>116</u></b>	<b><u>79.1</u></b>	115	79.6	116	79.4	<b><u>116</u></b>	<b><u>79.4</u></b>
434.zeusmp	50.2	181	50.4	180	<b><u>50.4</u></b>	<b><u>180</u></b>	50.2	181	50.4	180	<b><u>50.4</u></b>	<b><u>180</u></b>
435.gromacs	156	45.9	156	45.9	<b><u>156</u></b>	<b><u>45.9</u></b>	156	45.9	156	45.9	<b><u>156</u></b>	<b><u>45.9</u></b>
436.cactusADM	<b><u>19.0</u></b>	<b><u>628</u></b>	18.8	634	19.2	621	<b><u>19.0</u></b>	<b><u>628</u></b>	18.8	634	19.2	621
437.leslie3d	<b><u>32.6</u></b>	<b><u>288</u></b>	31.2	301	34.0	276	<b><u>32.6</u></b>	<b><u>288</u></b>	31.2	301	34.0	276
444.namd	283	28.4	<b><u>283</u></b>	<b><u>28.4</u></b>	283	28.4	278	28.8	278	28.9	<b><u>278</u></b>	<b><u>28.8</u></b>
447.dealII	161	71.0	161	70.9	<b><u>161</u></b>	<b><u>70.9</u></b>	161	71.0	161	70.9	<b><u>161</u></b>	<b><u>70.9</u></b>
450.soplex	149	56.0	<b><u>149</u></b>	<b><u>56.0</u></b>	149	55.8	149	56.0	<b><u>149</u></b>	<b><u>56.0</u></b>	149	55.8
453.povray	97.8	54.4	<b><u>98.8</u></b>	<b><u>53.8</u></b>	99.1	53.7	84.1	63.3	<b><u>84.1</u></b>	<b><u>63.2</u></b>	84.8	62.7
454.calculix	172	47.9	173	47.7	<b><u>173</u></b>	<b><u>47.8</u></b>	165	50.1	<b><u>164</u></b>	<b><u>50.2</u></b>	164	50.2
459.GemsFDTD	55.0	193	<b><u>55.0</u></b>	<b><u>193</u></b>	55.0	193	<b><u>49.3</u></b>	<b><u>215</u></b>	49.3	215	49.3	215
465.tonto	222	44.3	222	44.3	<b><u>222</u></b>	<b><u>44.3</u></b>	178	55.3	178	55.4	<b><u>178</u></b>	<b><u>55.3</u></b>
470.lbm	27.6	498	<b><u>26.6</u></b>	<b><u>516</u></b>	26.0	528	27.6	498	<b><u>26.6</u></b>	<b><u>516</u></b>	26.0	528
481.wrf	110	101	<b><u>110</u></b>	<b><u>101</u></b>	110	102	110	101	<b><u>110</u></b>	<b><u>101</u></b>	110	102
482.sphinx3	229	85.0	<b><u>229</u></b>	<b><u>85.1</u></b>	228	85.3	229	85.0	<b><u>229</u></b>	<b><u>85.1</u></b>	228	85.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Hyper-Threading to Disabled  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on localhost.localdomain Mon Sep 8 00:14:20 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013

## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 v2 @ 3.30GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 1 2 3 4 8 9 10 11
  physical 1: cores 1 2 3 4 8 9 10 11
cache size : 25600 KB

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 5 19:55

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  265G  16G  236G   7% /

Additional information from dmidecode:
Memory:
 10x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
 6x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "16"

```

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## General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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