



# SPEC® CFP2006 Result

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## NEC Corporation

SPECfp®\_rate2006 = 140

Express5800/T110g-S (Intel Xeon E3-1220 v3)

SPECfp\_rate\_base2006 = 137

CPU2006 license: 9006

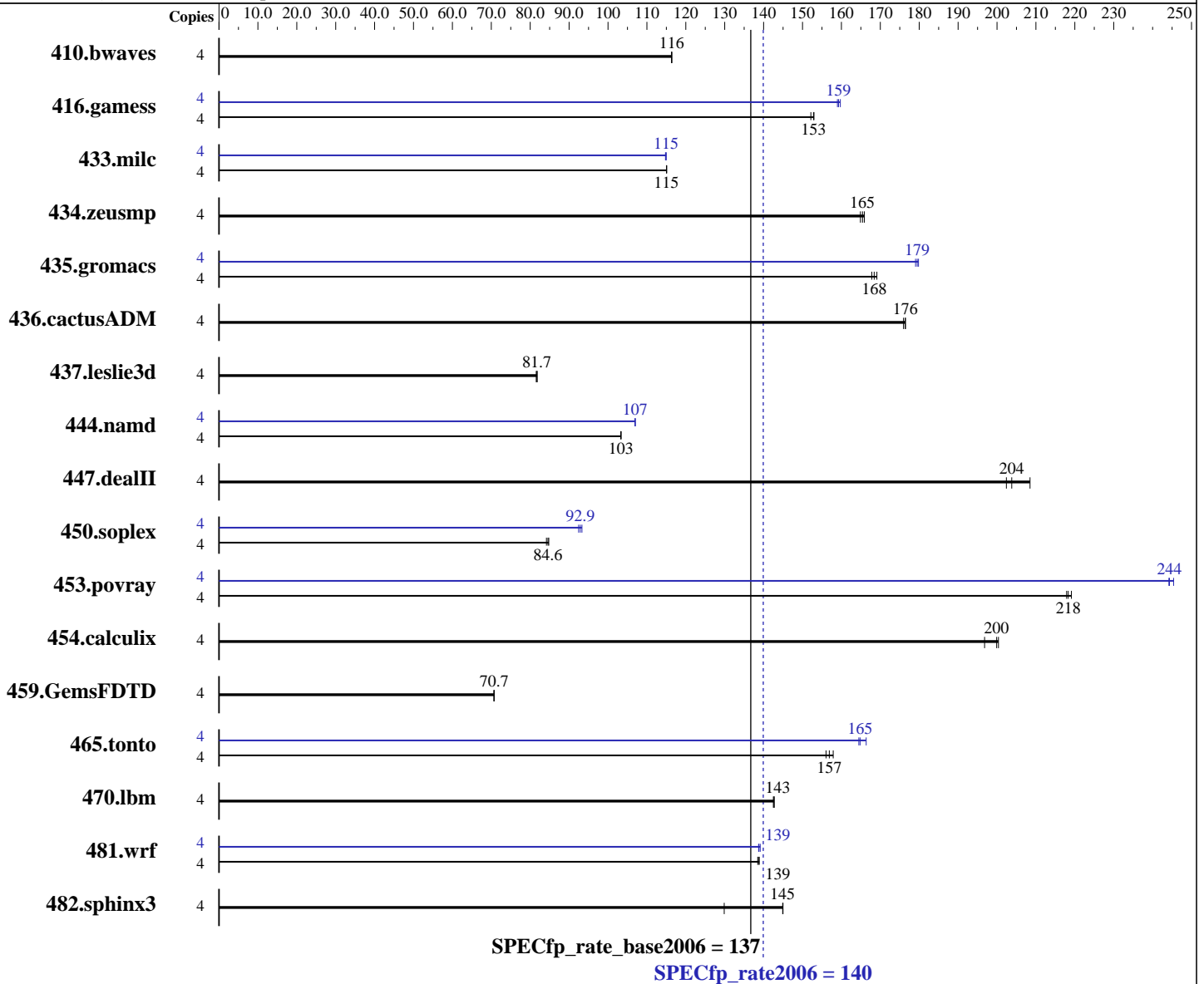
Test date: May-2014

Test sponsor: NEC Corporation

Hardware Availability: Jul-2014

Tested by: NEC Corporation

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E3-1220 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 3100  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Kernel 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.2.144 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	467	116	467	116	<b>467</b>	<b>116</b>	4	467	116	467	116	<b>467</b>	<b>116</b>
416.gamess	4	<b>512</b>	<b>153</b>	515	152	512	153	4	490	160	493	159	<b>492</b>	<b>159</b>
433.milc	4	319	115	<b>319</b>	<b>115</b>	319	115	4	319	115	<b>320</b>	<b>115</b>	320	115
434.zeusmp	4	221	165	<b>220</b>	<b>165</b>	219	166	4	221	165	<b>220</b>	<b>165</b>	219	166
435.gromacs	4	169	169	170	168	<b>170</b>	<b>168</b>	4	159	179	159	180	<b>159</b>	<b>179</b>
436.cactusADM	4	271	176	<b>271</b>	<b>176</b>	272	176	4	271	176	<b>271</b>	<b>176</b>	272	176
437.leslie3d	4	460	81.8	461	81.5	<b>460</b>	<b>81.7</b>	4	460	81.8	461	81.5	<b>460</b>	<b>81.7</b>
444.namd	4	310	103	311	103	<b>310</b>	<b>103</b>	4	300	107	300	107	<b>300</b>	<b>107</b>
447.dealII	4	219	208	226	202	<b>225</b>	<b>204</b>	4	219	208	226	202	<b>225</b>	<b>204</b>
450.soplex	4	<b>394</b>	<b>84.6</b>	394	84.8	396	84.2	4	<b>359</b>	<b>92.9</b>	358	93.3	361	92.4
453.povray	4	97.1	219	<b>97.5</b>	<b>218</b>	97.7	218	4	87.1	244	<b>87.1</b>	<b>244</b>	86.7	245
454.calculix	4	<b>165</b>	<b>200</b>	168	197	165	200	4	<b>165</b>	<b>200</b>	168	197	165	200
459.GemsFDTD	4	<b>600</b>	<b>70.7</b>	600	70.7	600	70.7	4	<b>600</b>	<b>70.7</b>	600	70.7	600	70.7
465.tonto	4	249	158	252	156	<b>251</b>	<b>157</b>	4	237	166	<b>239</b>	<b>165</b>	239	164
470.lbm	4	<b>385</b>	<b>143</b>	386	143	385	143	4	<b>385</b>	<b>143</b>	386	143	385	143
481.wrf	4	<b>322</b>	<b>139</b>	322	139	323	138	4	321	139	<b>322</b>	<b>139</b>	322	139
482.sphinx3	4	<b>538</b>	<b>145</b>	600	130	538	145	4	<b>538</b>	<b>145</b>	600	130	538	145

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2)  
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4  
 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

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## Peak Optimization Flags (Continued)

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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