



SPEC® CFP2006 Result

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IBM Corporation

IBM System x3850 X6
(Intel Xeon E7-4850 v2, 2.30 GHz)

SPECfp[®]_rate2006 = 1240

SPECfp_rate_base2006 = 1220

CPU2006 license: 11

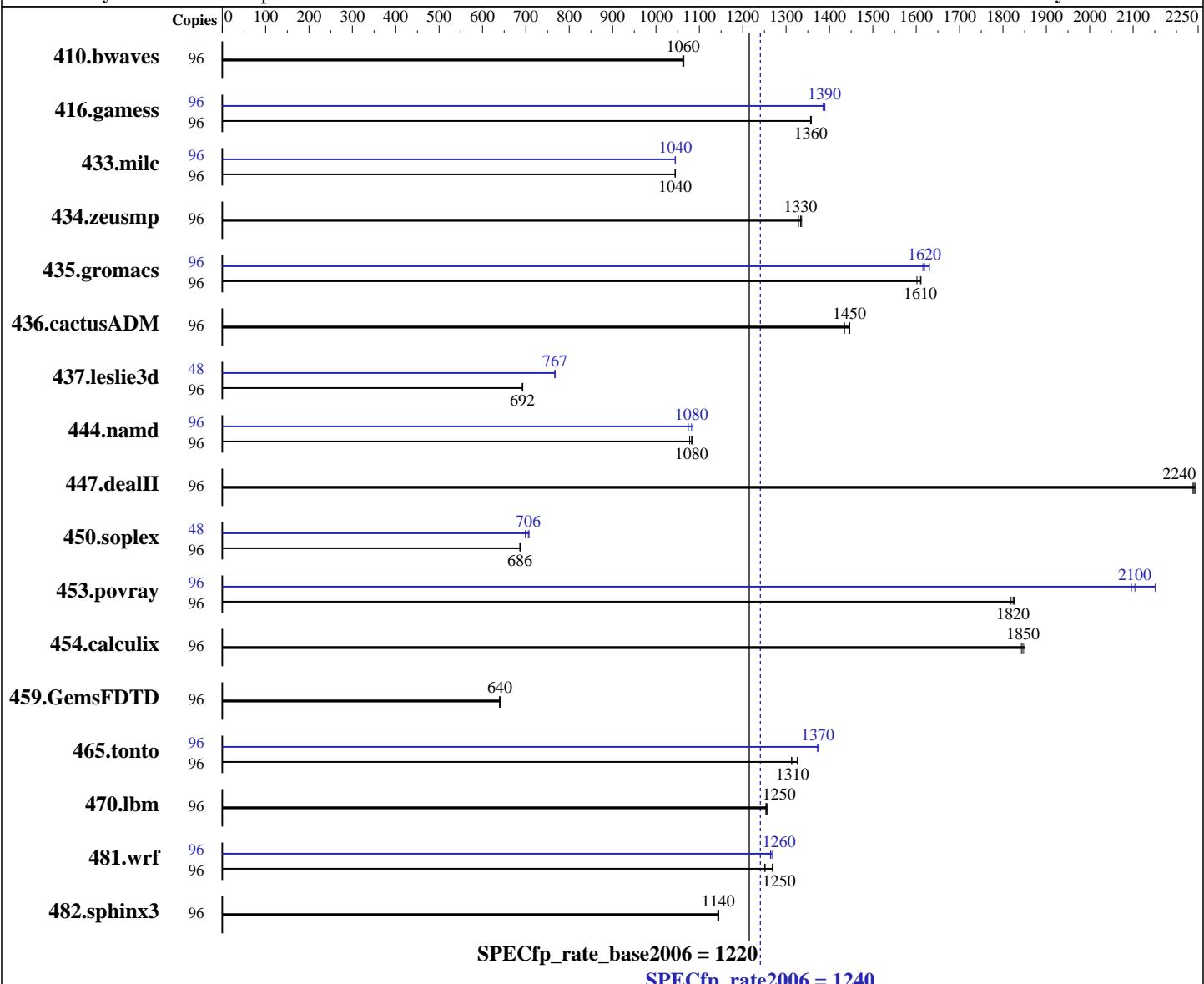
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E7-4850 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC,
running at 1067 MHz)
Disk Subsystem: 1 x 400 GB SATA, SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1226	1060	<u>1227</u>	<u>1060</u>	1229	1060	96	1226	1060	<u>1227</u>	<u>1060</u>	1229	1060	1229	1060
416.gamess	96	1386	1360	1384	1360	<u>1385</u>	<u>1360</u>	96	1353	1390	1357	1380	<u>1354</u>	<u>1390</u>	1354	1390
433.milc	96	844	1040	844	1040	<u>844</u>	<u>1040</u>	96	843	1040	844	1040	<u>844</u>	<u>1040</u>	844	1040
434.zeusmp	96	<u>655</u>	<u>1330</u>	654	1340	658	1330	96	<u>655</u>	<u>1330</u>	654	1340	658	1330	658	1330
435.gromacs	96	425	1610	<u>426</u>	<u>1610</u>	428	1600	96	420	1630	424	1620	<u>423</u>	<u>1620</u>	423	1620
436.cactusADM	96	793	1450	<u>793</u>	<u>1450</u>	800	1430	96	793	1450	<u>793</u>	<u>1450</u>	800	1430	800	1430
437.leslie3d	96	1305	692	<u>1303</u>	<u>692</u>	1302	693	48	588	767	589	767	<u>588</u>	<u>767</u>	588	767
444.namd	96	714	1080	711	1080	<u>712</u>	<u>1080</u>	96	<u>712</u>	<u>1080</u>	717	1070	710	1090	710	1090
447.dealII	96	<u>490</u>	<u>2240</u>	490	2240	491	2240	96	<u>490</u>	<u>2240</u>	490	2240	491	2240	491	2240
450.soplex	96	<u>1166</u>	<u>686</u>	1165	687	1167	686	48	573	699	<u>567</u>	<u>706</u>	566	707	566	707
453.povray	96	<u>280</u>	<u>1820</u>	280	1830	281	1820	96	237	2150	<u>243</u>	<u>2100</u>	244	2100	244	2100
454.calculix	96	428	1850	<u>429</u>	<u>1850</u>	430	1840	96	428	1850	<u>429</u>	<u>1850</u>	430	1840	430	1840
459.GemsFDTD	96	1593	639	<u>1591</u>	<u>640</u>	1590	640	96	1593	639	<u>1591</u>	<u>640</u>	1590	640	1590	640
465.tonto	96	713	1330	<u>718</u>	<u>1310</u>	720	1310	96	<u>688</u>	<u>1370</u>	688	1370	687	1380	687	1380
470.lbm	96	<u>1051</u>	<u>1250</u>	1052	1250	1050	1260	96	<u>1051</u>	<u>1250</u>	1052	1250	1050	1260	1050	1260
481.wrf	96	<u>857</u>	<u>1250</u>	857	1250	846	1270	96	848	1260	846	1270	<u>848</u>	<u>1260</u>	848	1260
482.sphinx3	96	1634	1140	1638	1140	<u>1636</u>	<u>1140</u>	96	1634	1140	1638	1140	<u>1636</u>	<u>1140</u>	1636	1140

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

Operating Mode set to Maximum Perfomance in BIOS
Sysinfo program /cpu2006.1.2_14.0_aug2013/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191
running on Larry-Andromeda Thu Jul 10 02:06:03 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4850 v2 @ 2.30GHz
        4 "physical id"s (chips)
        96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 12
    siblings   : 24
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 24576 KB
```

```
From /proc/meminfo
MemTotal:      1058470592 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux Larry-Andromeda 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 9 11:26
```

```
SPEC is set to: /cpu2006.1.2_14.0_aug2013
Filesystem              Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_larryandromed-lv_root ext4  357G  233G  106G  69% /
```

Additional information from dmidecode:

BIOS IBM -[A8E107JUS-1.00]- 05/02/2014

Memory:

32x NO DIMM Unknown

64x Samsung M393B2G70QH0-YK0 16 GB 1067 MHz 2 rank

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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006.1.2_14.0_aug2013/libs/32:/cpu2006.1.2_14.0_aug2013/libs/64:/cpu2006.1.2_14.0_aug2013/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

```

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
           -inline-level=0 -scalar-rep-

```

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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