



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint®\_rate2006 = 482

### Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175

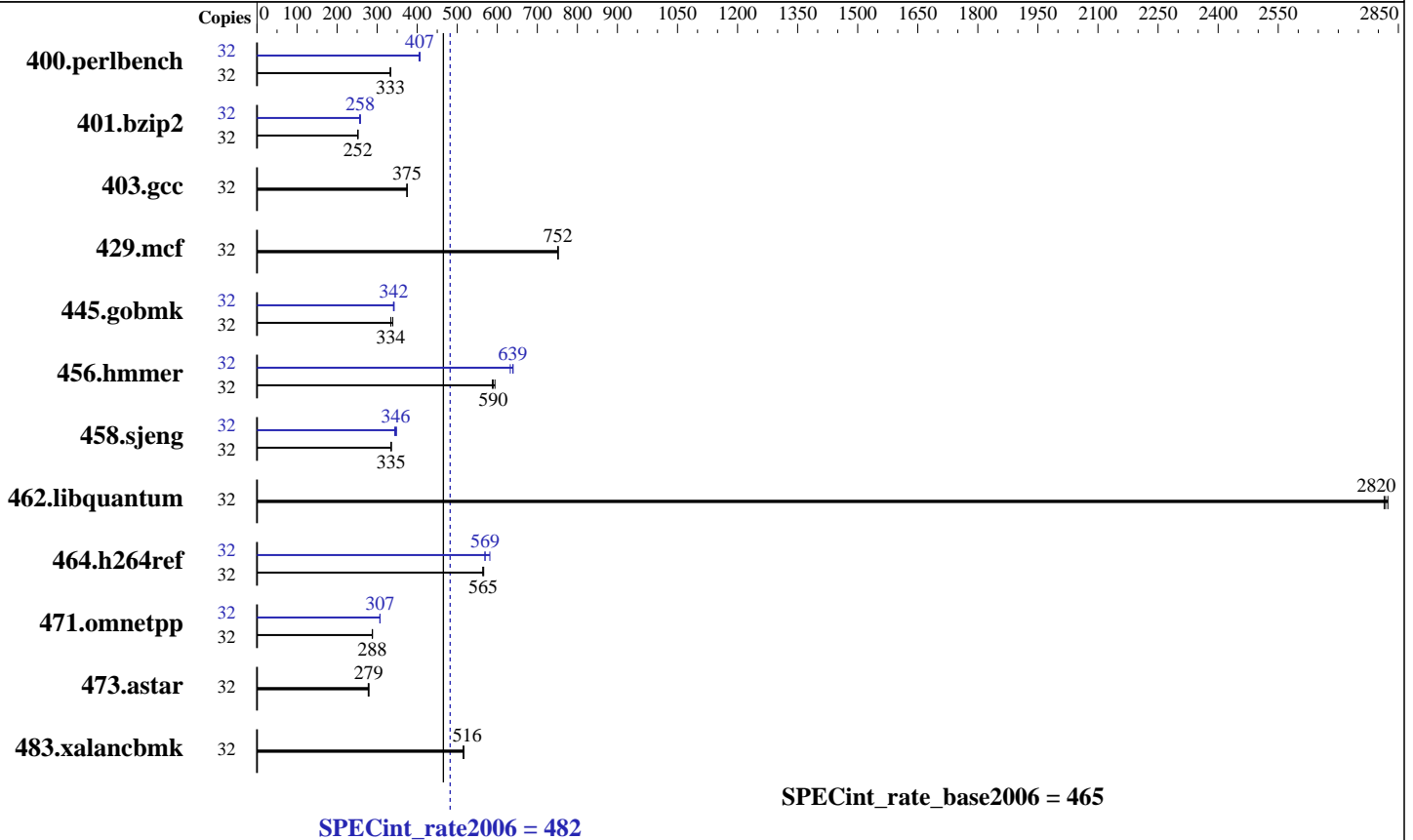
Test date: Jul-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2658  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Huawei

SPECint\_rate2006 = 482

## Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Jul-2014  
Hardware Availability: Sep-2013  
Software Availability: Nov-2013

### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	940	333	937	334	<b>938</b>	<b>333</b>	32	768	407	<b>769</b>	<b>407</b>	773	405
401.bzip2	32	1225	252	<b>1225</b>	<b>252</b>	1228	251	32	1197	258	1205	256	<b>1198</b>	<b>258</b>
403.gcc	32	<b>687</b>	<b>375</b>	687	375	688	374	32	<b>687</b>	<b>375</b>	687	375	688	374
429.mcf	32	389	751	388	752	<b>388</b>	<b>752</b>	32	389	751	388	752	<b>388</b>	<b>752</b>
445.gobmk	32	990	339	1004	334	<b>1004</b>	<b>334</b>	32	<b>983</b>	<b>342</b>	981	342	985	341
456.hammer	32	503	594	508	588	<b>506</b>	<b>590</b>	32	472	632	467	639	<b>467</b>	<b>639</b>
458.sjeng	32	1154	335	<b>1157</b>	<b>335</b>	1157	335	32	1126	344	1112	348	<b>1118</b>	<b>346</b>
462.libquantum	32	<b>235</b>	<b>2820</b>	235	2820	236	2810	32	<b>235</b>	<b>2820</b>	235	2820	236	2810
464.h264ref	32	1251	566	<b>1254</b>	<b>565</b>	1256	564	32	1244	569	<b>1244</b>	<b>569</b>	1218	581
471.omnetpp	32	694	288	695	288	<b>694</b>	<b>288</b>	32	652	307	651	307	<b>651</b>	<b>307</b>
473.astar	32	805	279	<b>805</b>	<b>279</b>	809	278	32	805	279	<b>805</b>	<b>279</b>	809	278
483.xalancbmk	32	<b>428</b>	<b>516</b>	428	516	429	515	32	<b>428</b>	<b>516</b>	428	516	429	515

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on wbspeccpu Fri Jul 11 17:25:14 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2658 0 @ 2.10GHz  
2 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 482

Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Jul-2014  
Hardware Availability: Sep-2013  
Software Availability: Nov-2013

## Platform Notes (Continued)

```
caution.)
cpu cores : 8
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      132103760 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux wbspeccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 9 17:06
```

```
SPEC is set to: /spec
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdal        ext4      270G      66G  191G  26% /
```

```
Additional information from dmidecode:
BIOS Insyde Corp. RMIBV372 12/21/2013
Memory:
16x Micron 18JSF1G72PDZ-1G6E 8 GB 1600 MHz 2 rank
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 482

Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Jul-2014  
Hardware Availability: Sep-2013  
Software Availability: Nov-2013

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64  
  
456.hmmer: icc -m64  
  
458.sjeng: icc -m64  
  
C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 482

Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 482

Huawei BH622 v2 (Intel Xeon E5-2658)

SPECint\_rate\_base2006 = 465

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013

## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Mon Aug 4 15:10:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 4 August 2014.