



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 105

Huawei RH2288H v2 (Intel Xeon E5-2643 v2)

SPECfp_base2006 = 101

CPU2006 license: 3175

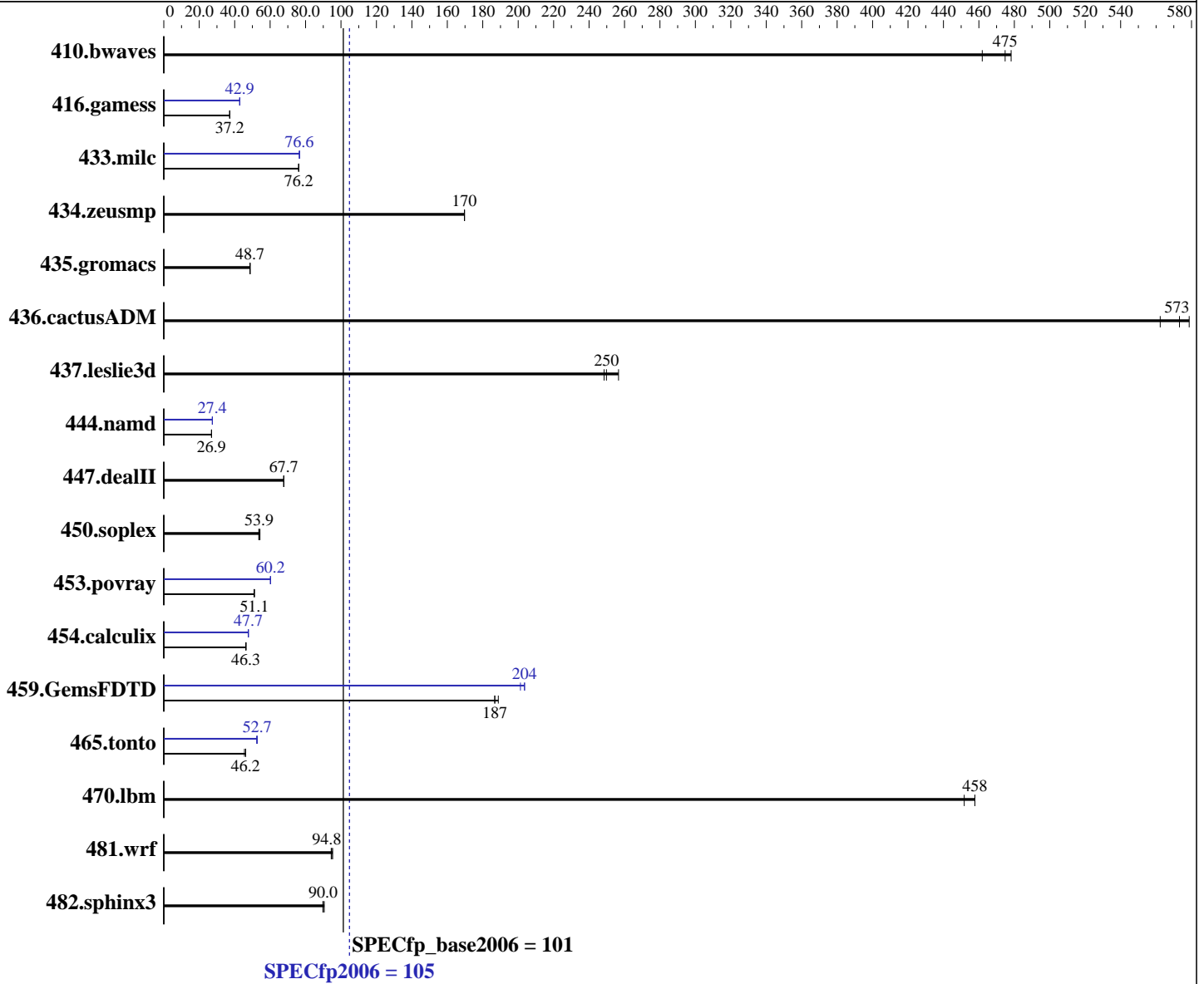
Test date: Jul-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2643 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13 ,ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	29.4	462	28.4	478	<u>28.6</u>	<u>475</u>	29.4	462	28.4	478	<u>28.6</u>	<u>475</u>
416.gamess	526	37.2	<u>526</u>	<u>37.2</u>	526	37.2	<u>457</u>	<u>42.9</u>	457	42.9	457	42.8
433.milc	121	76.2	<u>121</u>	<u>76.2</u>	121	76.1	<u>120</u>	<u>76.6</u>	120	76.5	120	76.6
434.zeusmp	53.6	170	53.6	170	<u>53.6</u>	<u>170</u>	53.6	170	53.6	170	<u>53.6</u>	<u>170</u>
435.gromacs	147	48.7	147	48.7	<u>147</u>	<u>48.7</u>	147	48.7	147	48.7	<u>147</u>	<u>48.7</u>
436.cactusADM	<u>20.8</u>	<u>573</u>	20.7	579	21.3	562	<u>20.8</u>	<u>573</u>	20.7	579	21.3	562
437.leslie3d	<u>37.6</u>	<u>250</u>	36.6	257	37.8	248	<u>37.6</u>	<u>250</u>	36.6	257	37.8	248
444.namd	298	26.9	298	26.9	<u>298</u>	<u>26.9</u>	293	27.4	<u>293</u>	<u>27.4</u>	293	27.4
447.dealII	<u>169</u>	<u>67.7</u>	169	67.7	169	67.7	<u>169</u>	<u>67.7</u>	169	67.7	169	67.7
450.soplex	154	54.3	156	53.6	<u>155</u>	<u>53.9</u>	154	54.3	156	53.6	<u>155</u>	<u>53.9</u>
453.povray	104	50.9	104	51.1	<u>104</u>	<u>51.1</u>	<u>88.3</u>	<u>60.2</u>	88.4	60.1	88.3	60.3
454.calculix	178	46.3	178	46.3	<u>178</u>	<u>46.3</u>	173	47.6	173	47.8	<u>173</u>	<u>47.7</u>
459.GemsFDTD	<u>56.8</u>	<u>187</u>	56.2	189	56.8	187	<u>52.1</u>	<u>204</u>	52.7	201	52.1	204
465.tonto	<u>213</u>	<u>46.2</u>	216	45.5	213	46.2	<u>187</u>	<u>52.7</u>	187	52.7	188	52.4
470.lbm	30.0	458	30.4	452	<u>30.0</u>	<u>458</u>	30.0	458	30.4	452	<u>30.0</u>	<u>458</u>
481.wrf	117	95.3	118	94.5	<u>118</u>	<u>94.8</u>	117	95.3	118	94.5	<u>118</u>	<u>94.8</u>
482.sphinx3	<u>217</u>	<u>90.0</u>	217	89.8	215	90.6	<u>217</u>	<u>90.0</u>	217	89.8	215	90.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Intel HT Technology Disable
 Sysinfo program /spec/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on localhost Wed Jul 9 20:09:03 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v2 @ 3.50GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 6
  physical 0: cores 2 3 4 8 9 10
  physical 1: cores 2 3 4 8 9 10
cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 9 15:09

```

SPEC is set to: /spec
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        ext4      272G      75G  184G  29% /

```

Additional information from dmidecode:

```

Memory:
 2x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
 8x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank
 6x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "12"

```

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General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

The Huawei RH2288H v2 and Huawei RH2288 v2 and the Huawei RH1288 v2 models are electronically equivalent.

The results have been measured on a Huawei RH2288H v2 model

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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