



# SPEC® CFP2006 Result

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## Huawei Huawei CH240

SPECfp®2006 = 48.4

SPECfp\_base2006 = 45.3

CPU2006 license: 3175

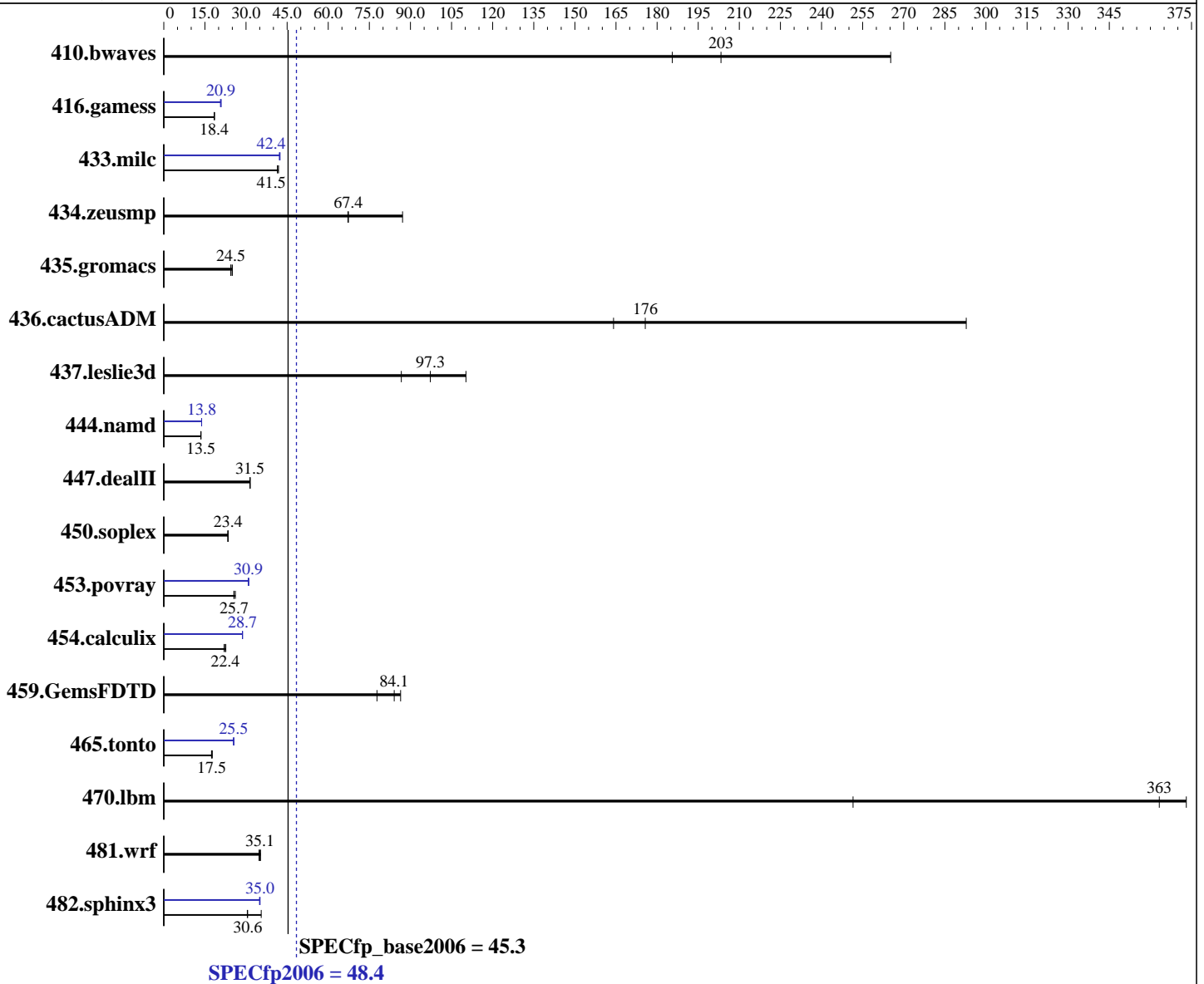
Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2014

Hardware Availability: May-2012

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-4603  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (32 x 8 GB 2Rx4 PC3L-10600R-09, ECC)  
Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	73.2	186	<b><u>66.8</u></b>	<b><u>203</u></b>	51.2	265	73.2	186	<b><u>66.8</u></b>	<b><u>203</u></b>	51.2	265
416.gamess	<b><u>1062</u></b>	<b><u>18.4</u></b>	1060	18.5	1063	18.4	938	20.9	<b><u>938</u></b>	<b><u>20.9</u></b>	947	20.7
433.milc	221	41.5	<b><u>221</u></b>	<b><u>41.5</u></b>	219	41.9	217	42.4	<b><u>217</u></b>	<b><u>42.4</u></b>	218	42.2
434.zeusmp	104	87.1	<b><u>135</u></b>	<b><u>67.4</u></b>	135	67.2	104	87.1	<b><u>135</u></b>	<b><u>67.4</u></b>	135	67.2
435.gromacs	<b><u>291</u></b>	<b><u>24.5</u></b>	292	24.5	285	25.0	<b><u>291</u></b>	<b><u>24.5</u></b>	292	24.5	285	25.0
436.cactusADM	40.8	293	72.8	164	<b><u>68.0</u></b>	<b><u>176</u></b>	40.8	293	72.8	164	<b><u>68.0</u></b>	<b><u>176</u></b>
437.leslie3d	108	86.7	<b><u>96.7</u></b>	<b><u>97.3</u></b>	85.3	110	108	86.7	<b><u>96.7</u></b>	<b><u>97.3</u></b>	85.3	110
444.namd	<b><u>592</u></b>	<b><u>13.5</u></b>	592	13.5	592	13.5	581	13.8	<b><u>581</u></b>	<b><u>13.8</u></b>	581	13.8
447.dealII	<b><u>363</u></b>	<b><u>31.5</u></b>	363	31.5	364	31.5	<b><u>363</u></b>	<b><u>31.5</u></b>	363	31.5	364	31.5
450.soplex	<b><u>356</u></b>	<b><u>23.4</u></b>	356	23.5	356	23.4	<b><u>356</u></b>	<b><u>23.4</u></b>	356	23.5	356	23.4
453.povray	208	25.6	204	26.1	<b><u>207</u></b>	<b><u>25.7</u></b>	173	30.8	<b><u>172</u></b>	<b><u>30.9</u></b>	171	31.0
454.calculix	<b><u>368</u></b>	<b><u>22.4</u></b>	365	22.6	375	22.0	<b><u>287</u></b>	<b><u>28.7</u></b>	287	28.7	288	28.7
459.GemsFDTD	123	86.4	136	77.8	<b><u>126</u></b>	<b><u>84.1</u></b>	123	86.4	136	77.8	<b><u>126</u></b>	<b><u>84.1</u></b>
465.tonto	556	17.7	565	17.4	<b><u>561</u></b>	<b><u>17.5</u></b>	<b><u>386</u></b>	<b><u>25.5</u></b>	387	25.4	385	25.6
470.lbm	<b><u>37.8</u></b>	<b><u>363</u></b>	54.6	251	36.8	373	<b><u>37.8</u></b>	<b><u>363</u></b>	54.6	251	36.8	373
481.wrf	<b><u>318</u></b>	<b><u>35.1</u></b>	321	34.7	316	35.3	<b><u>318</u></b>	<b><u>35.1</u></b>	321	34.7	316	35.3
482.sphinx3	<b><u>637</u></b>	<b><u>30.6</u></b>	640	30.5	548	35.5	558	34.9	555	35.1	<b><u>556</u></b>	<b><u>35.0</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on spec Thu Mar 6 15:13:13 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-4603 0 @ 2.00GHz  
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## Platform Notes (Continued)

```
4 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings  : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
physical 2: cores 0 1 2 3
physical 3: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      264479484 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux spec 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 6 05:29
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  193G   70G  114G  38% /
```

```
Additional information from dmidecode:
BIOS Insyde Corp. OARYV283 01/21/2014
Memory:
32x Micron 36KSZF1G72PZ-1G4D 8 GB 1066 MHz 2 rank
16x NO DIMM NO DIMM
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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## General Notes (Continued)

memory using RedHat EL 6.4  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 runspec command invoked through numactl i.e.:  
 numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

Fortran benchmarks:  
 ifort -m64

Benchmarks using both Fortran and C:  
 icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deall: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
 -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

Fortran benchmarks:  
 ifort -m64

Benchmarks using both Fortran and C:  
 icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
 -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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