



# SPEC® CFP2006 Result

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**HITACHI**

Compute Blade 2000 (Intel Xeon E5-2690 v2)

**SPECfp®2006 = 104**

**SPECfp\_base2006 = 99.2**

CPU2006 license: 35

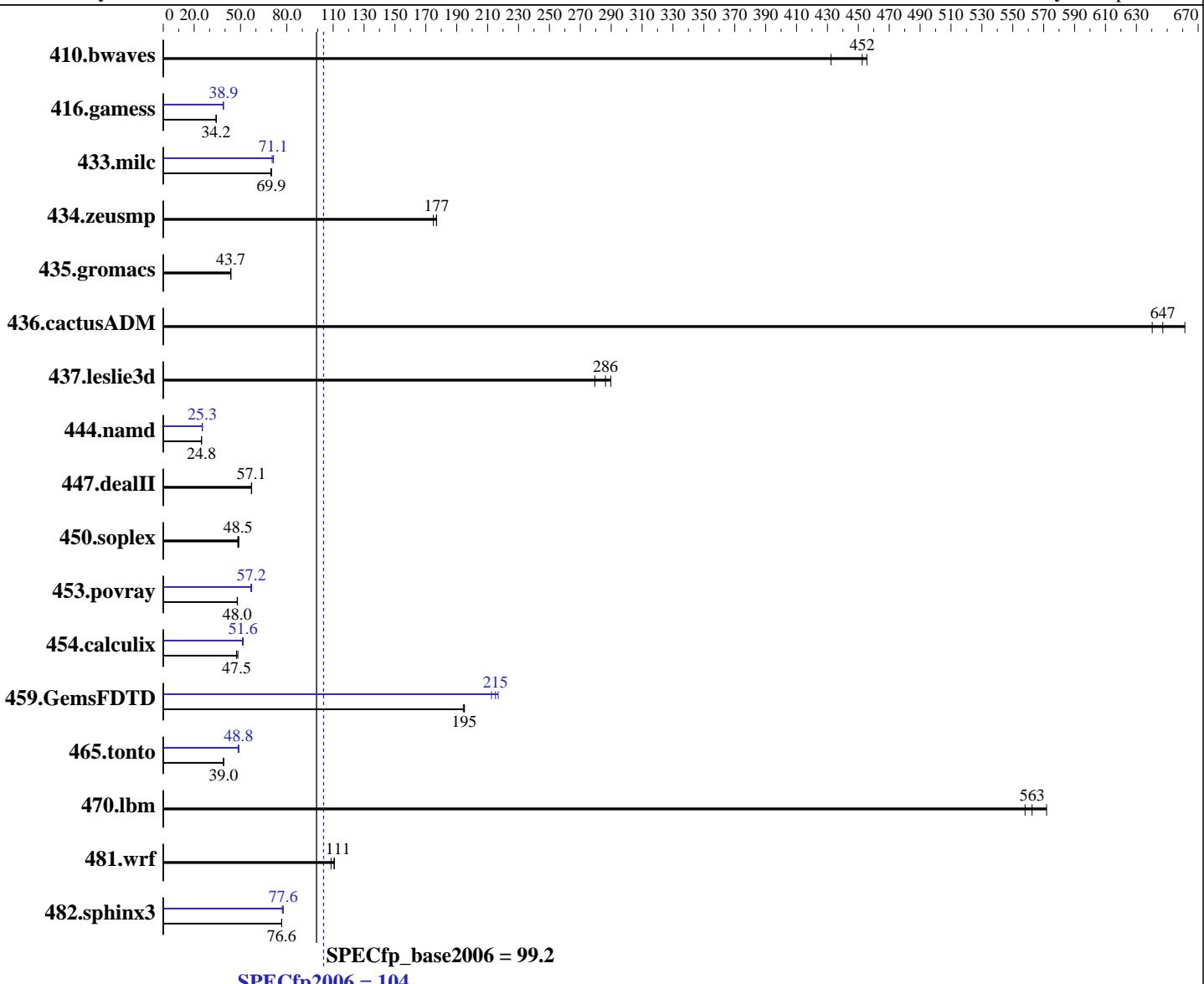
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Mar-2014

Hardware Availability: Nov-2013

Software Availability: Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2690 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Compiler: 2.6.32-358.23.2.el6.x86\_64  
 Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 File System: Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Software: ext4

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L3 Cache:	25 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 146 GB SAS, 15000 RPM	Other Software:	none
Other Hardware:	None		

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>30.0</b>	<b>452</b>	29.8	456	31.4	432	<b>30.0</b>	<b>452</b>	29.8	456	31.4	432
416.gamess	573	34.2	569	34.4	<b>573</b>	<b>34.2</b>	504	38.9	<b>503</b>	<b>38.9</b>	503	39.0
433.milc	132	69.8	<b>131</b>	<b>69.9</b>	131	70.0	<b>129</b>	<b>71.1</b>	130	70.5	129	71.4
434.zeusmp	<b>51.4</b>	<b>177</b>	51.4	177	52.0	175	<b>51.4</b>	<b>177</b>	51.4	177	52.0	175
435.gromacs	163	43.8	<b>163</b>	<b>43.7</b>	164	43.6	163	43.8	<b>163</b>	<b>43.7</b>	164	43.6
436.cactusADM	18.7	640	<b>18.5</b>	<b>647</b>	18.1	661	18.7	640	<b>18.5</b>	<b>647</b>	18.1	661
437.leslie3d	<b>32.8</b>	<b>286</b>	32.4	290	33.6	279	<b>32.8</b>	<b>286</b>	32.4	290	33.6	279
444.namd	325	24.7	324	24.8	<b>324</b>	<b>24.8</b>	317	25.3	318	25.2	<b>317</b>	<b>25.3</b>
447.dealII	<b>200</b>	<b>57.1</b>	200	57.1	200	57.2	<b>200</b>	<b>57.1</b>	200	57.1	200	57.2
450.soplex	173	48.3	<b>172</b>	<b>48.5</b>	170	48.9	173	48.3	<b>172</b>	<b>48.5</b>	170	48.9
453.povray	111	48.1	111	47.8	<b>111</b>	<b>48.0</b>	93.9	56.7	92.9	57.3	<b>93.0</b>	<b>57.2</b>
454.calculix	174	47.4	<b>174</b>	<b>47.5</b>	171	48.3	<b>160</b>	<b>51.6</b>	161	51.2	159	51.8
459.GemsFDTD	54.6	194	54.4	195	<b>54.4</b>	<b>195</b>	49.9	212	<b>49.3</b>	<b>215</b>	48.9	217
465.tonto	<b>252</b>	<b>39.0</b>	253	38.8	251	39.1	201	48.9	<b>201</b>	<b>48.8</b>	203	48.4
470.lbm	<b>24.4</b>	<b>563</b>	24.6	558	24.0	572	<b>24.4</b>	<b>563</b>	24.6	558	24.0	572
481.wrf	101	111	103	109	<b>101</b>	<b>111</b>	101	111	103	109	<b>101</b>	<b>111</b>
482.sphinx3	254	76.6	254	76.7	<b>254</b>	<b>76.6</b>	253	77.1	251	77.7	<b>251</b>	<b>77.6</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191  
running on DPx4-SPECCPU Wed Mar 19 21:11:04 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Sep-2013

## Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
        2 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings : 20
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      132193312 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DPx4-SPECCPU 2.6.32-358.23.2.el6.x86_64 #1 SMP Sat Sep 14 05:32:37 EDT
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 19 15:34
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_dpx4speccpu-lv_home
                ext4   81G   5.3G   72G   7% /home
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. 4.6.5 11/28/2013
Memory:
16x None None
8x Sams M393 16 GB 1866 MHz 1 rank
```

```
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY="granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "20"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled  
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

BladeSymphony BS2000 and Hitachi Compute Blade 2000 are electronically equivalent.  
The results have been measured on a BladeSymphony BS2000

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>  
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>  
<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>



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