



# SPEC® CINT2006 Result

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## IBM Corporation

IBM System x3250 M5  
(Intel Xeon E3-1240 v3, 3.40 GHz)

SPECint®2006 = **61.0**

SPECint\_base2006 = **59.0**

CPU2006 license: 11

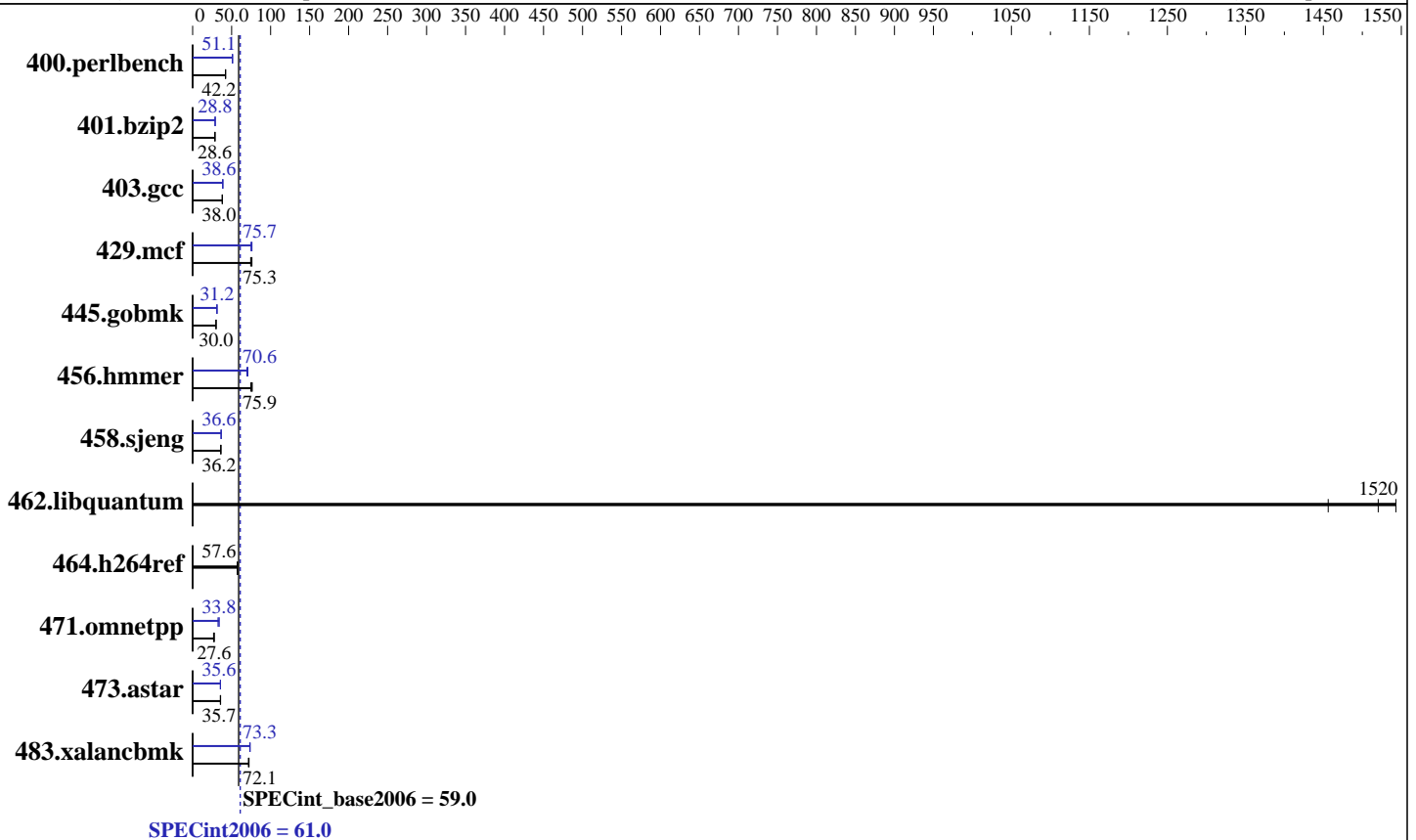
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E3-1240 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>231</u></b>	<b><u>42.2</u></b>	232	42.0	231	42.3	<b><u>191</u></b>	<b><u>51.1</u></b>	191	51.0	191	51.1
401.bzip2	338	28.6	<b><u>337</u></b>	<b><u>28.6</u></b>	337	28.6	334	28.9	<b><u>335</u></b>	<b><u>28.8</u></b>	335	28.8
403.gcc	<b><u>212</u></b>	<b><u>38.0</u></b>	212	37.9	211	38.2	209	38.6	208	38.7	<b><u>208</u></b>	<b><u>38.6</u></b>
429.mcf	<b><u>121</u></b>	<b><u>75.3</u></b>	123	74.4	120	76.1	<b><u>121</u></b>	<b><u>75.7</u></b>	122	74.8	120	76.1
445.gobmk	<b><u>350</u></b>	<b><u>30.0</u></b>	350	30.0	350	30.0	<b><u>336</u></b>	<b><u>31.2</u></b>	337	31.2	336	31.2
456.hammer	122	76.4	<b><u>123</u></b>	<b><u>75.9</u></b>	125	74.6	132	70.8	134	69.6	<b><u>132</u></b>	<b><u>70.6</u></b>
458.sjeng	<b><u>334</u></b>	<b><u>36.2</u></b>	336	36.1	334	36.2	<b><u>331</u></b>	<b><u>36.6</u></b>	331	36.5	330	36.6
462.libquantum	<b><u>13.6</u></b>	<b><u>1520</u></b>	14.2	1460	13.4	1540	<b><u>13.6</u></b>	<b><u>1520</u></b>	14.2	1460	13.4	1540
464.h264ref	384	57.6	<b><u>385</u></b>	<b><u>57.6</u></b>	385	57.4	384	57.6	<b><u>385</u></b>	<b><u>57.6</u></b>	385	57.4
471.omnetpp	226	27.7	<b><u>226</u></b>	<b><u>27.6</u></b>	233	26.8	184	33.9	192	32.5	<b><u>185</u></b>	<b><u>33.8</u></b>
473.astar	196	35.9	<b><u>197</u></b>	<b><u>35.7</u></b>	198	35.5	<b><u>197</u></b>	<b><u>35.6</u></b>	197	35.6	197	35.7
483.xalancbmk	<b><u>95.7</u></b>	<b><u>72.1</u></b>	96.5	71.5	95.4	72.3	<b><u>94.1</u></b>	<b><u>73.3</u></b>	94.2	73.3	93.5	73.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Hyper-Threading set to Disabled  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on x3250M5 Wed Feb 26 01:11:07 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E3-1240 v3 @ 3.40GHz  
1 "physical id"s (chips)  
4 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 4

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### Platform Notes (Continued)

```
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16299396 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3250M5 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 25 14:29
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3250m5-lv_home
                ext4      852G  17G  792G   3% /home
```

```
Additional information from dmidecode:
BIOS IBM -[JUE109OUS-1.00]- 11/20/2013
Memory:
2x 0000      1600 MHz
2x Micron 18KSF1G72AZ-1G6E1 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)  
"2x 0000 1600 MHz" memory information from dmidecode indicates unused DIMM slots.

### General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"  
OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`  
401.bzip2: `-DSPEC_CPU_LP64`  
403.gcc: `-DSPEC_CPU_LP64`  
429.mcf: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`  
464.h264ref: `-DSPEC_CPU_LP64`  
473.astar: `-DSPEC_CPU_LP64`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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