SPEC® CFP2006 Result
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ACTION S.A.

ACTINA SOLAR 200 S5 (Intel Xeon E5-2620 v2, 2.10 GHz)

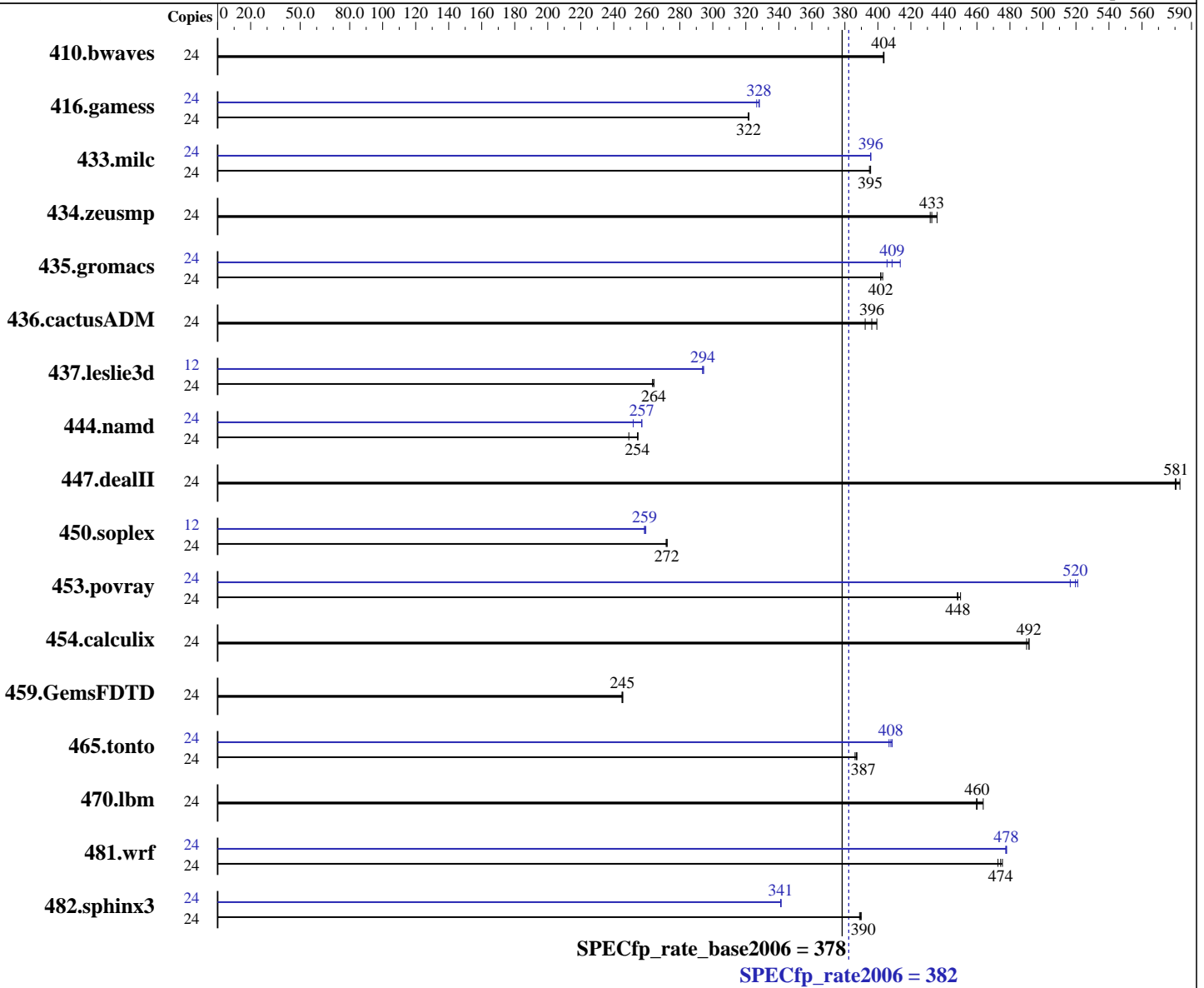$SPECfp®\_rate2006 = \quad 382$

$SPECfp\_rate\_base2006 = \quad 378$

CPU2006 license: 9008	**Test date:** Dec-2013
Test sponsor: ACTION S.A.	**Hardware Availability:** Oct-2013
Tested by: ACTION S.A.	**Software Availability:** Sep-2013

Copies 0 20.0 50.0 80.0 100 120 140 160 180 200 220 240 260 280 300 320 340 360 380 400 420 440 460 480 500 520 540 560 590

Benchmark	Copies	Value
410.bwaves	24	404
416.gamess	24	328
416.gamess	24	322
433.milc	24	396
433.milc	24	395
434.zeusmp	24	433
435.gromacs	24	409
435.gromacs	24	402
436.cactusADM	24	396
437.leslie3d	12	294
437.leslie3d	24	264
444.namd	24	257
444.namd	24	254
447.dealII	24	581
450.soplex	12	259
450.soplex	24	272
453.povray	24	520
453.povray	24	448
454.calculix	24	492
459.GemsFDTD	24	245
465.tonto	24	408
465.tonto	24	387
470.lbm	24	460
481.wrf	24	478
481.wrf	24	474
482.sphinx3	24	341
482.sphinx3	24	390

SPECfp_rate_base2006 = 378

SPECfp_rate2006 = 382

Hardware

CPU Name:	Intel Xeon E5-2620 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.60 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago)
	2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
	Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

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L3 Cache:	15 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz and CL11)	Peak Pointers:	32/64-bit
		Other Software:	None
Disk Subsystem:	1 x 240 GB SATA II SSD		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	808	404	**808**	**404**	809	403	24	808	404	**808**	**404**	809	403
416.gamess	24	1462	321	**1461**	**322**	1460	322	24	1432	328	**1433**	**328**	1439	327
433.milc	24	557	396	**557**	**395**	558	395	24	557	396	**557**	**396**	557	396
434.zeusmp	24	**505**	**433**	506	432	501	436	24	**505**	**433**	506	432	501	436
435.gromacs	24	425	403	427	402	**427**	**402**	24	**419**	**409**	423	406	414	414
436.cactusADM	24	718	399	**724**	**396**	731	392	24	718	399	**724**	**396**	731	392
437.leslie3d	24	853	264	857	263	**854**	**264**	12	383	294	384	294	**384**	**294**
444.namd	24	**757**	**254**	756	255	773	249	24	**749**	**257**	765	252	749	257
447.dealII	24	473	580	**473**	**581**	471	583	24	473	580	**473**	**581**	471	583
450.soplex	24	735	272	**736**	**272**	737	272	12	**387**	**259**	387	259	386	259
453.povray	24	**285**	**448**	284	450	285	448	24	245	521	**246**	**520**	247	517
454.calculix	24	404	490	**403**	**492**	403	492	24	404	490	**403**	**492**	403	492
459.GemsFDTD	24	1039	245	1037	245	**1039**	**245**	24	1039	245	1037	245	**1039**	**245**
465.tonto	24	612	386	610	387	**610**	**387**	24	**579**	**408**	578	409	581	407
470.lbm	24	**717**	**460**	717	460	711	464	24	**717**	**460**	717	460	711	464
481.wrf	24	564	475	**565**	**474**	567	473	24	561	478	**561**	**478**	561	477
482.sphinx3	24	1203	389	**1201**	**390**	1200	390	24	1371	341	**1371**	**341**	1370	341

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

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CPU2006 license: 9008		**Test date:**	Dec-2013
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Platform Notes

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sat Dec 28 08:43:59 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2620 v2 @ 2.10GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       264500920 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-358.11.1.el6.x86_64 #1 SMP Tue Nov 19
   17:43:04 CET 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 23 14:54

SPEC is set to: /cpu2006.1.2
   Filesystem     Type     Size   Used Avail Use% Mounted on
   /dev/sda1      ext4     193G    86G   97G  47% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. 3.0a 07/31/2013
  Memory:
   16x   16 GB
   16x Hynix Semiconducto HMT42GR7AFR4C 16 GB 1600 MHz 1 rank

 (End of data from sysinfo program)
dmidecode does not properly display memory modules,
```
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Platform Notes (Continued)

```
16 modules of 16 GB were used to run the test (256 GB total)
Due to BIOS issue memory rank is improperly displayed -
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Binaries compiled on a system with 2x Xeon E5-2650 v2 chips
 + 256 GB memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
```
    icc    -m64
```

C++ benchmarks:
```
    icpc   -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc    -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
     482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
    -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
    -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
    -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc    -m64
```
```
    482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
    icpc   -m64
```
```
     450.soplex: icpc -m32
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc    -m64 ifort -m64
```

Peak Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

        433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
                  -prof-use(pass 2) -auto-ilp32

         470.lbm: basepeak = yes

      482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
                   -unroll2

C++ benchmarks:

        444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
                  -prof-use(pass 2) -fno-alias -auto-ilp32

       447.dealII: basepeak = yes

       450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
                   -prof-use(pass 2) -opt-malloc-options=3

       453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
                   -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

       410.bwaves: basepeak = yes

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Tested by: ACTION S.A.		**Software Availability:**	Sep-2013

Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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