



SPEC® CINT2006 Result

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Supermicro

Supermicro A+ Server 4022G-6F, (H8DG6-F, Opteron 6370P)
AMD Opteron 6370P

SPECint®_rate2006 = 469

SPECint_rate_base2006 = 411

CPU2006 license: 001176

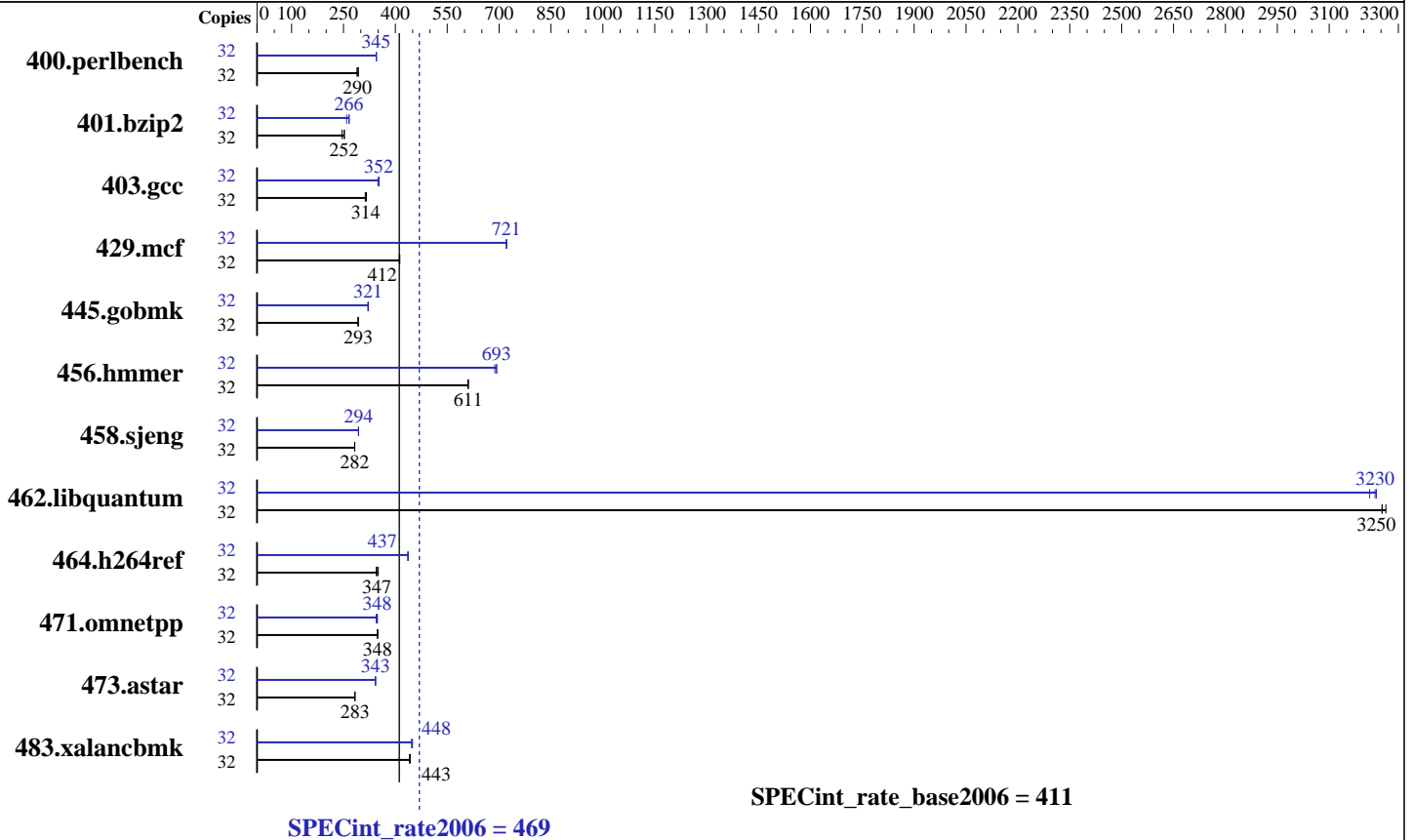
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Jan-2014

Software Availability: Aug-2012



Hardware

CPU Name: AMD Opteron 6370P
 CPU Characteristics: AMD Turbo CORE technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 512 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core
 Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
 L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4,
Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 4.5.2 of x86 Open64 Compiler Suite
(from AMD)
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 10.0 32-bit Library for Linux



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------|--------------------|--------------------|--------------------|-------------------|-------------------|-------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 32 | <u>1078</u> | <u>290</u> | 1066 | 293 | 1080 | 289 | 32 | <u>907</u> | <u>345</u> | 905 | 346 | 907 | 345 |
| 401.bzip2 | 32 | 1222 | 253 | <u>1227</u> | <u>252</u> | 1256 | 246 | 32 | <u>1162</u> | <u>266</u> | 1191 | 259 | 1158 | 267 |
| 403.gcc | 32 | 823 | 313 | <u>820</u> | <u>314</u> | 815 | 316 | 32 | <u>731</u> | <u>352</u> | 730 | 353 | 736 | 350 |
| 429.mcf | 32 | 707 | 413 | 708 | 412 | <u>708</u> | <u>412</u> | 32 | 405 | 721 | 404 | 722 | <u>405</u> | <u>721</u> |
| 445.gobmk | 32 | <u>1147</u> | <u>293</u> | 1147 | 293 | 1149 | 292 | 32 | <u>1045</u> | <u>321</u> | 1044 | 322 | 1046 | 321 |
| 456.hammer | 32 | 490 | 610 | <u>489</u> | <u>611</u> | 489 | 611 | 32 | 434 | 688 | 431 | 693 | <u>431</u> | <u>693</u> |
| 458.sjeng | 32 | <u>1371</u> | <u>282</u> | 1369 | 283 | 1371 | 282 | 32 | 1319 | 294 | <u>1319</u> | <u>294</u> | 1320 | 293 |
| 462.libquantum | 32 | 203 | 3260 | 204 | 3250 | <u>204</u> | <u>3250</u> | 32 | <u>205</u> | <u>3230</u> | 205 | 3240 | 206 | 3220 |
| 464.h264ref | 32 | 2020 | 351 | 2054 | 345 | <u>2043</u> | <u>347</u> | 32 | 1624 | 436 | <u>1621</u> | <u>437</u> | 1621 | 437 |
| 471.omnetpp | 32 | 575 | 348 | 573 | 349 | <u>574</u> | <u>348</u> | 32 | 575 | 348 | <u>575</u> | <u>348</u> | 580 | 345 |
| 473.astar | 32 | 794 | 283 | <u>794</u> | <u>283</u> | 793 | 283 | 32 | 655 | 343 | <u>656</u> | <u>343</u> | 656 | 342 |
| 483.xalancbmk | 32 | <u>499</u> | <u>443</u> | 501 | 441 | 499 | 443 | 32 | <u>493</u> | <u>448</u> | 491 | 450 | 494 | 447 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst

Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

C State mode set to disabled under Processor & Clock Options in BIOS.
HPC Mode set to enabled under Processor & Clock Options in BIOS.
C1E Support set to disabled under Processor & Clock Options in BIOS.
Power Down Enable set to disabled in Memory Configuration in BIOS.

General Notes

Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/home/spec/amd1206-rate-libs-revA/32:/home/spec/amd1206-rate-libs-revA/64"

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General Notes (Continued)

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:
openc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-Ofast -CG:local_sched_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000
-IPA:small_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2
-march=bdver1

C++ benchmarks:
-Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on -D__OPEN64_FAST_SET
-march=bdver1 -L/root/work/libraries/SmartHeap-10/lib -lsmarheap

Peak Compiler Invocation

C benchmarks:
openc

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Peak Compiler Invocation (Continued)

C++ benchmarks:
openCC

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-GRA:aggr_loop_splitting=off -GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-march=bdver2

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bdt=2m:heap=2m -mso -march=bdver1

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
-IPA:pu_reorder=1 -LNO:ignore_feedback=off -WOPT:if_conv=2
-HP:bd=2m:heap=2m -march=bdver1
```

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Peak Optimization Flags (Continued)

456.hmmer: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -OPT:alias=disjoint
-OPT:unroll_times_max=16 -OPT:unroll_size=512
-OPT:unroll_level=2 -OPT:keep_ext=on -CG:cflow=0
-CG:cmp_peep=on -CG:pre_local_sched=off -HP:bdt=2m:heap=2m
-CG:p2align=0 -CG:load_exe=3 -CG:dsched=on -march=bdver1

458.sjeng: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:ptr_load_use=0 -CG:divrem_opt=on -CG:movext_icmp=off
-CG:locs_best=on -LNO:full_unroll=10 -IPA:pu_reorder=2
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1

462.libquantum: -Ofast -mso -OPT:unroll_size=512 -OPT:unroll_times_max=16
-LNO:prefetch=2 -LNO:prefetch_ahead=4 -LNO:pf2=0
-CG:local_sched_alg=1 -CG:p2align=0 -INLINE:aggressive=ON
-IPA:plimit=15000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m,limit=300 -march=bdver2

464.h264ref: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-OPT:unroll_size=256 -OPT:unroll_times_max=2
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr_load_use=0
-CG:local_sched_alg=1 -HP:bdt=2m:heap=2m -march=bdver1

C++ benchmarks:

471.omnetpp: -Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on
-WOPT:sib=on -D__OPEN64_FAST_SET -march=bdver2 -mno-fma4
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap

473.astar: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
-CG:p2align=1 -CG:dsched=on -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -HP:bdt=2m:heap=2m
-march=bdver1

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll_size=512
-OPT:unroll_times_max=8 -D__OPEN64_FAST_SET
-INLINE:aggressive=on -m32 -CG:cmp_peep=on
-CG:local_sched=off -CG:p2align=1 -GRA:unspill=on
-TENV:frame_pointer=off -fno-emit-exceptions -march=bdver2
-mno-fma4
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.html>

<http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-III.xml>

<http://www.spec.org/cpu2006/flags/amd-Supermicro-platform-rate-revC-I.xml>

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