SPEC® CINT2006 Result

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Dell Inc.

PowerEdge M420 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint®2006 = 37.7

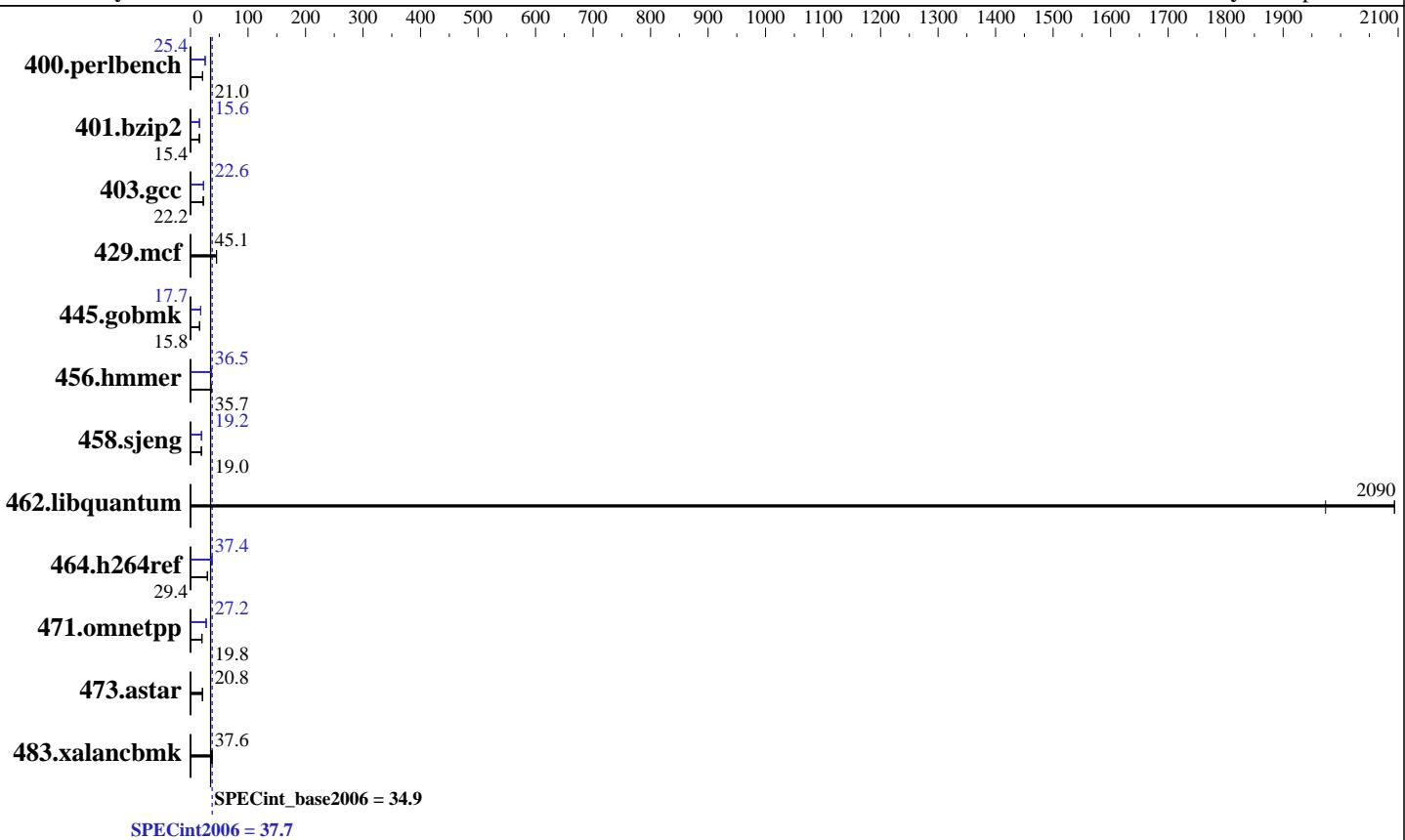SPECint_base2006 = 34.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013

Benchmark	Base	Peak
400.perlbench	21.0	25.4
401.bzip2	15.4	15.6
403.gcc	22.2	22.6
429.mcf	45.1	
445.gobmk	15.8	17.7
456.hmmer	35.7	36.5
458.sjeng	19.0	19.2
462.libquantum	2090	
464.h264ref	29.4	37.4
471.omnetpp	19.8	27.2
473.astar	20.8	
483.xalancbmk	37.6	

SPECint_base2006 = 34.9

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Hardware

CPU Name: Intel Xeon E5-2450L
CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (6 x 16 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 2 x 50 GB SATA SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
2.6.32-358.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	466	21.0	**465**	**21.0**	465	21.0	383	25.5	384	25.4	**384**	**25.4**
401.bzip2	627	15.4	627	15.4	**627**	**15.4**	620	15.6	620	15.6	**620**	**15.6**
403.gcc	362	22.3	**362**	**22.2**	362	22.2	356	22.6	356	22.6	**356**	**22.6**
429.mcf	201	45.4	**202**	**45.1**	203	45.0	201	45.4	**202**	**45.1**	203	45.0
445.gobmk	**665**	**15.8**	666	15.8	665	15.8	**594**	**17.7**	594	17.7	594	17.7
456.hmmer	**262**	**35.7**	265	35.3	261	35.7	**256**	**36.5**	255	36.6	256	36.5
458.sjeng	**636**	**19.0**	635	19.0	643	18.8	644	18.8	**630**	**19.2**	623	19.4
462.libquantum	**9.90**	**2090**	10.5	1970	9.90	2090	**9.90**	**2090**	10.5	1970	9.90	2090
464.h264ref	755	29.3	751	29.5	**753**	**29.4**	**591**	**37.4**	591	37.4	601	36.8
471.omnetpp	**315**	**19.8**	316	19.8	315	19.9	231	27.1	**230**	**27.2**	229	27.3
473.astar	337	20.8	**338**	**20.8**	339	20.7	337	20.8	**338**	**20.8**	339	20.7
483.xalancbmk	183	37.7	**184**	**37.6**	188	36.7	183	37.7	**184**	**37.6**	188	36.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
    BIOS settings:
    Virtualization Technology disabled
    Execute Disable disabled
    Logical Processor disabled
    System Profile set to Performance
    Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
    $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
    running on localhost.localdomain Thu Nov 21 03:49:07 2013

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2450L 0 @ 1.80GHz
          2 "physical id"s (chips)
          16 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 8
          siblings  : 8
          physical 0: cores 0 1 2 3 4 5 6 7
```

Continued on next page

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Platform Notes (Continued)

```
      physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 KB

  From /proc/meminfo
    MemTotal:        98983728 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

  /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
    Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
    EST 2013 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Nov 21 03:48

  SPEC is set to: /root/cpu2006-1.2
    Filesystem      Type     Size  Used Avail Use% Mounted on
    /dev/sda3       ext4      90G  6.5G   79G   8% /

  Additional information from dmidecode:
    BIOS Dell Inc. 2.0.22 09/23/2013
    Memory:
    6x 00AD00B300AD HMT42GR7AFR4A-PB 16 GB 1600 MHz 2 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
 OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
```

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Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      464.h264ref: -DSPEC_CPU_LP64
      471.omnetpp: -DSPEC_CPU_LP64
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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