



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint®_rate2006 = **635**

PRIMERGY BX924 S4, Intel Xeon E5-2643 v2, 3.50 GHz

SPECint_rate_base2006 = 610

CPU2006 license: 19

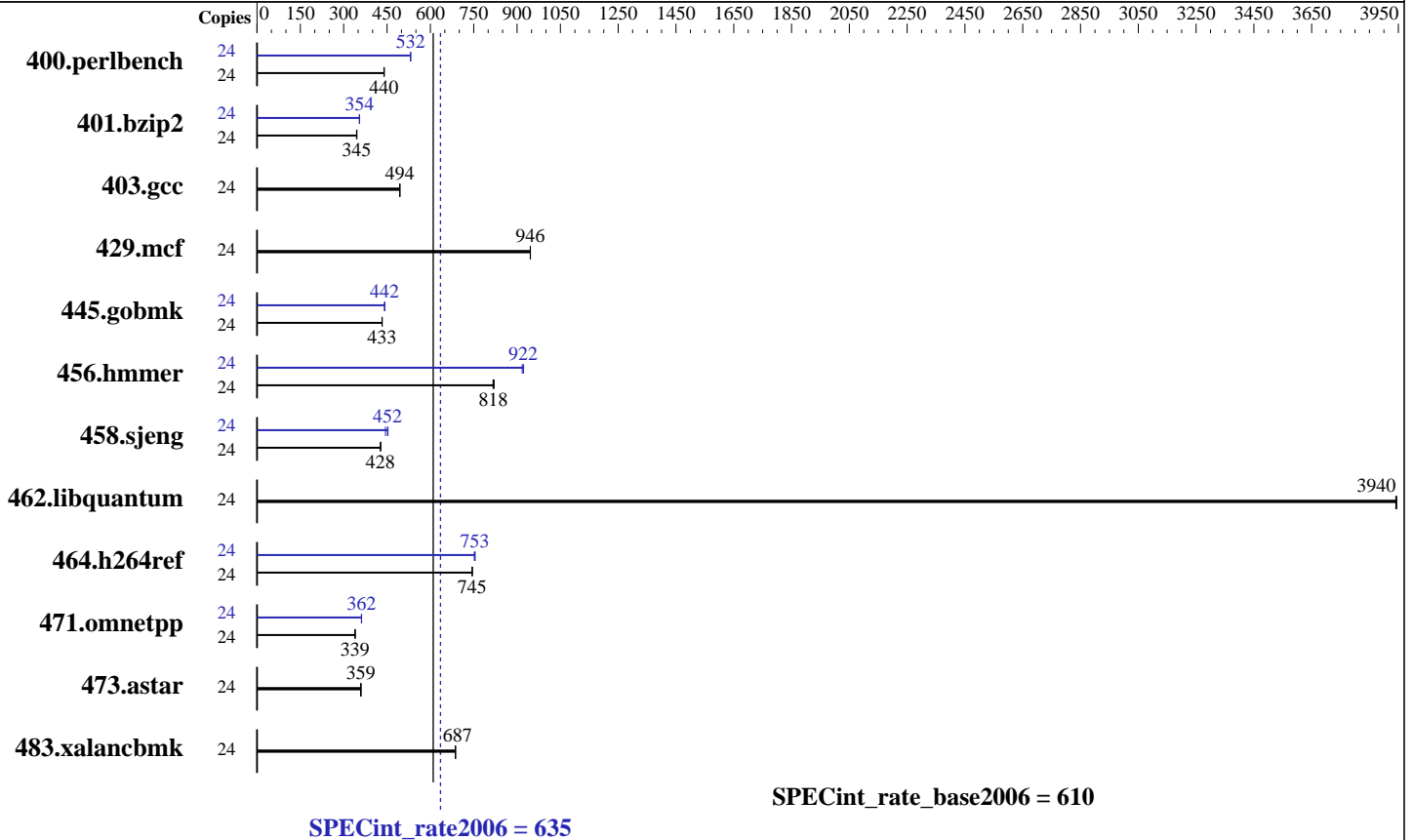
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2643 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.11.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = **635**

PRIMERGY BX924 S4, Intel Xeon E5-2643 v2, 3.50 GHz

SPECint_rate_base2006 = 610

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Oct-2013
Hardware Availability: Oct-2013
Software Availability: Sep-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	533	440	534	439	532	440	24	442	531	441	532	441	532
401.bzip2	24	670	346	672	345	671	345	24	652	355	653	354	654	354
403.gcc	24	391	494	391	495	391	494	24	391	494	391	495	391	494
429.mcf	24	231	946	231	946	231	946	24	231	946	231	946	231	946
445.gobmk	24	581	433	582	433	582	433	24	570	442	570	442	570	442
456.hammer	24	273	821	274	818	274	817	24	243	923	244	918	243	922
458.sjeng	24	680	427	678	428	679	428	24	641	453	653	445	643	452
462.libquantum	24	126	3940	126	3940	126	3940	24	126	3940	126	3940	126	3940
464.h264ref	24	713	745	713	745	713	745	24	703	755	705	753	706	752
471.omnetpp	24	441	340	442	339	442	339	24	415	361	414	362	415	362
473.astar	24	470	359	469	359	468	360	24	470	359	469	359	468	360
483.xalancbmk	24	241	686	241	687	241	687	24	241	686	241	687	241	687

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 635

PRIMERGY BX924 S4, Intel Xeon E5-2643 v2, 3.50 GHz

SPECint_rate_base2006 = 610

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

`icc -m32`

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 635

PRIMERGY BX924 S4, Intel Xeon E5-2643 v2, 3.50 GHz

SPECint_rate_base2006 = 610

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

Peak Compiler Invocation (Continued)

456.hmmr: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmr: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3`

456.hmmr: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 635

PRIMERGY BX924 S4, Intel Xeon E5-2643 v2, 3.50 GHz

SPECint_rate_base2006 = 610

CPU2006 license: 19

Test date: Oct-2013

Test sponsor: Fujitsu

Hardware Availability: Oct-2013

Tested by: Fujitsu

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 17:37:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 November 2013.