SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX200 S8, Intel Xeon E5-2690 v2, 3.00 GHz

$SPECint^®\_rate2006 =$ 892

$SPECint\_rate\_base2006 =$ 862

CPU2006 license: 19	**Test date:** Oct-2013
Test sponsor: Fujitsu	**Hardware Availability:** Oct-2013
Tested by: Fujitsu	**Software Availability:** Sep-2013

Copies

Benchmark	base	peak
400.perlbench	40 / 669	40 / 811
401.bzip2	40 / 468	40 / 476
403.gcc	40 / 671	
429.mcf	40 / 1250	
445.gobmk	40 / 659	40 / 673
456.hmmer	40 / 1190	40 / 1290
458.sjeng	40 / 653	40 / 678
462.libquantum	40 / 5850	
464.h264ref	40 / 1120	40 / 1130
471.omnetpp	40 / 448	40 / 470
473.astar	40 / 480	
483.xalancbmk	40 / 894	

SPECint_rate_base2006 = 862

SPECint_rate2006 = 892

Hardware

CPU Name:	Intel Xeon E5-2690 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 x SATA, 450 GB, 15000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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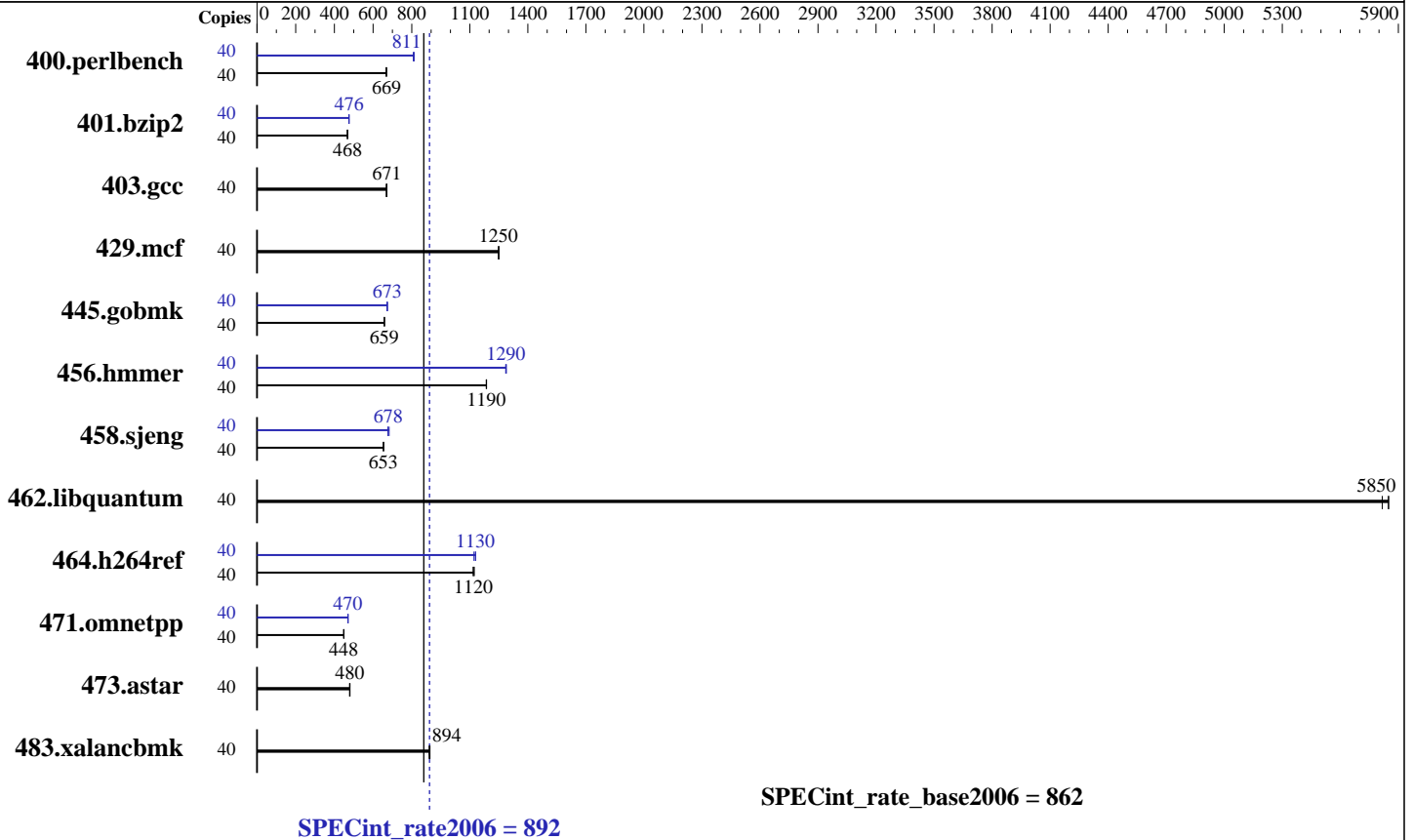
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	586	667	584	669	**584**	**669**	40	484	808	**482**	**811**	482	811
401.bzip2	40	825	468	826	467	**825**	**468**	40	**811**	**476**	813	475	811	476
403.gcc	40	482	668	**480**	**671**	480	671	40	482	668	**480**	**671**	480	671
429.mcf	40	292	1250	292	1250	**292**	**1250**	40	292	1250	292	1250	**292**	**1250**
445.gobmk	40	638	658	**637**	**659**	637	659	40	622	675	**623**	**673**	625	672
456.hmmer	40	314	1190	**315**	**1190**	315	1190	40	290	1290	290	1290	**290**	**1290**
458.sjeng	40	739	655	**741**	**653**	741	653	40	709	683	715	677	**714**	**678**
462.libquantum	40	142	5820	142	5850	**142**	**5850**	40	142	5820	142	5850	**142**	**5850**
464.h264ref	40	788	1120	**791**	**1120**	792	1120	40	789	1120	784	1130	**784**	**1130**
471.omnetpp	40	558	448	557	449	**558**	**448**	40	**532**	**470**	531	471	532	470
473.astar	40	587	478	585	480	**585**	**480**	40	587	478	585	480	**585**	**480**
483.xalancbmk	40	309	894	310	889	**309**	**894**	40	309	894	310	889	**309**	**894**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The numactl mechanism was used to bind copies to processors. The config file option 'submit'
   was used to generate numactl commands to bind each copy to a specific processor.
   For details, please see the config file.
```

Operating System Notes

```
   Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
   BIOS configuration:
   Energy Performance = Performance
```

General Notes

```
   Environment variables set by runspec before the start of the run:
   LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

   Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
   memory using RedHat EL 6.4
   Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
   Filesystem page cache cleared with:
   echo 1>      /proc/sys/vm/drop_caches
   runspec command invoked through numactl i.e.:
   numactl --interleave=all runspec <etc>
```

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General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

SPEC CINT2006 Result

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PRIMERGY RX200 S8, Intel Xeon E5-2690 v2, 3.00 GHz

SPECint_rate2006 =   892

SPECint_rate_base2006 =   862

CPU2006 license: 19		**Test date:**	Oct-2013
Test sponsor:	Fujitsu	**Hardware Availability:**	Oct-2013
Tested by:	Fujitsu	**Software Availability:**	Sep-2013

Peak Compiler Invocation (Continued)

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
> `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

Fujitsu

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml

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