SPEC® CINT2006 Result

Supermicro

SuperServer 5018D-TLN4F
(X10SLM+-LN4F , Intel Xeon E3-1270 v3)

$SPECint^®\_rate2006 =$ 217

$SPECint\_rate\_base2006 =$ 211

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2013

Hardware Availability: Jun-2013

Software Availability: May-2013

SPECint_rate_base2006 = 211

SPECint_rate2006 = 217

Hardware

CPU Name:	Intel Xeon E3-1270 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem:	1 x 500 GB SATA III, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) Kernel 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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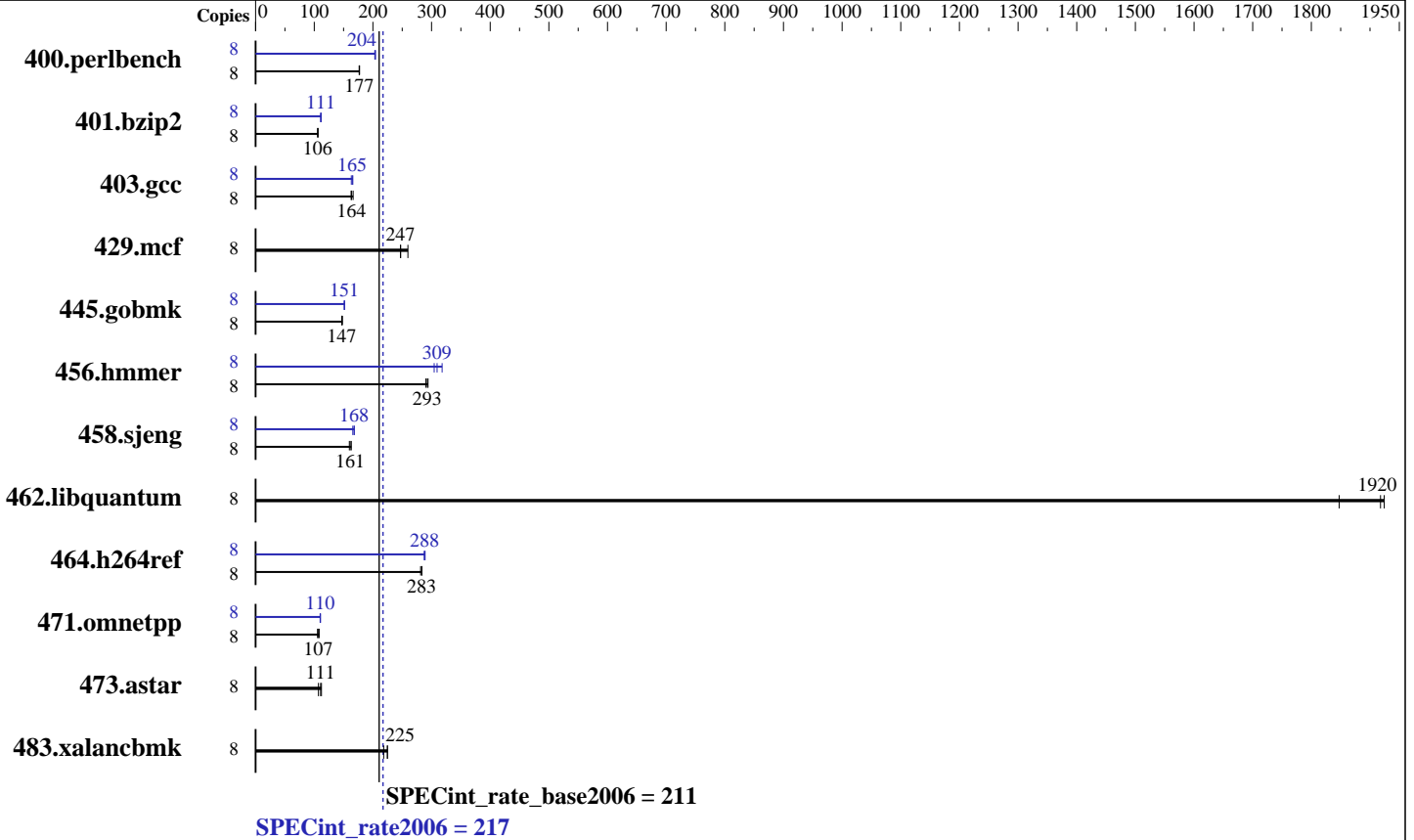
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**442**	**177**	442	177	441	177	8	385	203	382	204	**384**	**204**
401.bzip2	8	729	106	**728**	**106**	725	106	8	691	112	696	111	**693**	**111**
403.gcc	8	**393**	**164**	387	166	396	163	8	**390**	**165**	388	166	395	163
429.mcf	8	281	260	295	247	**295**	**247**	8	281	260	295	247	**295**	**247**
445.gobmk	8	571	147	567	148	**571**	**147**	8	555	151	553	152	**554**	**151**
456.hmmer	8	**255**	**293**	254	294	257	290	8	245	304	**241**	**309**	235	318
458.sjeng	8	**600**	**161**	606	160	593	163	8	**576**	**168**	575	168	584	166
462.libquantum	8	86.1	1920	89.7	1850	**86.4**	**1920**	8	86.1	1920	89.7	1850	**86.4**	**1920**
464.h264ref	8	629	281	624	284	**625**	**283**	8	613	289	616	287	**614**	**288**
471.omnetpp	8	461	108	**467**	**107**	474	106	8	**454**	**110**	452	111	454	110
473.astar	8	524	107	500	112	**507**	**111**	8	524	107	500	112	**507**	**111**
483.xalancbmk	8	253	218	**246**	**225**	246	225	8	253	218	**246**	**225**	246	225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RHEL5.5
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

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Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
        -opt-mem-layout-trans=3

C++ benchmarks:
        -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
        -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

        400.perlbench: icc -m64

        401.bzip2: icc -m64

        456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks:
        icpc -m32

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

    401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

      403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/sh -lsmartheap

     473.astar: basepeak = yes

 483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

> 403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml

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