



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®_rate2006 = 70.6

Express5800/GT110e-S (Intel Pentium G2020)

SPECint_rate_base2006 = 67.6

CPU2006 license: 9006

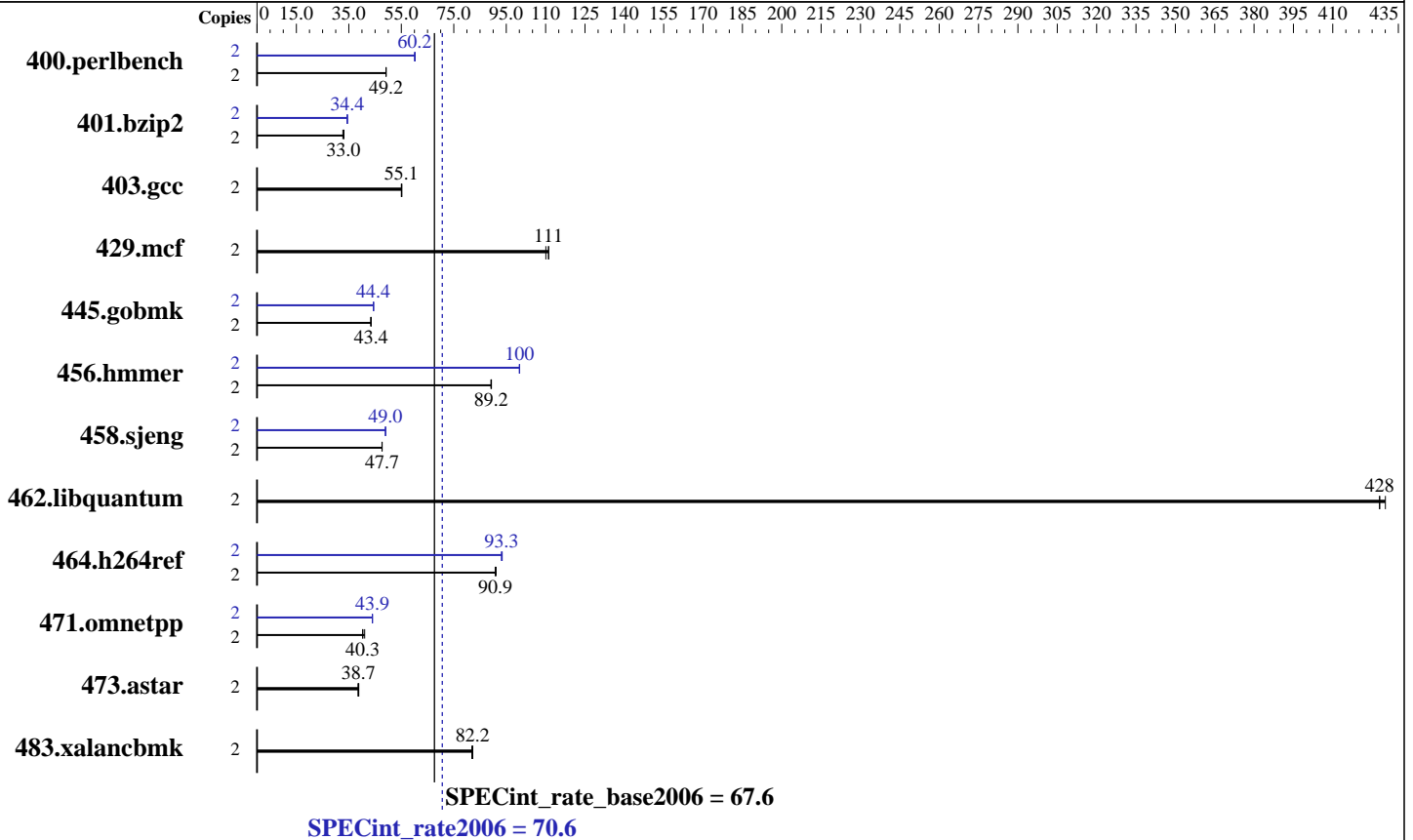
Test date: May-2013

Test sponsor: NEC Corporation

Hardware Availability: Apr-2013

Tested by: NEC Corporation

Software Availability: Feb-2012



Hardware

CPU Name: Intel Pentium G2020
 CPU Characteristics:
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux;
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<u>397</u>	<u>49.2</u>	397	49.2	398	49.2	2	<u>325</u>	<u>60.2</u>	326	60.0	324	60.3
401.bzip2	2	590	32.7	<u>585</u>	<u>33.0</u>	582	33.1	2	<u>560</u>	<u>34.4</u>	562	34.3	560	34.5
403.gcc	2	292	55.2	<u>292</u>	<u>55.1</u>	293	55.0	2	292	55.2	<u>292</u>	<u>55.1</u>	293	55.0
429.mcf	2	<u>164</u>	<u>111</u>	166	110	164	111	2	<u>164</u>	<u>111</u>	166	110	164	111
445.gobmk	2	484	43.3	482	43.5	<u>483</u>	<u>43.4</u>	2	472	44.4	<u>472</u>	<u>44.4</u>	473	44.4
456.hammer	2	209	89.2	209	89.4	<u>209</u>	<u>89.2</u>	2	186	100	187	100	<u>186</u>	<u>100</u>
458.sjeng	2	507	47.7	508	47.6	<u>508</u>	<u>47.7</u>	2	<u>494</u>	<u>49.0</u>	494	49.0	495	48.9
462.libquantum	2	<u>96.8</u>	<u>428</u>	96.9	428	96.4	430	2	<u>96.8</u>	<u>428</u>	96.9	428	96.4	430
464.h264ref	2	<u>487</u>	<u>90.9</u>	486	91.1	488	90.8	2	475	93.2	<u>475</u>	<u>93.3</u>	474	93.5
471.omnetpp	2	305	41.0	<u>310</u>	<u>40.3</u>	310	40.3	2	285	43.9	<u>285</u>	<u>43.9</u>	284	44.1
473.astar	2	365	38.5	<u>363</u>	<u>38.7</u>	362	38.8	2	365	38.5	<u>363</u>	<u>38.7</u>	362	38.8
483.xalancbmk	2	<u>168</u>	<u>82.2</u>	168	82.2	169	81.9	2	<u>168</u>	<u>82.2</u>	168	82.2	169	81.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Default BIOS settings were used.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For other inquiries, please contact webmaster@spec.org.

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