SPEC® CFP2006 Result

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Itautec

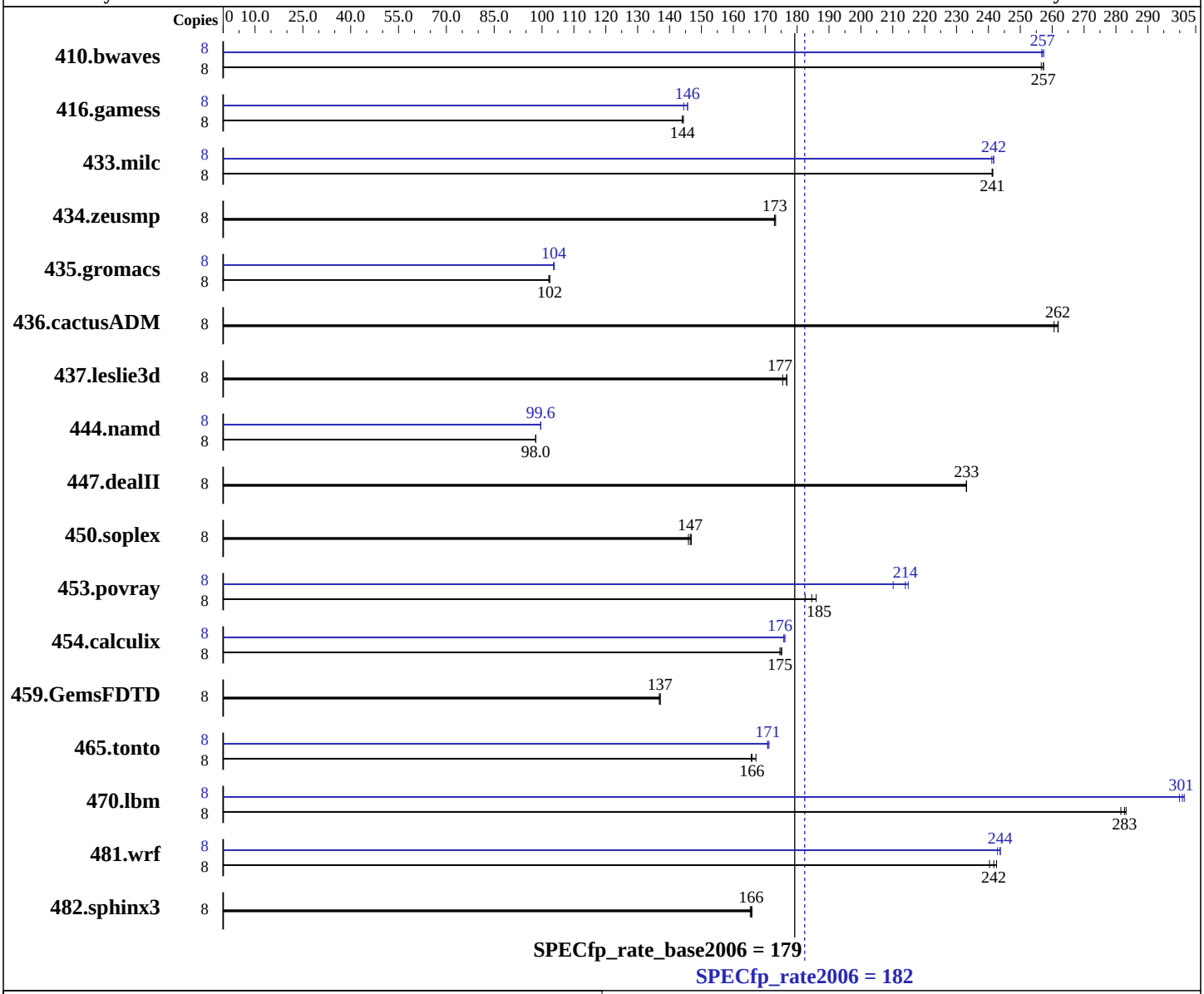
Servidor Itautec MX215 (Intel Xeon E5-2603)

SPECfp®_rate2006 = 182

SPECfp_rate_base2006 = 179

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2013
Hardware Availability: Jun-2012
Software Availability: Oct-2011



Hardware

CPU Name:	Intel Xeon E5-2603
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86_64
Compiler:	C/C++/Fortran: Version 12.1.0 of Intel Compiler XE Build 20111011
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 1000 GB, SATA-3, 7200 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	423	257	424	257	**423**	**257**	8	423	257	422	257	**423**	**257**
416.gamess	8	1088	144	**1088**	**144**	1085	144	8	1084	144	1075	146	**1076**	**146**
433.milc	8	305	241	**304**	**241**	304	241	8	**304**	**242**	304	242	305	241
434.zeusmp	8	421	173	**421**	**173**	420	173	8	421	173	**421**	**173**	420	173
435.gromacs	8	559	102	**558**	**102**	558	102	8	**550**	**104**	550	104	551	104
436.cactusADM	8	**365**	**262**	365	262	367	261	8	**365**	**262**	365	262	367	261
437.leslie3d	8	429	175	**426**	**177**	425	177	8	429	175	**426**	**177**	425	177
444.namd	8	654	98.0	**654**	**98.0**	655	98.0	8	644	99.6	644	99.6	**644**	**99.6**
447.dealII	8	393	233	393	233	**393**	**233**	8	393	233	393	233	**393**	**233**
450.soplex	8	454	147	**455**	**147**	457	146	8	454	147	**455**	**147**	457	146
453.povray	8	229	186	**231**	**185**	233	183	8	203	210	**199**	**214**	198	215
454.calculix	8	377	175	378	175	**377**	**175**	8	**375**	**176**	375	176	375	176
459.GemsFDTD	8	621	137	619	137	**619**	**137**	8	621	137	619	137	**619**	**137**
465.tonto	8	**475**	**166**	471	167	475	166	8	**461**	**171**	460	171	461	171
470.lbm	8	390	282	388	283	**389**	**283**	8	366	300	**365**	**301**	365	301
481.wrf	8	**370**	**242**	372	240	368	243	8	**367**	**244**	368	243	367	244
482.sphinx3	8	943	165	**941**	**166**	940	166	8	943	165	**941**	**166**	940	166

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

Platform Notes

```
Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on mx225 Sun Feb 24 04:30:53 2013
```

Continued on next page

Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2603 0 @ 1.80GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
   MemTotal:       65917572 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux mx225 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012 x86_64
   x86_64 x86_64 GNU/Linux

run-level 3 Feb 22 17:37

SPEC is set to: /home/rcaneca/cpu2006
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/mapper/vg_mx225-lv_home
                  ext4    836G  2.2G  791G   1% /home

(End of data from sysinfo program)
```

General Notes

```
This result was measured on the Servidor Itautec MX225.
The Servidor Itautec MX215 and the Servidor Itautec MX225
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -opt-mem-layout-trans=3`

470.lbm: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3 -ansi-alias -opt-prefetch -auto-ilp32 -opt-mem-layout-trans=3`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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