SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 222 S5 (Intel Xeon E5-2620)

$SPECint^®\_rate2006 =$ 200

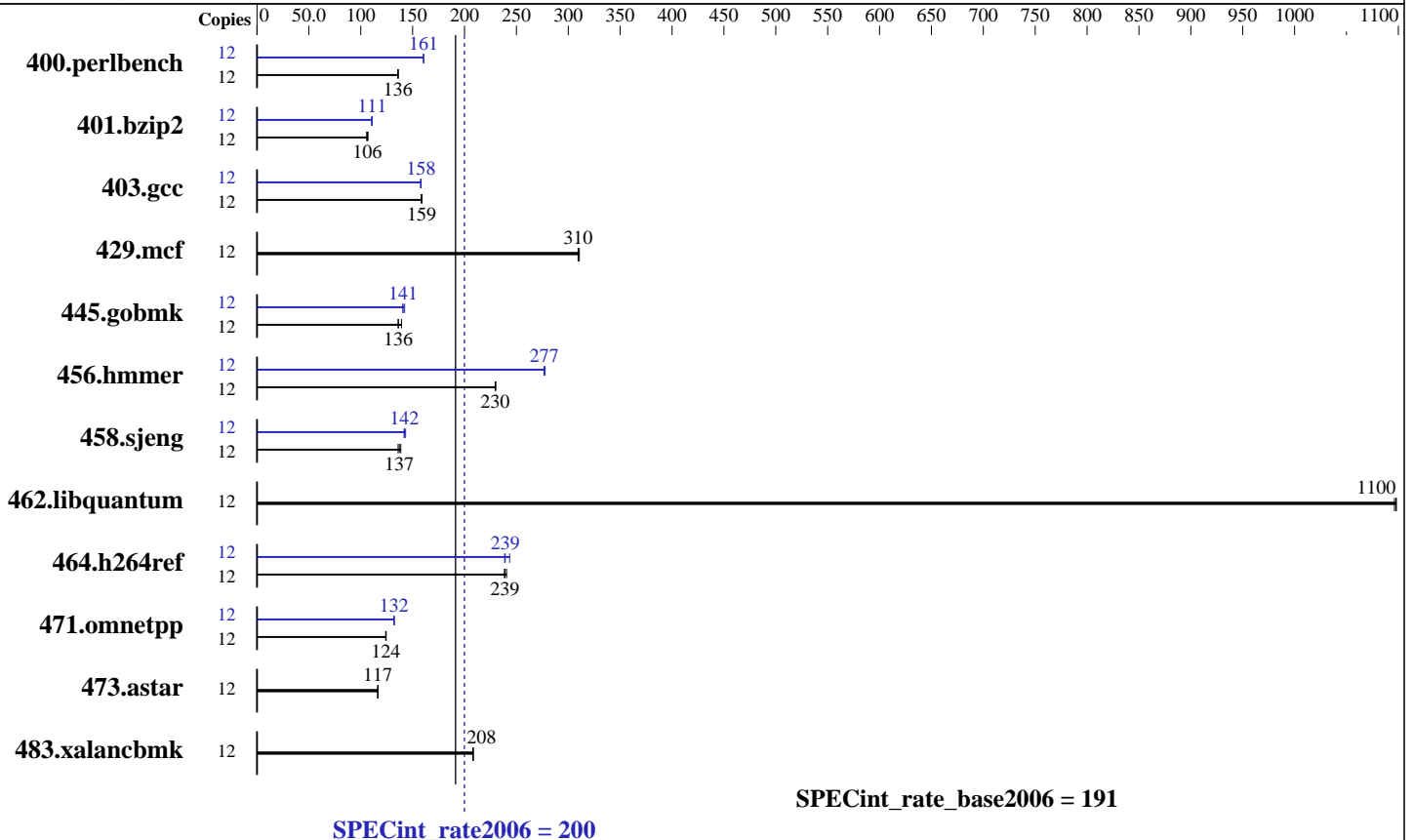$SPECint\_rate\_base2006 =$ 191

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Dec-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012

Benchmark	Copies	Result (peak)	Result (base)
400.perlbench	12	161	136
401.bzip2	12	111	106
403.gcc	12	158	159
429.mcf	12		310
445.gobmk	12	141	136
456.hmmer	12	277	230
458.sjeng	12	142	137
462.libquantum	12		1100
464.h264ref	12	239	239
471.omnetpp	12	132	124
473.astar	12		117
483.xalancbmk	12		208

SPECint_rate_base2006 = 191

SPECint_rate2006 = 200

Hardware
CPU Name: Intel Xeon E5-2620
CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 2 TB SATA, 7200 RPM
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	863	136	861	136	**863**	**136**	12	731	160	**730**	**161**	730	161
401.bzip2	12	**1089**	**106**	1096	106	1083	107	12	1042	111	**1042**	**111**	1050	110
403.gcc	12	**608**	**159**	608	159	610	158	12	611	158	**611**	**158**	613	158
429.mcf	12	354	309	352	311	**354**	**310**	12	354	309	352	311	**354**	**310**
445.gobmk	12	**924**	**136**	904	139	926	136	12	896	141	**895**	**141**	885	142
456.hmmer	12	**488**	**230**	486	230	488	229	12	405	277	**404**	**277**	403	278
458.sjeng	12	**1058**	**137**	1048	139	1068	136	12	**1023**	**142**	1024	142	1014	143
462.libquantum	12	226	1100	227	1100	**226**	**1100**	12	226	1100	227	1100	**226**	**1100**
464.h264ref	12	**1112**	**239**	1104	241	1113	239	12	1091	243	**1110**	**239**	1113	239
471.omnetpp	12	602	125	605	124	**604**	**124**	12	569	132	567	132	**567**	**132**
473.astar	12	725	116	723	117	**723**	**117**	12	725	116	723	117	**723**	**117**
483.xalancbmk	12	397	209	398	208	**398**	**208**	12	397	209	398	208	**398**	**208**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on SUT Fri Dec  7 09:38:11 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
      1 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
```

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Platform Notes (Continued)

```
     physical 0: cores 0 1 2 3 4 5
   cache size : 15360 KB

 From /proc/meminfo
   MemTotal:       132117212 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

 From /etc/*release* /etc/*version*
   SuSE-release:
     SUSE Linux Enterprise Server 11 (x86_64)
     VERSION = 11
     PATCHLEVEL = 2

 uname -a:
   Linux SUT 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012 (d73692b)
   x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 Dec 7 09:36 last=S

 SPEC is set to: /cpu2006.1.2
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda2       ext3  1.8T   69G  1.8T   4% /

 Additional information from dmidecode:

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
    icc   -m32

C++ benchmarks:
    icpc -m32

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc   -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -auto-ilp32

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32 -ansi-alias

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

        429.mcf: basepeak = yes

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                 -ansi-alias -opt-mem-layout-trans=3

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll4 -auto-ilp32

  462.libquantum: basepeak = yes

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/smartheap -lsmartheap

      473.astar: basepeak = yes

  483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 14:24:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 January 2013.