



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 542

ProLiant DL585 G7
(2.0 GHz AMD Opteron 6128 HE)

SPECint_rate_base2006 = 469

CPU2006 license: 3

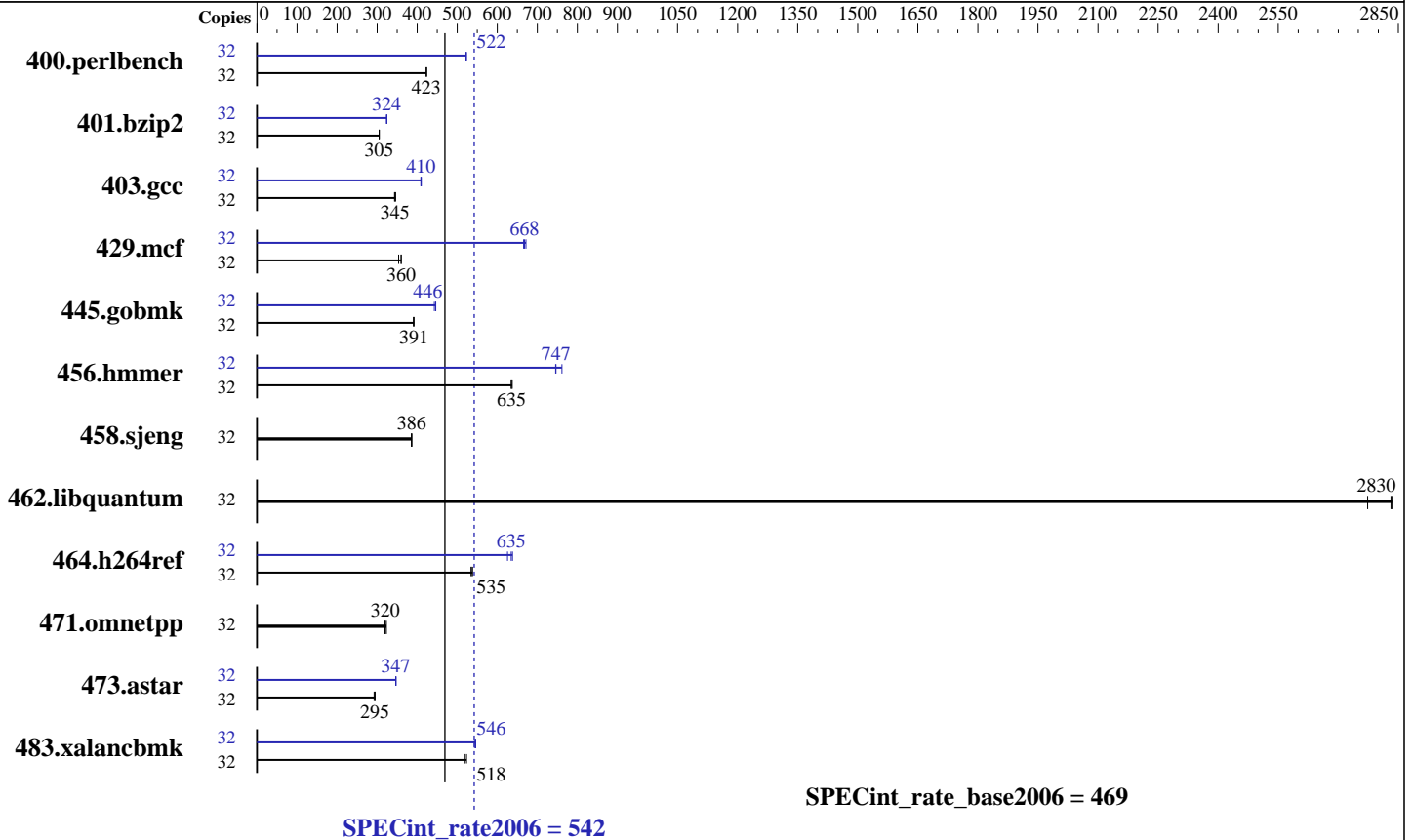
Test date: Oct-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2010

Tested by: Hewlett-Packard Company

Software Availability: Jun-2012



Hardware

CPU Name: AMD Opteron 6128 HE
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 2 x 146 GB 10 K SAS, RAID 1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.3, (Santiago)
 Kernel 2.6.32-279.el6.x86_64
 Compiler: C/C++: Version 4.2.5.2 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	739	423	<u>739</u>	<u>423</u>	739	423	32	599	522	598	523	<u>598</u>	<u>522</u>
401.bzip2	32	1013	305	<u>1012</u>	<u>305</u>	1012	305	32	953	324	<u>953</u>	<u>324</u>	955	324
403.gcc	32	744	346	<u>747</u>	<u>345</u>	750	343	32	630	409	<u>629</u>	<u>410</u>	628	410
429.mcf	32	825	354	811	360	<u>811</u>	<u>360</u>	32	435	672	<u>437</u>	<u>668</u>	438	666
445.gobmk	32	<u>858</u>	<u>391</u>	857	392	859	391	32	<u>752</u>	<u>446</u>	758	443	752	446
456.hammer	32	<u>470</u>	<u>635</u>	470	635	469	637	32	392	761	400	746	<u>400</u>	<u>747</u>
458.sjeng	32	<u>1003</u>	<u>386</u>	1002	386	1004	386	32	<u>1003</u>	<u>386</u>	1002	386	1004	386
462.libquantum	32	239	2770	234	2830	<u>234</u>	<u>2830</u>	32	239	2770	234	2830	<u>234</u>	<u>2830</u>
464.h264ref	32	<u>1324</u>	<u>535</u>	1316	538	1324	535	32	<u>1116</u>	<u>635</u>	1110	638	1132	626
471.omnetpp	32	619	323	<u>625</u>	<u>320</u>	625	320	32	619	323	<u>625</u>	<u>320</u>	625	320
473.astar	32	<u>763</u>	<u>295</u>	768	292	763	295	32	648	346	<u>648</u>	<u>347</u>	647	347
483.xalancbmk	32	426	518	<u>426</u>	<u>518</u>	422	523	32	407	543	404	546	<u>404</u>	<u>546</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
Set kernel/randomize_va_space=0 in /etc/sysctl.conf

Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

BIOS Configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling

Sysinfo program /cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on DL585Gen7 Thu Oct 18 19:20:22 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : AMD Opteron(tm) Processor 6128 HE
  4 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 8
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
    physical 2: cores 0 1 2 3
    physical 3: cores 0 1 2 3
  cache size : 512 KB

```

```

From /proc/meminfo
MemTotal:      264496304 kB
HugePages_Total:    28672
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.3 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux DL585Gen7 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 17 18:12

```

SPEC is set to: /cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3       ext4      133G  13G  114G  10% /

```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/cpu2006/amd1002mc-rate-libs-revC/64:/cpu2006/amd1002mc-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

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General Notes (Continued)

Binaries were compiled on a system with 2x AMD Opteron 6274 chips + 64GB Memory using RHEL 6.1

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:

-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalanbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
               -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
               -OPT:unroll_times_max=8 -OPT:unroll_size=256
               -OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
               -CG:local_sched_alg=1 -CG:unroll_fb_req=on
               -HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
            -OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
          -LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
          -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
          -CG:gcm=off -GRA:prioritize_by_density=on -m32
          -HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
            -OPT:unroll_times_max=8 -OPT:unroll_size=256
            -OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
            -IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
            -LNO:ignore_feedback=off -CG:p2align=on
            -CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
            -OPT:alias=disjoint -OPT:unroll_times_max=8
            -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
            -CG:local_sched_alg=1 -CG:cflow=0
            -CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
            -HP:bdt=2m:heap=2m

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Peak Optimization Flags (Continued)

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
-CG:push_pop_int_saved_regs=off

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -m32
-HP:bdt=2m:heap=2m

483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
-fno-emit-exceptions
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-I.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20120703.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-I.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20120703.xml>

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