



# SPEC® CFP2006 Result

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## NEC Corporation

### SPECfp®\_rate2006 = 102

### Express5800/E120d-1 (Intel Xeon E5-2407)

### SPECfp\_rate\_base2006 = 99.0

CPU2006 license: 9006

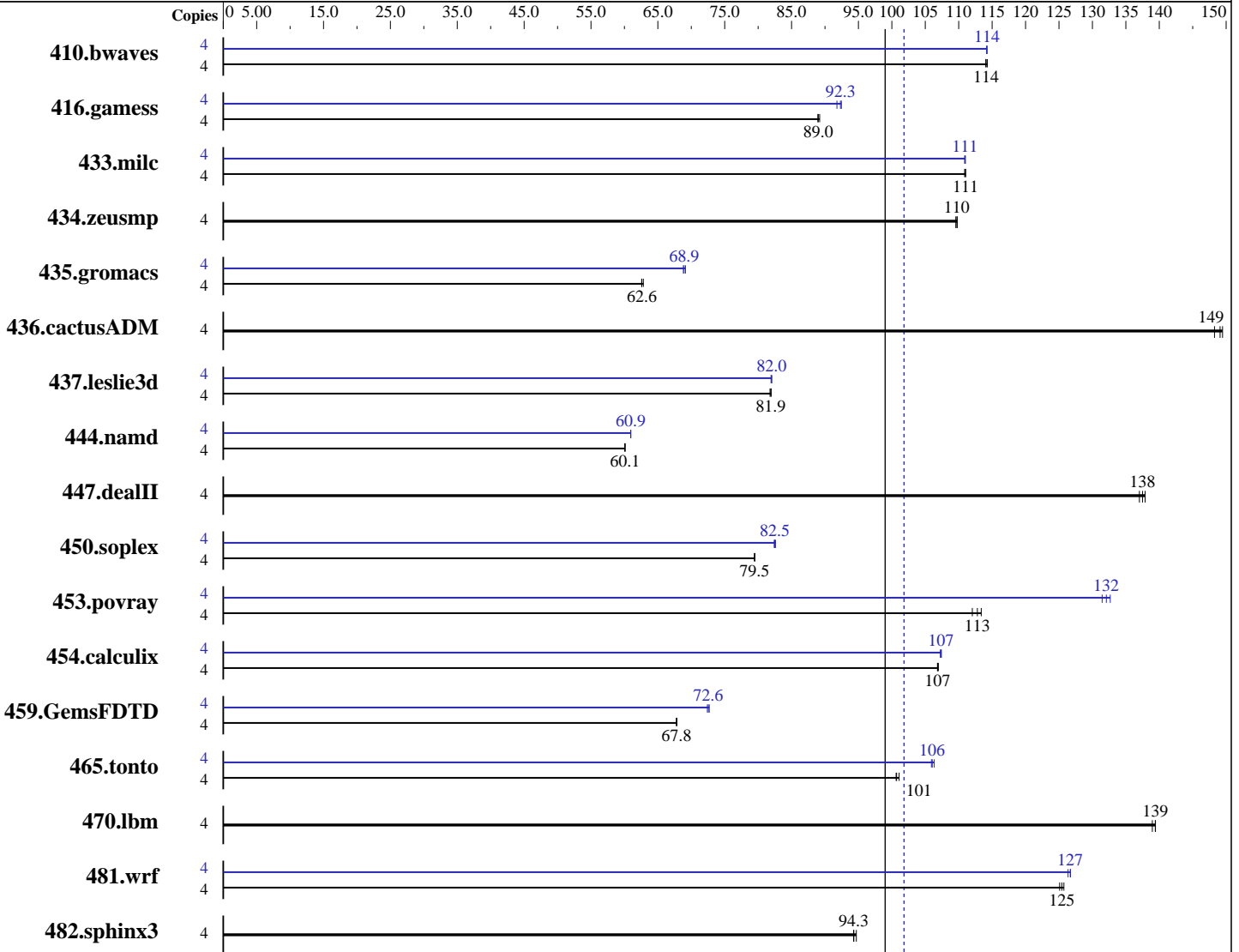
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012



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#### Hardware

CPU Name: Intel Xeon E5-2407  
 CPU Characteristics:  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.3.293 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (3 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1066 MHz and CL7)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	476	114	<b>476</b>	<b>114</b>	477	114	4	<b>476</b>	<b>114</b>	476	114	476	114		
416.gamess	4	878	89.2	881	88.9	<b>880</b>	<b>89.0</b>	4	<b>848</b>	<b>92.3</b>	853	91.8	847	92.5		
433.milc	4	331	111	<b>331</b>	<b>111</b>	331	111	4	<b>331</b>	<b>111</b>	331	111	331	111		
434.zeusmp	4	332	110	<b>332</b>	<b>110</b>	332	110	4	332	110	<b>332</b>	<b>110</b>	332	110		
435.gromacs	4	455	62.8	456	62.6	<b>456</b>	<b>62.6</b>	4	<b>414</b>	<b>68.9</b>	415	68.8	413	69.1		
436.cactusADM	4	<b>321</b>	<b>149</b>	322	148	320	149	4	<b>321</b>	<b>149</b>	322	148	320	149		
437.leslie3d	4	459	82.0	<b>459</b>	<b>81.9</b>	460	81.8	4	458	82.0	459	81.9	<b>458</b>	<b>82.0</b>		
444.namd	4	534	60.1	534	60.1	<b>534</b>	<b>60.1</b>	4	<b>526</b>	<b>60.9</b>	526	61.0	526	60.9		
447.dealII	4	<b>333</b>	<b>138</b>	334	137	332	138	4	<b>333</b>	<b>138</b>	334	137	332	138		
450.soplex	4	420	79.4	<b>420</b>	<b>79.5</b>	420	79.5	4	405	82.4	<b>404</b>	<b>82.5</b>	404	82.6		
453.povray	4	190	112	<b>189</b>	<b>113</b>	188	113	4	160	133	<b>161</b>	<b>132</b>	162	131		
454.calculix	4	309	107	<b>309</b>	<b>107</b>	309	107	4	308	107	307	107	<b>307</b>	<b>107</b>		
459.GemsFDTD	4	<b>626</b>	<b>67.8</b>	626	67.8	626	67.8	4	584	72.7	<b>585</b>	<b>72.6</b>	586	72.4		
465.tonto	4	389	101	391	101	<b>391</b>	<b>101</b>	4	370	106	372	106	<b>371</b>	<b>106</b>		
470.lbm	4	394	139	396	139	<b>394</b>	<b>139</b>	4	394	139	396	139	<b>394</b>	<b>139</b>		
481.wrf	4	<b>356</b>	<b>125</b>	357	125	355	126	4	354	126	353	127	<b>353</b>	<b>127</b>		
482.sphinx3	4	<b>827</b>	<b>94.3</b>	827	94.3	823	94.7	4	<b>827</b>	<b>94.3</b>	827	94.3	823	94.7		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Added glibc-static-2.12-1.47.el6.x86\_64.rpm  
to enable static linking

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-p32 -ansi-alias -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 12:52:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 October 2012.