



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®2006 = 71.6

ProLiant BL460c Gen8
(2.30 GHz, Intel Xeon E5-2630)

SPECfp_base2006 = 68.1

CPU2006 license: 3

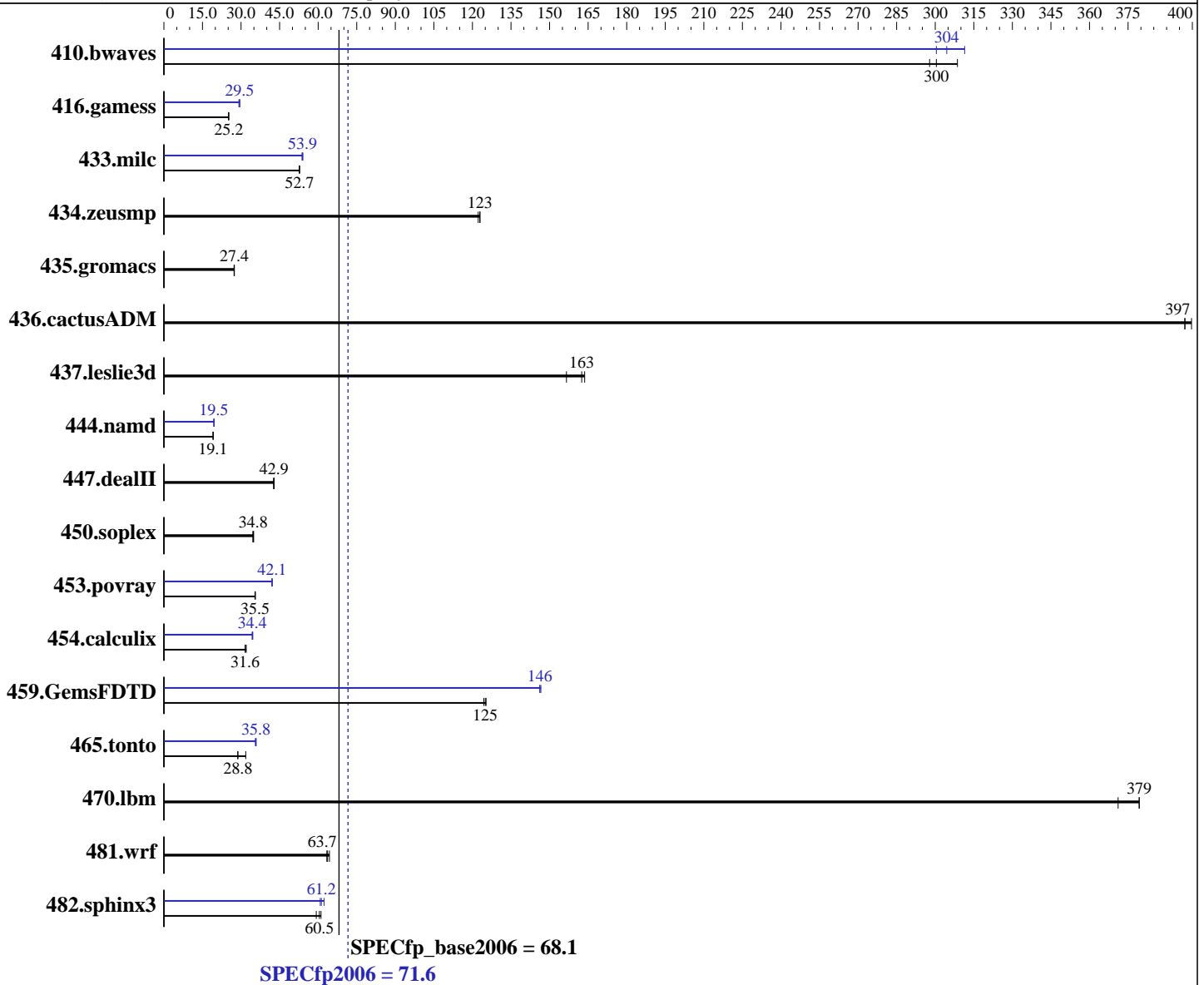
Test date: Sep-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Oct-2012

Tested by: Hewlett-Packard Company

Software Availability: Mar-2012



Hardware

CPU Name: Intel Xeon E5-2630
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.2, (Santiago)
 Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 300 GB 15 K SAS
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: HP Array Configuration Utility, CLI version

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	45.2	300	45.6	298	44.0	309	43.6	311	45.2	300	44.6	304
416.gamess	779	25.1	777	25.2	777	25.2	671	29.2	664	29.5	664	29.5
433.milc	174	52.7	174	52.7	174	52.7	170	53.9	171	53.7	170	54.0
434.zeusmp	74.4	122	74.0	123	74.0	123	74.4	122	74.0	123	74.0	123
435.gromacs	262	27.3	261	27.4	261	27.4	262	27.3	261	27.4	261	27.4
436.cactusADM	29.9	400	30.1	397	30.1	397	29.9	400	30.1	397	30.1	397
437.leslie3d	60.0	157	57.4	164	57.8	163	60.0	157	57.4	164	57.8	163
444.namd	419	19.1	419	19.2	419	19.1	412	19.5	412	19.5	412	19.5
447.dealII	267	42.9	268	42.6	267	42.9	267	42.9	268	42.6	267	42.9
450.soplex	239	34.9	240	34.8	241	34.6	239	34.9	240	34.8	241	34.6
453.povray	150	35.4	150	35.5	150	35.5	126	42.1	126	42.1	127	42.1
454.calculix	258	32.0	261	31.6	261	31.6	239	34.6	240	34.4	240	34.4
459.GemsFDTD	85.3	124	84.9	125	84.7	125	72.4	147	72.6	146	72.6	146
465.tonto	342	28.8	309	31.8	342	28.7	274	35.9	275	35.8	277	35.6
470.lbm	37.0	371	36.2	379	36.2	379	37.0	371	36.2	379	36.2	379
481.wrf	173	64.4	176	63.4	175	63.7	173	64.4	176	63.4	175	63.7
482.sphinx3	319	61.1	322	60.5	329	59.3	313	62.3	318	61.2	321	60.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
runspec command invoked through numactl i.e.:
numactl --localalloc runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility,
CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write
in HP Array Configuration Utility, CLI version
```



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Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
 Minimum Processor Idle Power Core State set to C6
 Minimum Processor Idle Power Package State set to C6 (non-retention)
 Energy/Performance Bias is set to Maximum Performance
 Memory Power Savings Mode set to Maximum Performance
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
 Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on BL460cGen8 Wed Sep 5 17:46:39 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 6
  siblings     : 12
  physical 0   : cores 0 1 2 3 4 5
  physical 1   : cores 0 1 2 3 4 5
cache size    : 15360 KB
```

From /proc/meminfo

```
MemTotal:      132119940 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb_release -d

Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux BL460cGen8 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 5 16:28

SPEC is set to: /cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
Continued on next page						



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Platform Notes (Continued)

/dev/sda3 ext4 273G 8.3G 251G 4% /

Additional information from dmidecode:

BIOS HP I31 08/12/2012

Memory:

16x HP Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml>

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