



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®2006 = 48.4

ProLiant DL380p Gen8
(2.40 GHz, Intel Xeon E5-2609)

SPECfp_base2006 = 47.0

CPU2006 license: 3

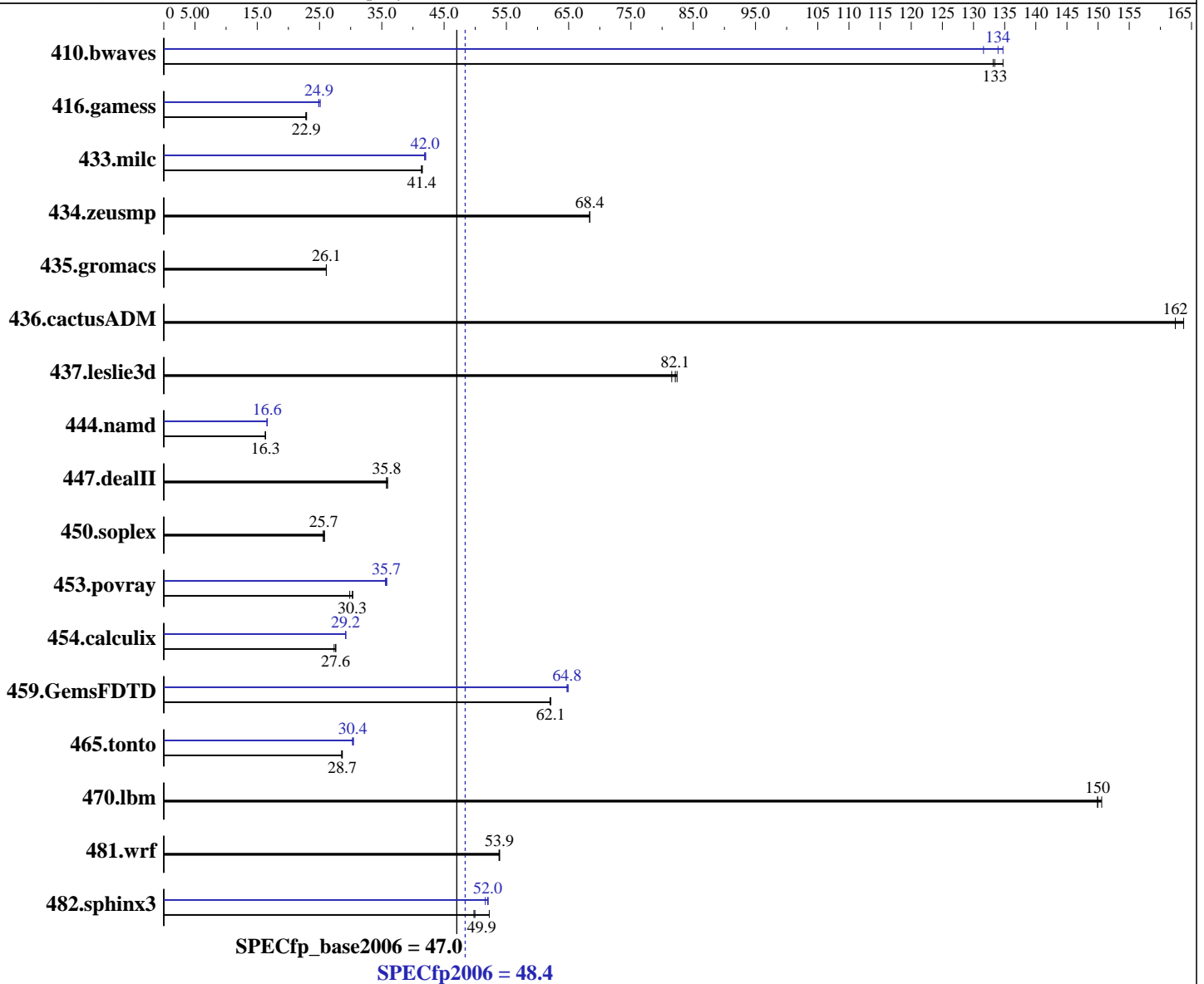
Test date: May-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Oct-2012

Tested by: Hewlett-Packard Company

Software Availability: Mar-2012



Hardware

CPU Name: Intel Xeon E5-2609
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2, (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 1 x 146 GB 15 K SAS
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: HP Array Configuration Utility, CLI

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	102	133	102	133	101	135	103	132	101	134	101	135
416.gamess	854	22.9	855	22.9	859	22.8	787	24.9	779	25.1	786	24.9
433.milc	221	41.5	222	41.4	222	41.4	218	42.1	219	42.0	219	41.9
434.zeusmp	133	68.4	133	68.4	133	68.4	133	68.4	133	68.4	133	68.4
435.gromacs	274	26.1	274	26.1	274	26.1	274	26.1	274	26.1	274	26.1
436.cactusADM	73.6	162	73.6	162	73.0	164	73.6	162	73.6	162	73.0	164
437.leslie3d	114	82.1	114	82.4	115	81.5	114	82.1	114	82.4	115	81.5
444.namd	491	16.3	492	16.3	492	16.3	484	16.6	484	16.6	483	16.6
447.dealII	320	35.8	318	35.9	320	35.8	320	35.8	318	35.9	320	35.8
450.soplex	326	25.6	323	25.8	325	25.7	326	25.6	323	25.8	325	25.7
453.povray	176	30.3	175	30.3	178	29.9	149	35.8	149	35.7	150	35.6
454.calculix	302	27.3	299	27.6	299	27.6	283	29.2	283	29.2	282	29.2
459.GemsFDTD	171	62.0	171	62.1	171	62.1	164	64.8	163	64.9	164	64.8
465.tonto	345	28.5	343	28.7	343	28.7	323	30.5	324	30.4	325	30.3
470.lbm	91.6	150	91.2	151	91.6	150	91.6	150	91.2	151	91.6	150
481.wrf	208	53.8	207	53.9	207	53.9	208	53.8	207	53.9	207	53.9
482.sphinx3	390	49.9	391	49.8	373	52.3	378	51.6	375	52.0	374	52.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility,
CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write
in HP Array Configuration Utility, CLI version
```



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Platform Notes

BIOS Configuration:

HP Power Profile set to Custom
Energy/Performance Bias is set to Maximum Performance
Thermal Configuration set to Maximum Cooling

Sysinfo program /cpu2006/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Thu May 31 05:00:16 2012

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
 cache size : 10240 KB
```

From /proc/meminfo

```
MemTotal:      132119996 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 30 20:26

SPEC is set to: /cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        ext4      133G  9.6G  117G   8% /
```

Additional information from dmidecode:

```
BIOS HP P70 07/15/2012
Memory:
```

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Platform Notes (Continued)

16x HP Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"
OMP_NUM_THREADS = "4"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml>

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