



# SPEC® CINT2006 Result

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## IBM Corporation

IBM Flex System x440  
(Intel Xeon E5-4610, 2.40 GHz)

SPECint®2006 = 45.1

SPECint\_base2006 = 42.3

CPU2006 license: 11

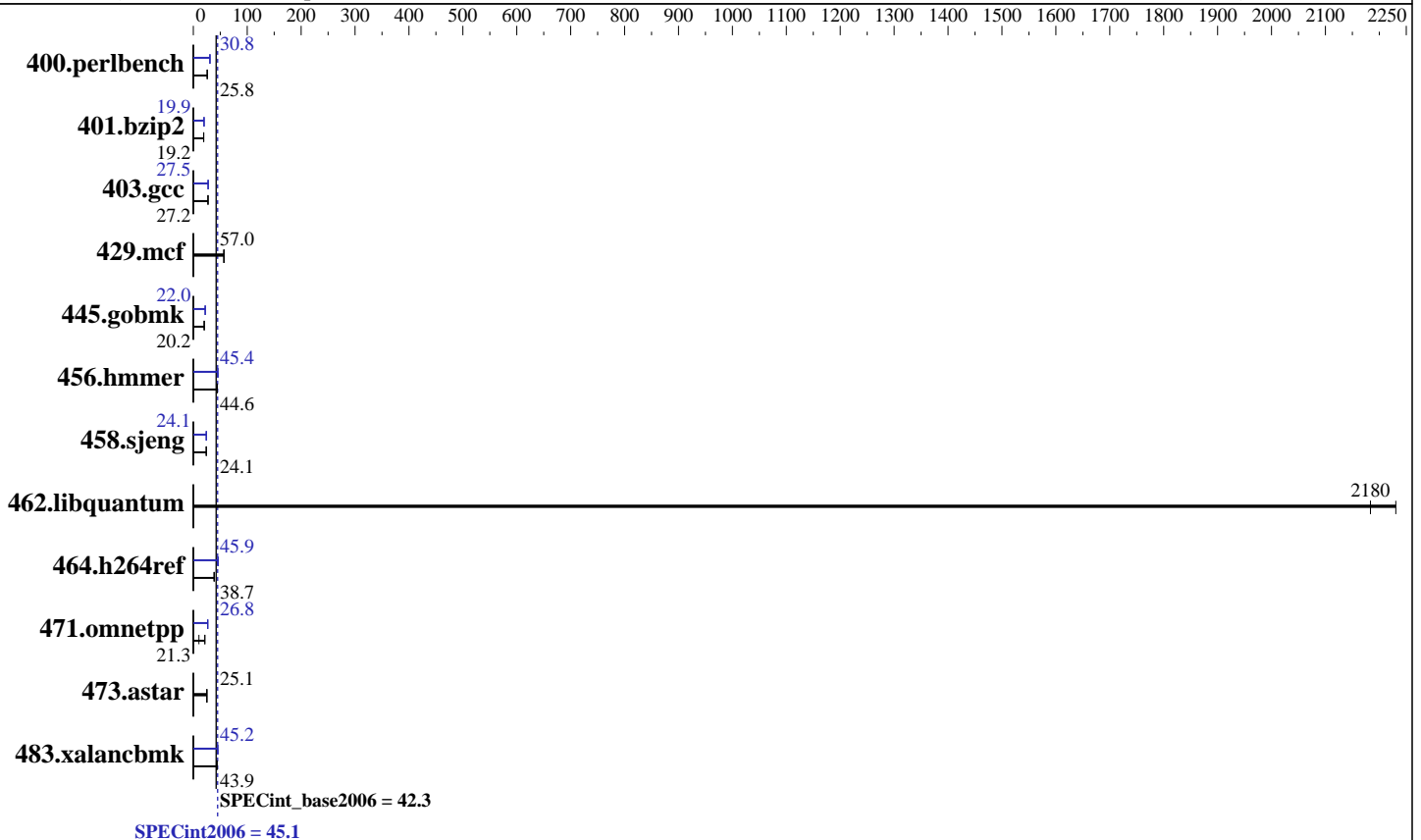
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Aug-2012

Hardware Availability: Aug-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-4610  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>378</b>	<b>25.8</b>	379	25.8	378	25.8	316	30.9	<b>317</b>	<b>30.8</b>	318	30.8
401.bzip2	502	19.2	502	19.2	<b>502</b>	<b>19.2</b>	485	19.9	486	19.9	<b>485</b>	<b>19.9</b>
403.gcc	296	27.2	<b>296</b>	<b>27.2</b>	296	27.2	293	27.5	<b>293</b>	<b>27.5</b>	293	27.5
429.mcf	160	57.1	<b>160</b>	<b>57.0</b>	162	56.3	160	57.1	<b>160</b>	<b>57.0</b>	162	56.3
445.gobmk	<b>519</b>	<b>20.2</b>	518	20.2	519	20.2	476	22.1	<b>476</b>	<b>22.0</b>	476	22.0
456.hammer	<b>209</b>	<b>44.6</b>	210	44.4	209	44.6	206	45.4	<b>205</b>	<b>45.4</b>	205	45.5
458.sjeng	<b>503</b>	<b>24.1</b>	503	24.0	503	24.1	502	24.1	503	24.1	<b>503</b>	<b>24.1</b>
462.libquantum	9.49	2180	<b>9.49</b>	<b>2180</b>	9.29	2230	9.49	2180	<b>9.49</b>	<b>2180</b>	9.29	2230
464.h264ref	572	38.7	572	38.7	<b>572</b>	<b>38.7</b>	481	46.0	<b>482</b>	<b>45.9</b>	491	45.0
471.omnetpp	600	10.4	<b>293</b>	<b>21.3</b>	292	21.4	234	26.7	<b>234</b>	<b>26.8</b>	233	26.8
473.astar	<b>280</b>	<b>25.1</b>	278	25.2	281	25.0	<b>280</b>	<b>25.1</b>	278	25.2	281	25.0
483.xalancbmk	158	43.8	157	43.9	<b>157</b>	<b>43.9</b>	<b>153</b>	<b>45.2</b>	153	45.2	151	45.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on congo-pete Sun Aug 19 01:57:02 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4610 0 @ 2.40GHz
4 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
physical 2: cores 0 1 2 3 4 5
physical 3: cores 0 1 2 3 4 5
cache size : 15360 KB
```

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## Platform Notes (Continued)

```

From /proc/meminfo
MemTotal:      264503640 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux congo-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 17 13:21

SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_congopete-lv_root
                ext4      264G  5.9G  245G   3% /

Additional information from dmidecode:
Memory:
32x Micron 36JSF1G72PZ-1G6M1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,scatter"  
 LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"  
 OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches

## Base Compiler Invocation

C benchmarks:  
 icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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