



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 38.9

ProLiant BL460c Gen8
(2.00 GHz, Intel Xeon E5-2620)

SPECint_base2006 = 36.6

CPU2006 license: 3

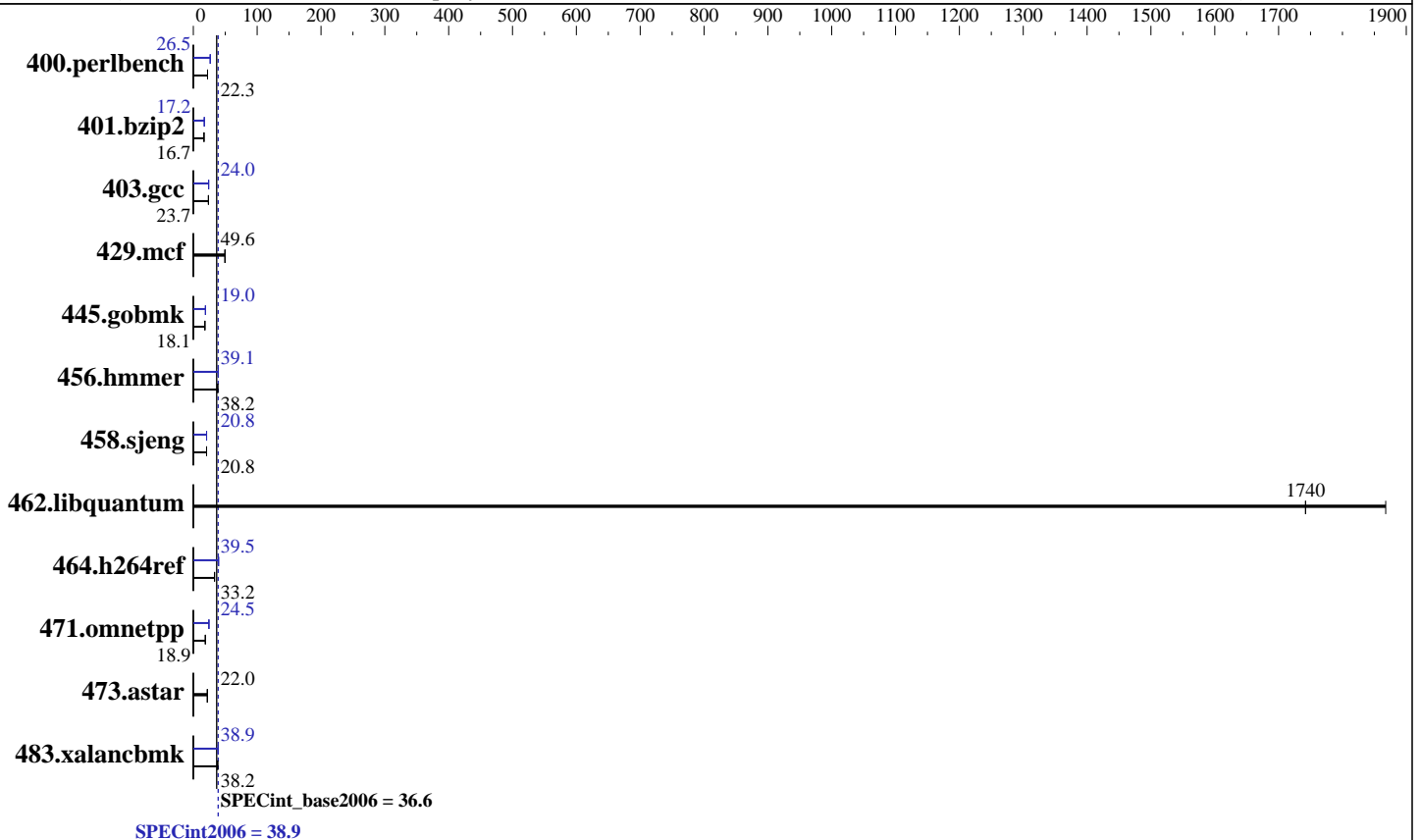
Test date: Jul-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2012

Tested by: Hewlett-Packard Company

Software Availability: Mar-2012



Hardware

CPU Name: Intel Xeon E5-2620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 2 x 146 GB 15 K SAS, RAID 1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2, (Santiago)
 Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01
 HP Array Configuration Utility, CLI version



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	439	22.3	438	22.3	439	22.3	369	26.5	369	26.5	368	26.5
401.bzip2	578	16.7	577	16.7	578	16.7	560	17.2	561	17.2	559	17.2
403.gcc	340	23.7	340	23.7	340	23.7	335	24.0	336	24.0	335	24.0
429.mcf	183	49.9	185	49.4	184	49.6	183	49.9	185	49.4	184	49.6
445.gobmk	579	18.1	578	18.1	579	18.1	552	19.0	552	19.0	552	19.0
456.hammer	245	38.2	244	38.2	244	38.2	238	39.2	238	39.1	238	39.1
458.sjeng	583	20.8	583	20.8	582	20.8	582	20.8	582	20.8	582	20.8
462.libquantum	11.9	1740	11.9	1740	11.1	1870	11.9	1740	11.9	1740	11.1	1870
464.h264ref	666	33.2	665	33.3	668	33.1	558	39.7	561	39.5	563	39.3
471.omnetpp	331	18.9	331	18.9	332	18.8	255	24.5	255	24.5	258	24.2
473.astar	318	22.1	320	22.0	320	22.0	318	22.1	320	22.0	320	22.0
483.xalancbmk	181	38.0	181	38.2	180	38.2	177	38.9	177	38.9	178	38.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --localalloc runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version
Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version
```

Platform Notes

```
BIOS Configuration:
HP Power Profile set to Custom
Intel Hyperthreading set to Disabled
Minimum Processor Idle Power Core set to C6
Minimum Processor Idle Power Package set to C6 (non-retention)
Energy/Performance Bias is set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Sysinfo program /cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on BL460cGen8-1 Tue Jul 31 14:48:19 2012
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
    2 "physical id"s (chips)
    12 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 6
    siblings  : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
  cache size : 15360 KB

```

```

From /proc/meminfo
  MemTotal:      132120004 kB
  HugePages_Total:      0
  Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.2 (Santiago)

```

```

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
  Linux BL460cGen8-1 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
  x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 31 13:18

```

SPEC is set to: /cpu2006
  Filesystem      Type      Size Used Avail Use% Mounted on
  /dev/sda3       ext4     133G  20G  107G  16% /

```

```

Additional information from dmidecode:
  BIOS HP I31 02/25/2012
  Memory:
    16x Not Specified Not Specified 8 GB 1600 MHz 2 rank

```

(End of data from sysinfo program)

General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64"
OMP_NUM_THREADS = "12"

```

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

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Peak Optimization Flags (Continued)

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120829.xml>



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