



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

SPECfp<sup>®</sup>2006 = **58.2**

NovaScale T820 F3 (Intel Xeon E5-1410, 2.80 GHz)

SPECfp\_base2006 = **54.5**

CPU2006 license: 20

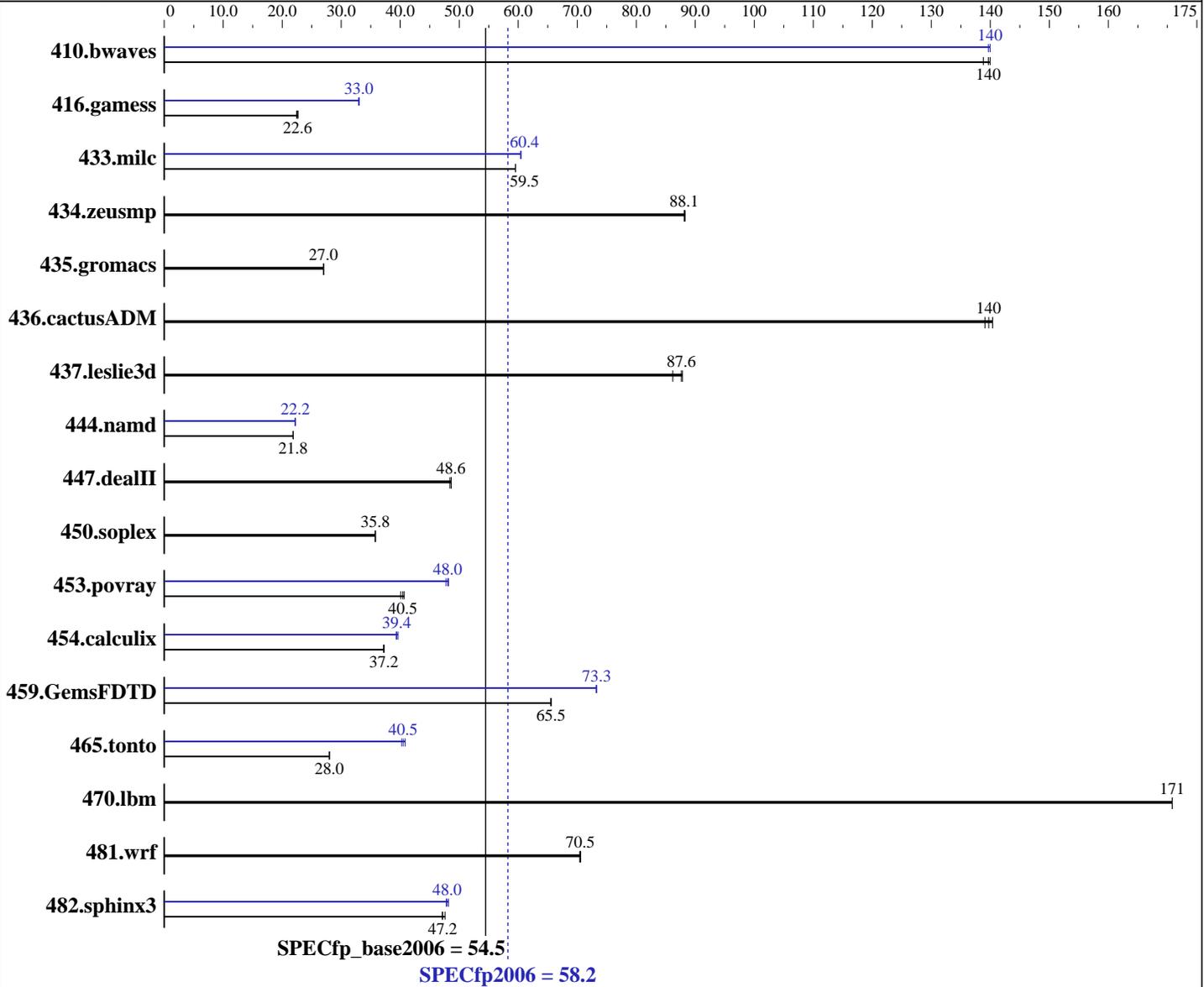
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Jun-2012

Hardware Availability: Jun-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-1410  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2(x86\_64)  
 3.0.13-0.27-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	97.9	139	97.1	140	<b>97.3</b>	<b>140</b>	97.3	140	97.1	140	<b>97.1</b>	<b>140</b>
416.gamess	864	22.7	<b>866</b>	<b>22.6</b>	873	22.4	594	33.0	593	33.0	<b>593</b>	<b>33.0</b>
433.milc	154	59.5	154	59.5	<b>154</b>	<b>59.5</b>	152	60.4	<b>152</b>	<b>60.4</b>	152	60.4
434.zeusmp	103	88.1	103	88.3	<b>103</b>	<b>88.1</b>	103	88.1	103	88.3	<b>103</b>	<b>88.1</b>
435.gromacs	265	27.0	<b>264</b>	<b>27.0</b>	264	27.0	265	27.0	<b>264</b>	<b>27.0</b>	264	27.0
436.cactusADM	85.9	139	<b>85.5</b>	<b>140</b>	85.1	140	85.9	139	<b>85.5</b>	<b>140</b>	85.1	140
437.leslie3d	107	87.8	<b>107</b>	<b>87.6</b>	109	86.2	107	87.8	<b>107</b>	<b>87.6</b>	109	86.2
444.namd	367	21.8	367	21.9	<b>367</b>	<b>21.8</b>	361	22.2	<b>361</b>	<b>22.2</b>	362	22.2
447.dealII	236	48.4	235	48.6	<b>235</b>	<b>48.6</b>	236	48.4	235	48.6	<b>235</b>	<b>48.6</b>
450.soplex	<b>233</b>	<b>35.8</b>	233	35.7	233	35.8	<b>233</b>	<b>35.8</b>	233	35.7	233	35.8
453.povray	131	40.7	<b>132</b>	<b>40.5</b>	133	40.1	111	47.7	110	48.2	<b>111</b>	<b>48.0</b>
454.calculix	<b>222</b>	<b>37.2</b>	221	37.3	222	37.2	<b>209</b>	<b>39.4</b>	210	39.3	208	39.6
459.GemsFDTD	162	65.5	<b>162</b>	<b>65.5</b>	162	65.5	<b>145</b>	<b>73.3</b>	145	73.3	145	73.2
465.tonto	352	27.9	<b>352</b>	<b>28.0</b>	351	28.0	241	40.8	245	40.2	<b>243</b>	<b>40.5</b>
470.lbm	80.4	171	<b>80.4</b>	<b>171</b>	80.4	171	80.4	171	<b>80.4</b>	<b>171</b>	80.4	171
481.wrf	159	70.4	<b>158</b>	<b>70.5</b>	158	70.6	159	70.4	<b>158</b>	<b>70.5</b>	158	70.6
482.sphinx3	414	47.1	<b>413</b>	<b>47.2</b>	410	47.6	405	48.2	408	47.8	<b>406</b>	<b>48.0</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled

Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on linux-sxkz Sun Jun 10 00:50:08 2012

Continued on next page



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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-1410 0 @ 2.80GHz
1 "physical id"s (chips)
8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
cache size : 10240 KB
```

From /proc/meminfo

```
MemTotal: 49348896 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 11 (x86_64)
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

uname -a:

```
Linux linux-sxkz 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 7 18:57 last=S

SPEC is set to: /root/CPU2006-1.2

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext3 271G 40G 218G 16% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "8"
```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages disabled with:  
 echo never > /sys/kernel/mm/transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches  
 The Dell PowerEdge T320 and the Bull NovaScale T820 F3 models are electronically equivalent.  
 The results have been measured on a Dell PowerEdge T320 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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