



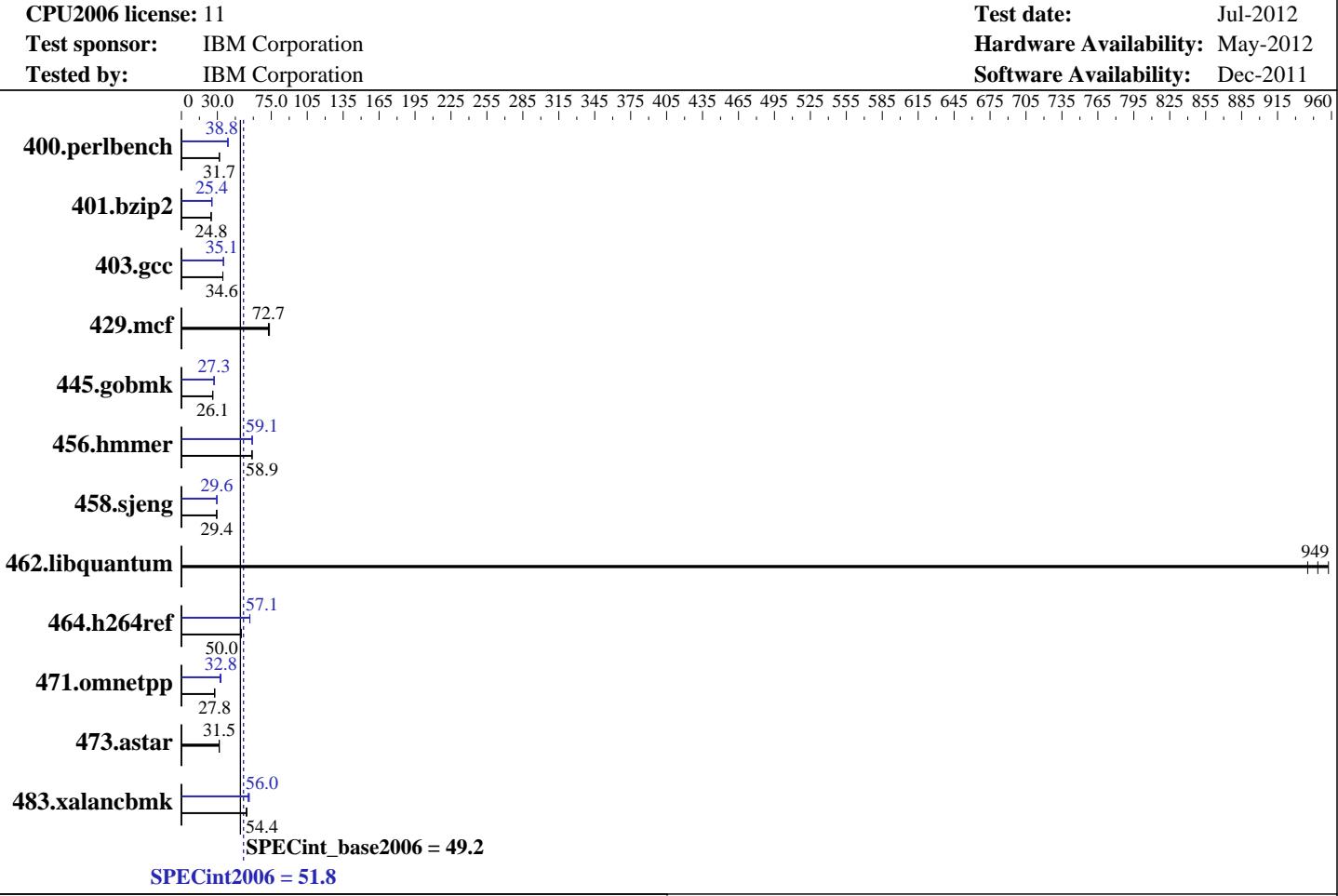
SPEC® CINT2006 Result

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IBM Corporation

IBM System x3100 M4 (Intel Xeon E3-1220 v2)

SPECint®2006 = 51.8



Hardware

CPU Name:	Intel Xeon E3-1220 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



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Software Availability: Dec-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	308	31.7	309	31.6	<u>309</u>	<u>31.7</u>	251	38.9	<u>252</u>	<u>38.8</u>	252	38.8
401.bzip2	389	24.8	<u>389</u>	<u>24.8</u>	389	24.8	379	25.5	<u>380</u>	<u>25.4</u>	381	25.4
403.gcc	<u>233</u>	<u>34.6</u>	233	34.5	233	34.6	<u>229</u>	<u>35.1</u>	229	35.1	230	35.0
429.mcf	<u>125</u>	<u>72.7</u>	124	73.5	126	72.6	<u>125</u>	<u>72.7</u>	124	73.5	126	72.6
445.gobmk	<u>401</u>	<u>26.1</u>	401	26.1	401	26.1	385	27.3	<u>385</u>	<u>27.3</u>	385	27.3
456.hmmer	<u>158</u>	<u>58.9</u>	158	58.9	158	58.9	158	59.1	158	59.0	<u>158</u>	<u>59.1</u>
458.sjeng	411	29.4	411	29.4	<u>411</u>	<u>29.4</u>	409	29.6	<u>409</u>	<u>29.6</u>	409	29.6
462.libquantum	<u>21.8</u>	<u>949</u>	21.6	957	22.0	940	<u>21.8</u>	<u>949</u>	21.6	957	22.0	940
464.h264ref	443	50.0	442	50.0	<u>443</u>	<u>50.0</u>	387	57.1	388	57.1	<u>388</u>	<u>57.1</u>
471.omnetpp	<u>225</u>	<u>27.8</u>	225	27.8	226	27.7	194	32.3	<u>191</u>	<u>32.8</u>	190	32.9
473.astar	223	31.5	<u>223</u>	<u>31.5</u>	222	31.6	223	31.5	<u>223</u>	<u>31.5</u>	222	31.6
483.xalancbmk	128	54.1	126	54.6	<u>127</u>	<u>54.4</u>	122	56.6	<u>123</u>	<u>56.0</u>	124	55.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Turbo Mode enabled in BIOS

C-State enabled in BIOS

Sysinfo program /root/SPECcpu1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date::: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on localhost.localdomain Fri Jul 6 15:47:53 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1220 V2 @ 3.10GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

physical 0: cores 0 1 2 3

cache size : 8192 KB

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Platform Notes (Continued)

```
From /proc/meminfo
  MemTotal:       16322724 kB
  HugePages_Total:      0
  Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
  Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
  EST 2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 5 11:15
```

```
SPEC is set to: /root/SPECcpul.2
  Filesystem      Type  Size  Used Avail Use% Mounted on
  /dev/mapper/VolGroup-lv_root
                ext4   50G   38G   9.1G  81%  /
```

```
Additional information from dmidecode:
```

```
  Memory:
    2x Micron 18JSF1G72AZ-1G6D1 8 GB 1600 MHz 2 rank
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
  icc -m64
```

C++ benchmarks:

```
  icpc -m64
```



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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hammer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smarterheap -lsmarterheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
   403.gcc: -DSPEC_CPU_LP64
   429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
               -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
               -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
               -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-ra-region-strategy=block           -ansi-alias
               -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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