SPEC® CFP2006 Result
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Dell Inc.

PowerEdge M820 (Intel Xeon E5-4610, 2.40 GHz)

$SPECfp^{®}\_rate2006 = \quad 674$

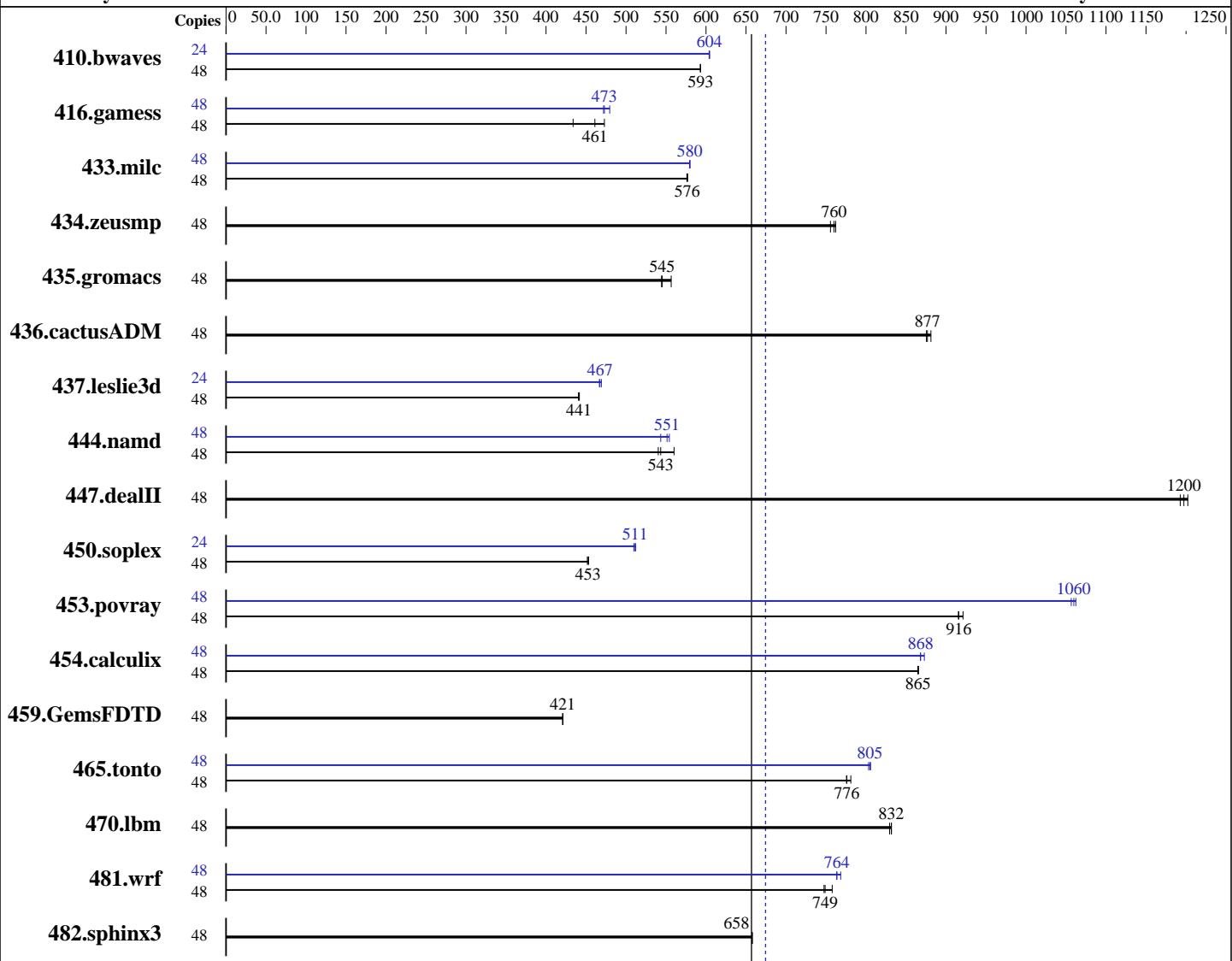$SPECfp\_rate\_base2006 = \quad 657$

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2012
Hardware Availability: Aug-2012
Software Availability: Feb-2012

Copies

Benchmark		
410.bwaves	24	604
	48	593
416.gamess	48	473
	48	461
433.milc	48	580
	48	576
434.zeusmp	48	760
435.gromacs	48	545
436.cactusADM	48	877
437.leslie3d	24	467
	48	441
444.namd	48	551
	48	543
447.dealII	48	1200
450.soplex	24	511
	48	453
453.povray	48	1060
	48	916
454.calculix	48	868
	48	865
459.GemsFDTD	48	421
465.tonto	48	805
	48	776
470.lbm	48	832
481.wrf	48	764
	48	749
482.sphinx3	48	658

SPECfp_rate_base2006 = 657

SPECfp_rate2006 = 674

Hardware

CPU Name: Intel Xeon E5-4610
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
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L3 Cache:	15 MB I+D on chip per chip		Base Pointers:	32/64-bit
Other Cache:	None		Peak Pointers:	32/64-bit
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)		Other Software:	None
Disk Subsystem:	1 x 900 GB 10000 RPM SAS			
Other Hardware:	None			

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	**1100**	**593**	1100	593	1100	593	24	**540**	**604**	540	604	540	604
416.gamess	48	1987	473	**2038**	**461**	2166	434	48	1992	472	1960	480	**1987**	**473**
433.milc	48	**765**	**576**	764	577	765	576	48	760	580	**760**	**580**	760	579
434.zeusmp	48	573	762	**575**	**760**	578	756	48	573	762	**575**	**760**	578	756
435.gromacs	48	616	556	630	544	**629**	**545**	48	616	556	630	544	**629**	**545**
436.cactusADM	48	651	881	655	875	**654**	**877**	48	651	881	655	875	**654**	**877**
437.leslie3d	48	**1023**	**441**	1022	442	1024	441	24	483	467	**483**	**467**	481	469
444.namd	48	687	560	712	540	**709**	**543**	48	695	554	708	543	**698**	**551**
447.dealII	48	**459**	**1200**	460	1190	457	1200	48	**459**	**1200**	460	1190	457	1200
450.soplex	48	883	453	886	452	**885**	**453**	24	393	510	391	512	**391**	**511**
453.povray	48	277	921	279	915	**279**	**916**	48	**241**	**1060**	240	1060	242	1060
454.calculix	48	**458**	**865**	457	866	458	865	48	454	873	456	868	**456**	**868**
459.GemsFDTD	48	1210	421	1209	421	**1210**	**421**	48	1210	421	1209	421	**1210**	**421**
465.tonto	48	**609**	**776**	605	781	609	776	48	586	806	588	803	**587**	**805**
470.lbm	48	**793**	**832**	795	829	793	832	48	**793**	**832**	795	829	793	832
481.wrf	48	717	748	708	758	**716**	**749**	48	698	768	702	764	**702**	**764**
482.sphinx3	48	1422	658	**1422**	**658**	1423	658	48	1422	658	**1422**	**658**	1423	658

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

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Platform Notes

```
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-b61l Fri May 18 23:28:46 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4610 0 @ 2.40GHz
      4 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
      physical 2: cores 0 1 2 3 4 5
      physical 3: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       264468688 kB
   HugePages_Total:        0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 2

uname -a:
   Linux linux-b61l 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
   (d73692b) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 17 11:33 last=5

SPEC is set to: /root/CPU2006-1.2
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda1       ext3  911G   52G  813G   7% /

Additional information from dmidecode:
```
Continued on next page

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Platform Notes (Continued)

```
(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
       icc   -m64

C++ benchmarks:
       icpc   -m64

Fortran benchmarks:
       ifort -m64

Benchmarks using both Fortran and C:
       icc   -m64 ifort -m64

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```
```
  450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
   433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
            -opt-mem-layout-trans=3

    470.lbm: basepeak = yes

 482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
   444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
            -auto-ilp32

  447.dealII: basepeak = yes

  450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

  453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
  410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static

  416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static
```

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Peak Optimization Flags (Continued)

434.zeusmp: `basepeak = yes`

437.leslie3d: `-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto`
`-inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xAVX -ipo -O3 -no-prec-div -static -auto-ilp32`
`-opt-mem-layout-trans=3`

481.wrf: `Same as 454.calculix`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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