



SPEC® CFP2006 Result

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IBM Corporation

IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2609)

SPECfp®2006 = 58.8

SPECfp_base2006 = 56.3

CPU2006 license: 11

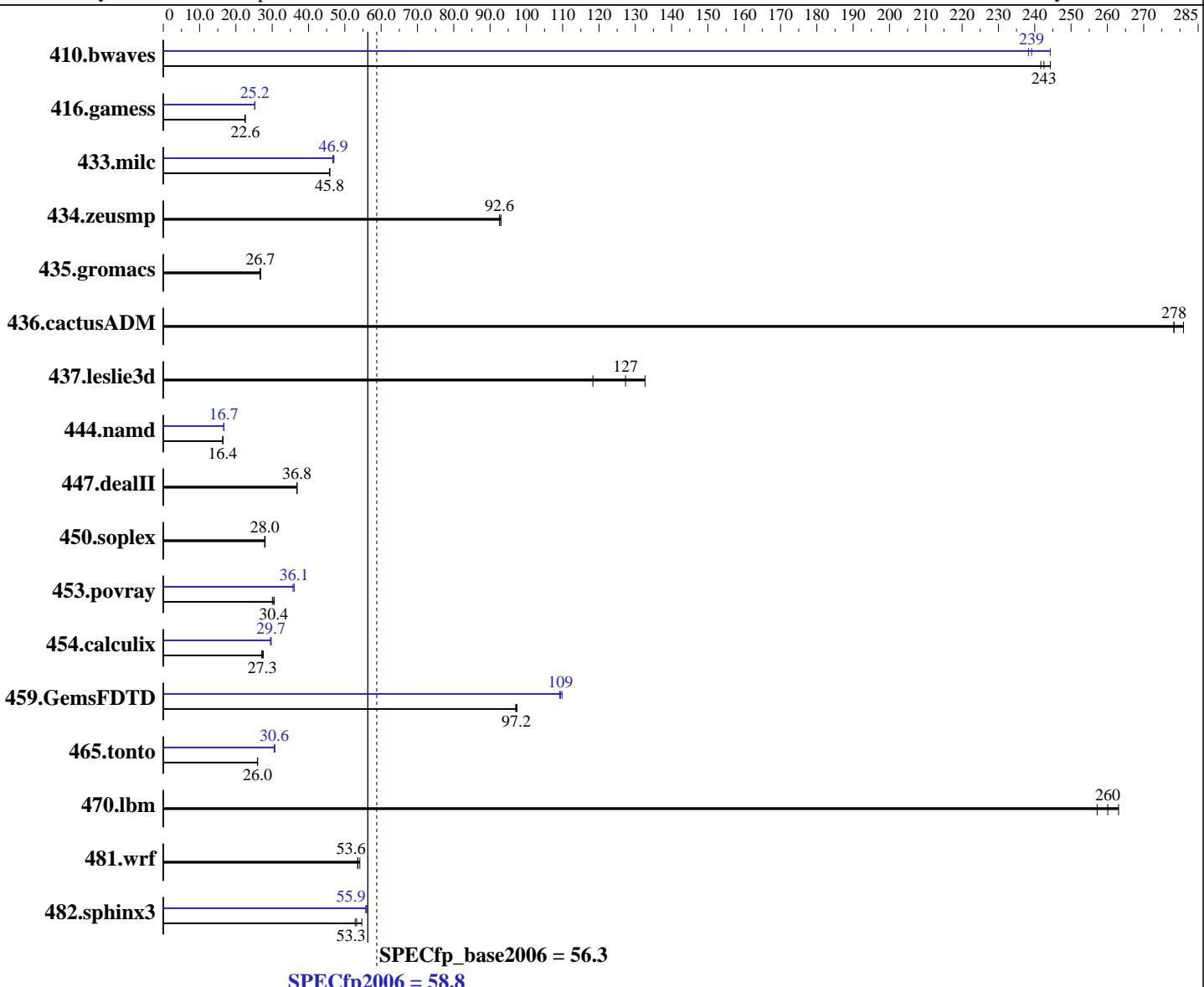
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2609
CPU Characteristics:
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: 2.6.32-220.el6.x86_64
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

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L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	55.6	244	56.2	242	<u>56.0</u>	<u>243</u>	<u>55.6</u>	<u>244</u>	<u>56.8</u>	<u>239</u>	57.0	238
416.gamess	<u>867</u>	<u>22.6</u>	870	22.5	867	22.6	<u>778</u>	<u>25.2</u>	<u>775</u>	<u>25.3</u>	<u>776</u>	<u>25.2</u>
433.milc	200	45.8	200	45.8	<u>200</u>	<u>45.8</u>	197	46.7	195	47.0	<u>196</u>	<u>46.9</u>
434.zeusmp	98.2	92.6	97.8	93.0	<u>98.2</u>	<u>92.6</u>	98.2	92.6	97.8	93.0	<u>98.2</u>	<u>92.6</u>
435.gromacs	<u>267</u>	<u>26.7</u>	266	26.8	268	26.7	<u>267</u>	<u>26.7</u>	266	26.8	268	26.7
436.cactusADM	<u>42.9</u>	<u>278</u>	42.5	281	42.9	278	<u>42.9</u>	<u>278</u>	42.5	281	42.9	278
437.leslie3d	<u>73.8</u>	<u>127</u>	70.8	133	79.4	118	<u>73.8</u>	<u>127</u>	70.8	133	79.4	118
444.namd	489	16.4	489	16.4	<u>489</u>	<u>16.4</u>	<u>481</u>	<u>16.7</u>	481	16.7	481	16.7
447.dealII	<u>311</u>	<u>36.8</u>	310	36.9	311	36.8	<u>311</u>	<u>36.8</u>	310	36.9	311	36.8
450.soplex	299	27.9	297	28.1	<u>298</u>	<u>28.0</u>	299	27.9	297	28.1	<u>298</u>	<u>28.0</u>
453.povray	<u>175</u>	<u>30.4</u>	174	30.5	177	30.0	<u>147</u>	<u>36.1</u>	149	35.7	147	36.1
454.calculix	299	27.5	<u>302</u>	<u>27.3</u>	304	27.1	<u>278</u>	<u>29.7</u>	278	29.7	279	29.5
459.GemsFDTD	109	97.1	<u>109</u>	<u>97.2</u>	109	97.4	<u>97.2</u>	109	96.6	110	<u>97.0</u>	<u>109</u>
465.tonto	378	26.0	<u>379</u>	<u>26.0</u>	379	25.9	<u>321</u>	<u>30.6</u>	320	30.7	322	30.5
470.lbm	53.4	257	52.2	263	<u>52.8</u>	<u>260</u>	53.4	257	52.2	263	<u>52.8</u>	<u>260</u>
481.wrf	<u>208</u>	<u>53.6</u>	209	53.5	206	54.1	<u>208</u>	<u>53.6</u>	209	53.5	206	54.1
482.sphinx3	<u>366</u>	<u>53.3</u>	356	54.7	369	52.9	<u>349</u>	<u>55.9</u>	350	55.8	348	56.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

BIOS Settings:

Operating Mode set to Maximum Performance

Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo

\$Rev: 6775 \$ \$Date::: 2011-08-16 #\\$ 8787f7622badcf24e01c368b1db4377c
 running on tianden Sat Jun 9 04:38:32 2012

This section contains SUT (System Under Test) info as seen by

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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
        2 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 4
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      132096584 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 7 15:36
```

```
SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_tianden-lv_root
                ext4   145G   61G   78G  44%  /
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5

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General Notes (Continued)

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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