



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 199

CELSIUS W520 (Intel Xeon E3-1280 v2)

SPECint_rate_base2006 = 192

CPU2006 license: 19

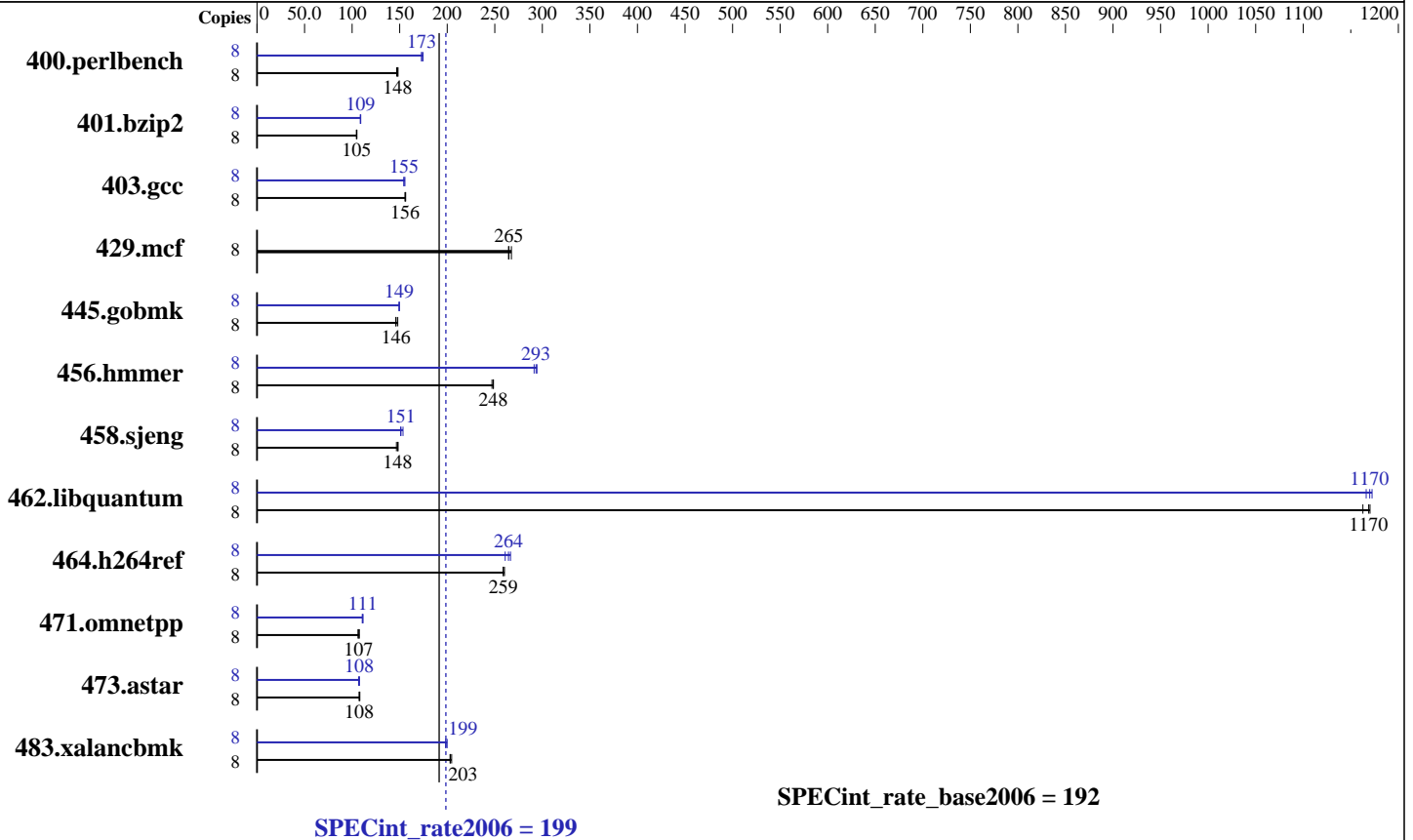
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1280 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2, 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi - user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap 10 (Multi-Core)



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	527	148	532	147	<u>529</u>	<u>148</u>	8	448	175	452	173	<u>452</u>	<u>173</u>
401.bzip2	8	<u>736</u>	<u>105</u>	735	105	739	104	8	<u>710</u>	<u>109</u>	708	109	710	109
403.gcc	8	<u>413</u>	<u>156</u>	412	156	414	156	8	417	154	<u>417</u>	<u>155</u>	414	156
429.mcf	8	276	264	<u>275</u>	<u>265</u>	273	267	8	276	264	<u>275</u>	<u>265</u>	273	267
445.gobmk	8	575	146	567	148	<u>574</u>	<u>146</u>	8	561	150	562	149	<u>561</u>	<u>149</u>
456.hammer	8	300	249	<u>300</u>	<u>248</u>	302	247	8	<u>254</u>	<u>293</u>	256	291	254	294
458.sjeng	8	660	147	653	148	<u>654</u>	<u>148</u>	8	641	151	<u>639</u>	<u>151</u>	631	154
462.libquantum	8	143	1160	<u>142</u>	<u>1170</u>	142	1170	8	141	1170	142	1170	<u>142</u>	<u>1170</u>
464.h264ref	8	<u>684</u>	<u>259</u>	680	260	684	259	8	<u>670</u>	<u>264</u>	664	267	679	261
471.omnetpp	8	<u>467</u>	<u>107</u>	465	108	471	106	8	<u>449</u>	<u>111</u>	449	111	452	111
473.astar	8	<u>522</u>	<u>108</u>	523	107	519	108	8	522	108	526	107	<u>522</u>	<u>108</u>
483.xalancbmk	8	<u>272</u>	<u>203</u>	272	203	270	205	8	278	198	276	200	<u>278</u>	<u>199</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

Binaries were compiled on a system with
2 x E5-2650 CPU + 96 GB memory using RHEL 6.2.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

403.gcc: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

462.libquantum: icc -m32

464.h264ref: icc -m32

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap/lib -lsmarheap
473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs
-L/opt/SmartHeap/lib -lsmarheap

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Peak Optimization Flags (Continued)

483.xalanbmk: Same as 473.astar

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 09:50:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 July 2012.