SPEC® CINT2006 Result
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Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint®_rate2006 =   451

SPECint_rate_base2006 =   430
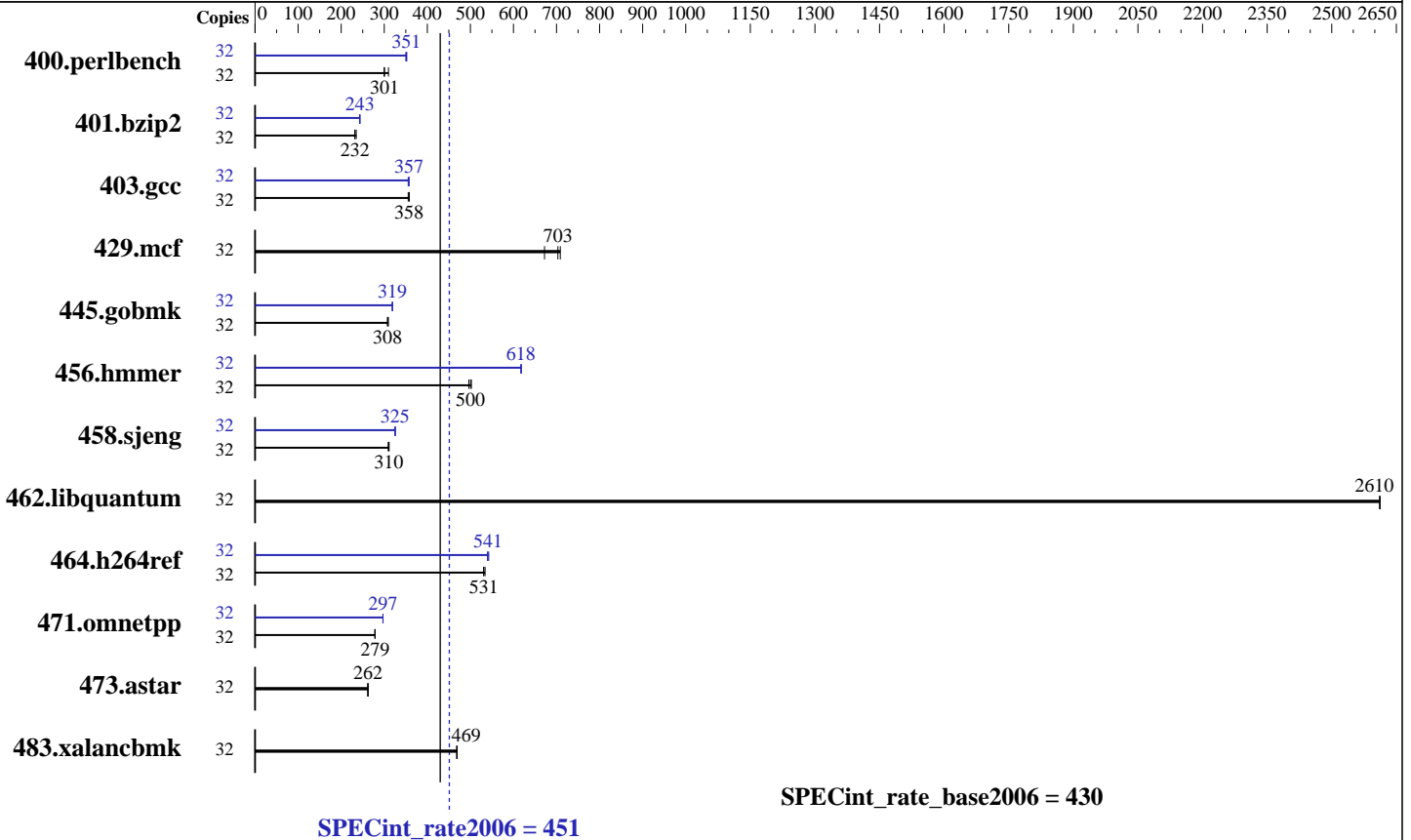
CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

Benchmark	Copies	Base	Peak
400.perlbench	32	301	351
401.bzip2	32	232	243
403.gcc	32	358	357
429.mcf	32	703	
445.gobmk	32	308	319
456.hmmer	32	500	618
458.sjeng	32	310	325
462.libquantum	32	2610	
464.h264ref	32	531	541
471.omnetpp	32	279	297
473.astar	32	262	
483.xalancbmk	32	469	

SPECint_rate_base2006 = 430

SPECint_rate2006 = 451

Hardware

CPU Name: Intel Xeon E5-2650L

CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz

CPU MHz: 1800

FPU: Integrated

CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 20 MB I+D on chip per chip

Other Cache: None

Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)

Disk Subsystem: 1 x 750 GB SATA III, 7200 RPM

Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64

Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux

Auto Parallel: No

File System: ext4

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1009	310	1043	300	**1040**	**301**	32	893	350	**890**	**351**	887	352
401.bzip2	32	1314	235	1332	232	**1331**	**232**	32	1270	243	1269	243	**1269**	**243**
403.gcc	32	724	356	719	358	**720**	**358**	32	724	356	721	358	**721**	**357**
429.mcf	32	434	672	412	709	**415**	**703**	32	434	672	412	709	**415**	**703**
445.gobmk	32	1092	307	**1090**	**308**	1084	310	32	1052	319	1053	319	**1053**	**319**
456.hmmer	32	594	502	**597**	**500**	601	496	32	483	618	**483**	**618**	483	618
458.sjeng	32	1253	309	1244	311	**1247**	**310**	32	**1191**	**325**	1192	325	1188	326
462.libquantum	32	254	2610	254	2610	**254**	**2610**	32	254	2610	254	2610	**254**	**2610**
464.h264ref	32	1326	534	**1333**	**531**	1334	531	32	1312	540	1305	543	**1310**	**541**
471.omnetpp	32	**718**	**279**	719	278	717	279	32	**673**	**297**	673	297	674	297
473.astar	32	856	262	**856**	**262**	859	262	32	856	262	**856**	**262**	859	262
483.xalancbmk	32	470	469	**471**	**469**	472	468	32	470	469	**471**	**469**	472	468

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RHEL5.5
    Filesystem page cache cleared with:
    echo 1>   /proc/sys/vm/drop_caches
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

        401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                   -auto-ilp32 -ansi-alias

          403.gcc: -xAVX -ipo -O3 -no-prec-div

          429.mcf: basepeak = yes

       445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                   -ansi-alias -opt-mem-layout-trans=3

       456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

       458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
                   -auto-ilp32

    462.libquantum: basepeak = yes

      464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                   -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                   -opt-ra-region-strategy=block -Wl,-z,muldefs
                   -L/smartheap -lsmartheap

         473.astar: basepeak = yes

    483.xalancbmk: basepeak = yes
```

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Tested by:	Supermicro	**Software Availability:**	Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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