



# SPEC® CFP2006 Result

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## IBM Corporation

IBM Flex System x220  
(Intel Xeon E5-2450L, 1.80 GHz)

SPECfp®2006 = **60.5**

SPECfp\_base2006 = **57.2**

CPU2006 license: 11

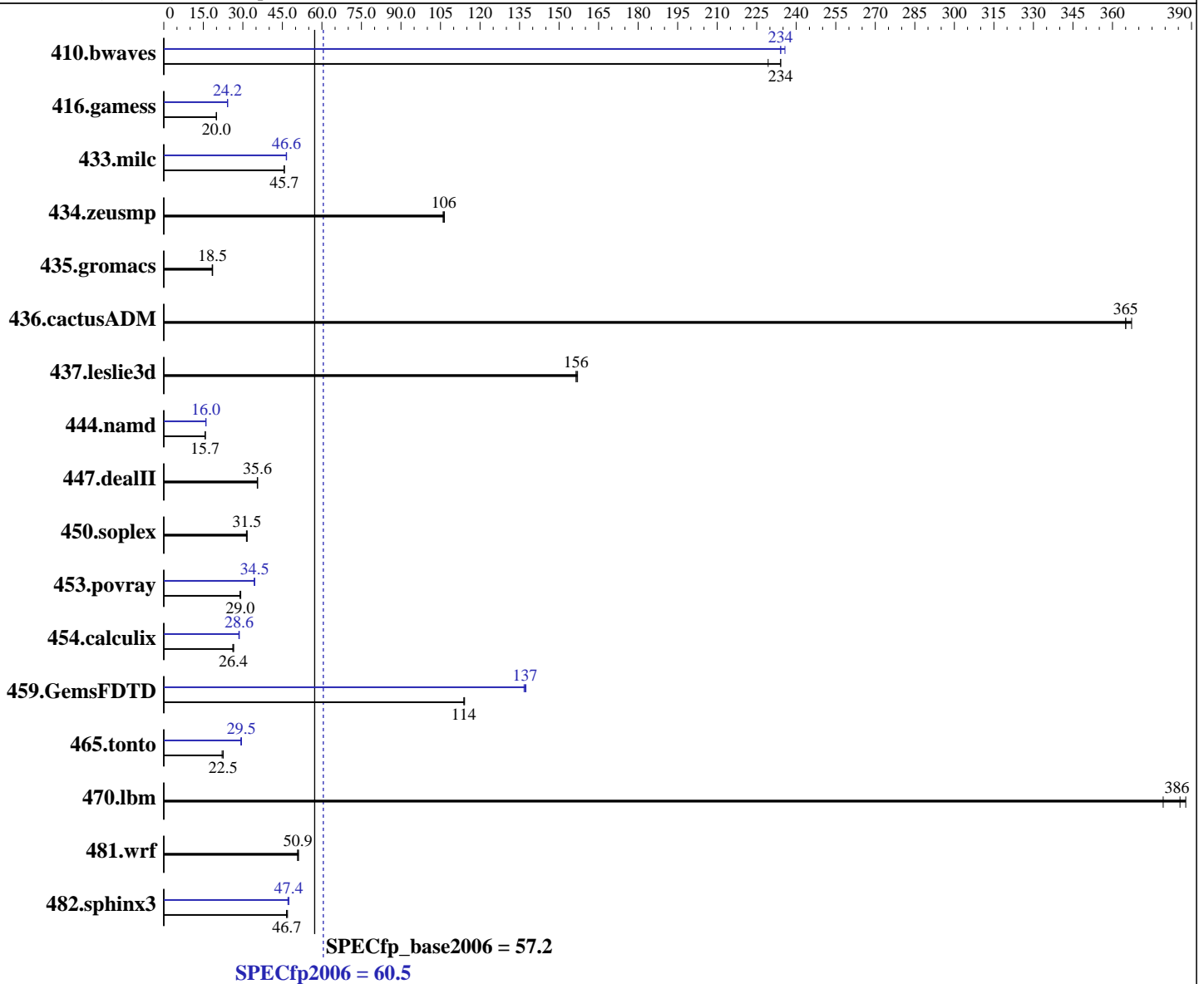
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2012

Hardware Availability: Jun-2012

Software Availability: Dec-2011



**Hardware**

CPU Name: Intel Xeon E5-2450L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
Disk Subsystem: 1 x 500 GB SAS, 10000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	59.3	229	<b>58.1</b>	<b>234</b>	58.1	234	58.1	234	<b>58.1</b>	<b>234</b>	57.7	236
416.gamess	980	20.0	<b>981</b>	<b>20.0</b>	981	20.0	<b>809</b>	<b>24.2</b>	810	24.2	809	24.2
433.milc	201	45.7	<b>201</b>	<b>45.7</b>	201	45.7	197	46.6	198	46.5	<b>197</b>	<b>46.6</b>
434.zeusmp	<b>85.7</b>	<b>106</b>	85.5	106	85.9	106	<b>85.7</b>	<b>106</b>	85.5	106	85.9	106
435.gromacs	<b>386</b>	<b>18.5</b>	386	18.5	389	18.4	<b>386</b>	<b>18.5</b>	386	18.5	389	18.4
436.cactusADM	<b>32.7</b>	<b>365</b>	32.7	365	32.5	367	<b>32.7</b>	<b>365</b>	32.7	365	32.5	367
437.leslie3d	<b>60.1</b>	<b>156</b>	59.9	157	60.1	156	<b>60.1</b>	<b>156</b>	59.9	157	60.1	156
444.namd	510	15.7	<b>510</b>	<b>15.7</b>	510	15.7	502	16.0	<b>502</b>	<b>16.0</b>	502	16.0
447.dealII	323	35.5	321	35.6	<b>322</b>	<b>35.6</b>	323	35.5	321	35.6	<b>322</b>	<b>35.6</b>
450.soplex	266	31.3	<b>265</b>	<b>31.5</b>	264	31.6	266	31.3	<b>265</b>	<b>31.5</b>	264	31.6
453.povray	184	28.9	<b>183</b>	<b>29.0</b>	182	29.2	154	34.5	<b>154</b>	<b>34.5</b>	155	34.3
454.calculix	<b>312</b>	<b>26.4</b>	315	26.2	311	26.5	288	28.6	290	28.5	<b>289</b>	<b>28.6</b>
459.GemsFDTD	<b>93.1</b>	<b>114</b>	93.1	114	92.9	114	<b>77.4</b>	<b>137</b>	77.6	137	77.2	137
465.tonto	<b>437</b>	<b>22.5</b>	436	22.5	445	22.1	334	29.5	<b>334</b>	<b>29.5</b>	337	29.2
470.lbm	35.4	388	<b>35.6</b>	<b>386</b>	36.2	379	35.4	388	<b>35.6</b>	<b>386</b>	36.2	379
481.wrf	218	51.1	<b>220</b>	<b>50.9</b>	220	50.8	218	51.1	<b>220</b>	<b>50.9</b>	220	50.8
482.sphinx3	415	47.0	<b>417</b>	<b>46.7</b>	419	46.6	414	47.1	<b>411</b>	<b>47.4</b>	410	47.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on kestral-pete Thu May 24 11:47:30 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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### Platform Notes (Continued)

```
model name : Intel(R) Xeon(R) CPU E5-2450L 0 @ 1.80GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      99042576 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux kestral-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 21 15:36
```

```
SPEC is set to: /cpu2006.1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_kestralpete-lv_root
    ext4    449G  6.1G  420G   2% /
```

```
Additional information from dmidecode:
Memory:
 12x Micron 36JSF1G72PZ-1G6M1 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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