



# SPEC® CFP2006 Result

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## IBM Corporation

IBM BladeCenter HS23E  
(Intel Xeon E5-2430L, 2.00 GHz)

**SPECfp®2006 = 61.7**

**SPECfp\_base2006 = 58.6**

CPU2006 license: 11

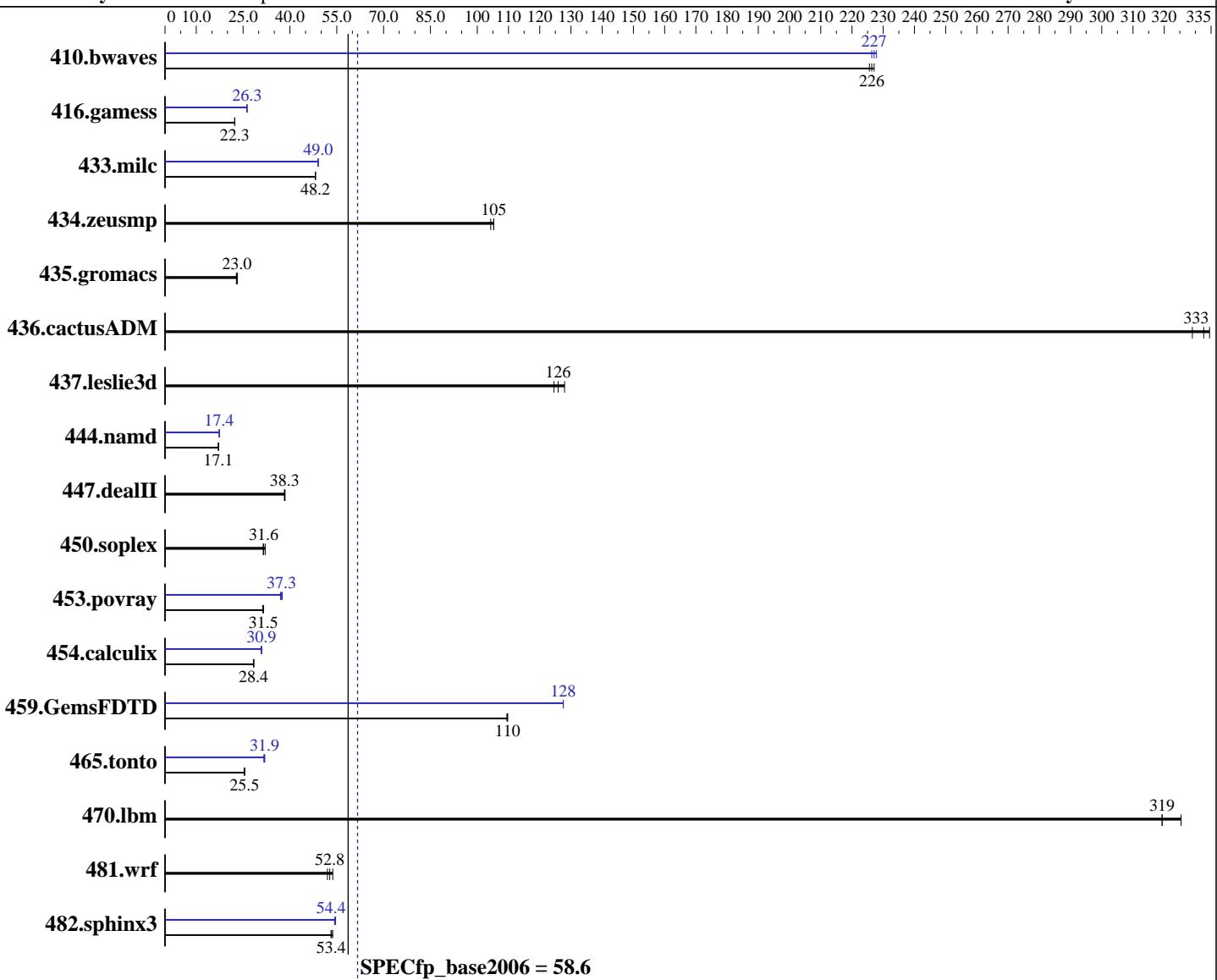
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2012

Hardware Availability: Jun-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2430L  
CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
Compiler: 2.6.32-220.el6.x86\_64  
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>60.0</b>	<b>226</b>	60.2	226	59.8	227	<b>59.6</b>	<b>228</b>	60.0	226	<b>59.8</b>	<b>227</b>
416.gamess	877	22.3	<b>877</b>	<b>22.3</b>	876	22.3	<b>745</b>	<b>26.3</b>	747	26.2	745	26.3
433.milc	<b>191</b>	<b>48.2</b>	191	48.2	191	48.2	<b>187</b>	<b>49.0</b>	<b>187</b>	<b>49.0</b>	187	49.1
434.zeusmp	<b>86.4</b>	<b>105</b>	86.4	105	87.2	104	<b>86.4</b>	<b>105</b>	86.4	105	87.2	104
435.gromacs	313	22.8	309	23.1	<b>310</b>	<b>23.0</b>	313	22.8	309	23.1	<b>310</b>	<b>23.0</b>
436.cactusADM	35.7	335	<b>35.9</b>	<b>333</b>	36.3	329	<b>35.7</b>	<b>335</b>	<b>35.9</b>	<b>333</b>	36.3	329
437.leslie3d	75.5	125	73.5	128	<b>74.7</b>	<b>126</b>	<b>75.5</b>	125	73.5	128	<b>74.7</b>	<b>126</b>
444.namd	<b>469</b>	<b>17.1</b>	469	17.1	469	17.1	<b>462</b>	<b>17.4</b>	462	17.4	<b>462</b>	<b>17.4</b>
447.dealII	299	38.2	<b>298</b>	<b>38.3</b>	298	38.4	<b>299</b>	38.2	<b>298</b>	<b>38.3</b>	298	38.4
450.soplex	260	32.1	265	31.4	<b>264</b>	<b>31.6</b>	<b>260</b>	32.1	265	31.4	<b>264</b>	<b>31.6</b>
453.povray	170	31.3	<b>169</b>	<b>31.5</b>	168	31.6	<b>143</b>	<b>37.3</b>	144	37.0	142	37.5
454.calculix	291	28.3	<b>291</b>	<b>28.4</b>	290	28.5	<b>267</b>	<b>30.9</b>	267	30.9	268	30.7
459.GemsFDTD	96.7	110	<b>96.7</b>	<b>110</b>	96.9	109	<b>83.2</b>	128	83.2	128	<b>83.2</b>	<b>128</b>
465.tonto	<b>386</b>	<b>25.5</b>	386	25.5	388	25.3	<b>309</b>	<b>31.9</b>	308	31.9	311	31.6
470.lbm	<b>43.0</b>	<b>319</b>	43.0	319	42.2	325	<b>43.0</b>	<b>319</b>	43.0	319	42.2	325
481.wrf	215	52.0	<b>212</b>	<b>52.8</b>	208	53.7	<b>215</b>	<b>52.0</b>	<b>212</b>	<b>52.8</b>	208	53.7
482.sphinx3	367	53.1	<b>365</b>	<b>53.4</b>	363	53.7	<b>357</b>	<b>54.6</b>	<b>358</b>	<b>54.4</b>	359	54.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3  
running on angleshark-pete Thu May 31 20:56:07 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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**Test date:** May-2012

**Hardware Availability:** Jun-2012

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## Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2430L 0 @ 2.00GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      99042356 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux angleshark-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST
2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 29 18:32
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_anglesharkpet-lv_root
                ext4   265G   36G  217G  14%  /
```

Additional information from dmidecode:

```
Memory:
6x Micron 36JDYS1G72PZ-1G6M1 8 GB 1600 MHz 2 rank
6x Samsung M392B1K70DM0-CK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"  
OMP\_NUM\_THREADS = "12"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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