SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX100 S7p, Intel Xeon E3-1280 v2, 3.60 GHz

$SPECint^{®}\_rate2006 =$ 199

$SPECint\_rate\_base2006 =$ 192

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Apr-2012
Hardware Availability: May-2012
Software Availability: Feb-2012

Copies

Benchmark	Copies	Result
400.perlbench	8	175
400.perlbench	8	148
401.bzip2	8	109
401.bzip2	8	106
403.gcc	8	156
403.gcc	8	156
429.mcf	8	262
445.gobmk	8	150
445.gobmk	8	148
456.hmmer	8	290
456.hmmer	8	249
458.sjeng	8	156
458.sjeng	8	148
462.libquantum	8	1170
462.libquantum	8	1170
464.h264ref	8	264
464.h264ref	8	261
471.omnetpp	8	110
471.omnetpp	8	107
473.astar	8	108
473.astar	8	108
483.xalancbmk	8	204

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Hardware

CPU Name:	Intel Xeon E3-1280 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 4.0 GHz
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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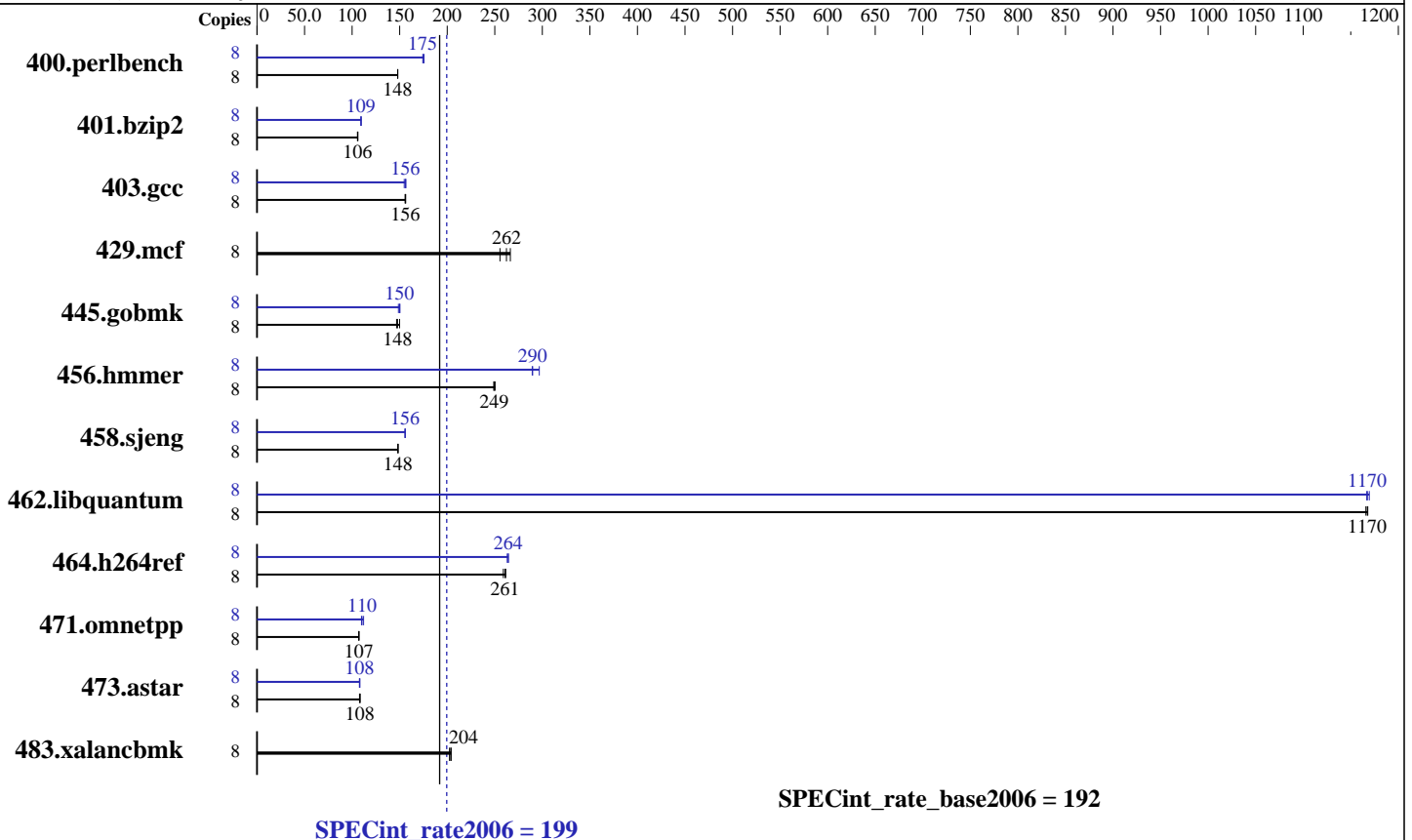SPECint_rate_base2006 = 192

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	528	148	**529**	**148**	529	148	8	**447**	**175**	445	176	448	174
401.bzip2	8	731	106	728	106	**729**	**106**	8	705	110	**706**	**109**	707	109
403.gcc	8	412	156	**413**	**156**	413	156	8	411	157	416	155	**413**	**156**
429.mcf	8	274	266	286	255	**278**	**262**	8	274	266	286	255	**278**	**262**
445.gobmk	8	572	147	560	150	**568**	**148**	8	**560**	**150**	564	149	558	150
456.hmmer	8	300	249	**299**	**249**	298	250	8	258	289	251	297	**258**	**290**
458.sjeng	8	654	148	**653**	**148**	653	148	8	622	156	**621**	**156**	621	156
462.libquantum	8	**142**	**1170**	142	1170	142	1170	8	142	1170	142	1170	**142**	**1170**
464.h264ref	8	**679**	**261**	684	259	677	261	8	673	263	670	264	**670**	**264**
471.omnetpp	8	468	107	**467**	**107**	466	107	8	**453**	**110**	455	110	446	112
473.astar	8	521	108	**519**	**108**	518	108	8	519	108	521	108	**521**	**108**
483.xalancbmk	8	**271**	**204**	273	202	270	204	8	**271**	**204**	273	202	270	204

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

    Binaries compiled on a system with 2x E5-2650 CPU + 96 GB
    memory using RHEL6.2
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
       icc   -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

     403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

     429.mcf: basepeak = yes

   445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
               -opt-mem-layout-trans=3

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/opt/SmartHeap/lib -lsmartheap

     473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
               -opt-mem-layout-trans=3 -Wl,-z,muldefs
               -L/opt/SmartHeap/lib -lsmartheap
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml

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For other inquiries, please contact webmaster@spec.org.

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