



SPEC® CFP2006 Result

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Supermicro

SuperServer 5017C-LF (X9SCL-F. Intel Xeon E3-1220LV2)

SPECfp®2006 = 54.8

SPECfp_base2006 = 53.4

CPU2006 license: 001176

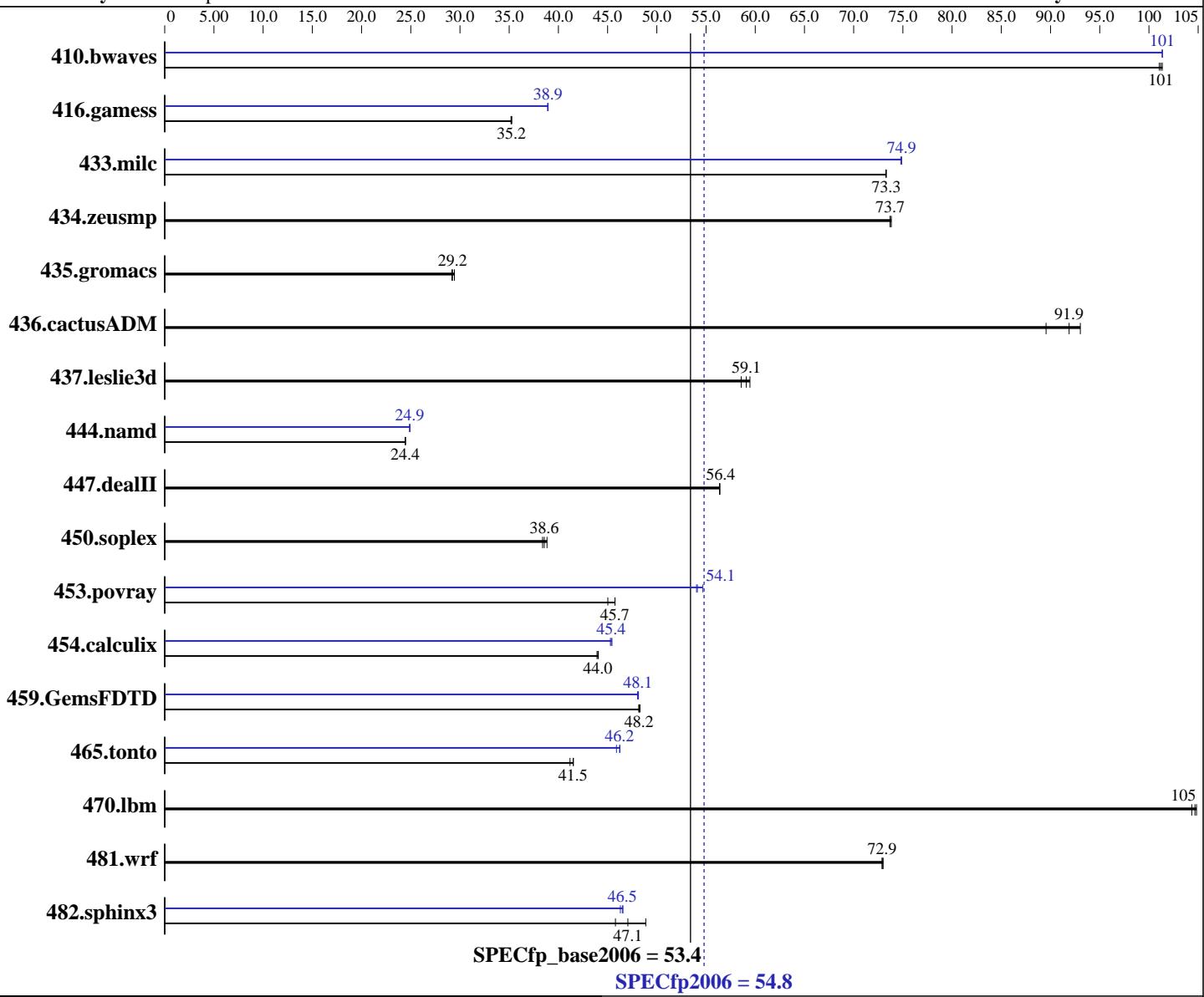
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E3-1220L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago), Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	134	101	134	101	134	101	134	101	134	101	134	101
416.gamess	555	35.3	556	35.2	556	35.2	503	39.0	503	38.9	503	38.9
433.milc	125	73.3	125	73.3	125	73.3	123	74.9	123	74.9	123	74.8
434.zeusmp	123	73.7	123	73.7	123	73.8	123	73.7	123	73.7	123	73.8
435.gromacs	243	29.4	245	29.2	244	29.2	243	29.4	245	29.2	244	29.2
436.cactusADM	130	91.9	128	93.0	133	89.5	130	91.9	128	93.0	133	89.5
437.leslie3d	158	59.5	160	58.6	159	59.1	158	59.5	160	58.6	159	59.1
444.namd	328	24.5	328	24.4	328	24.4	322	24.9	322	24.9	322	24.9
447.dealII	203	56.4	203	56.4	203	56.4	203	56.4	203	56.4	203	56.4
450.soplex	216	38.6	217	38.4	215	38.8	216	38.6	217	38.4	215	38.8
453.povray	116	45.7	118	45.0	116	45.8	98.5	54.0	97.3	54.7	98.3	54.1
454.calculix	187	44.1	188	43.9	187	44.0	182	45.4	182	45.4	182	45.3
459.GemsFDTD	220	48.3	220	48.2	220	48.2	221	48.1	220	48.1	221	48.0
465.tonto	239	41.2	237	41.5	237	41.5	214	45.9	213	46.2	213	46.3
470.lbm	131	105	132	104	131	105	131	105	132	104	131	105
481.wrf	153	72.9	153	72.9	153	73.0	153	72.9	153	72.9	153	73.0
482.sphinx3	426	45.8	399	48.9	414	47.1	419	46.5	421	46.3	419	46.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:

 -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

 -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

 -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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