



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 49.3

IBM System x3630 M4 (Intel Xeon E5-2470)

SPECint_base2006 = 46.1

CPU2006 license: 11

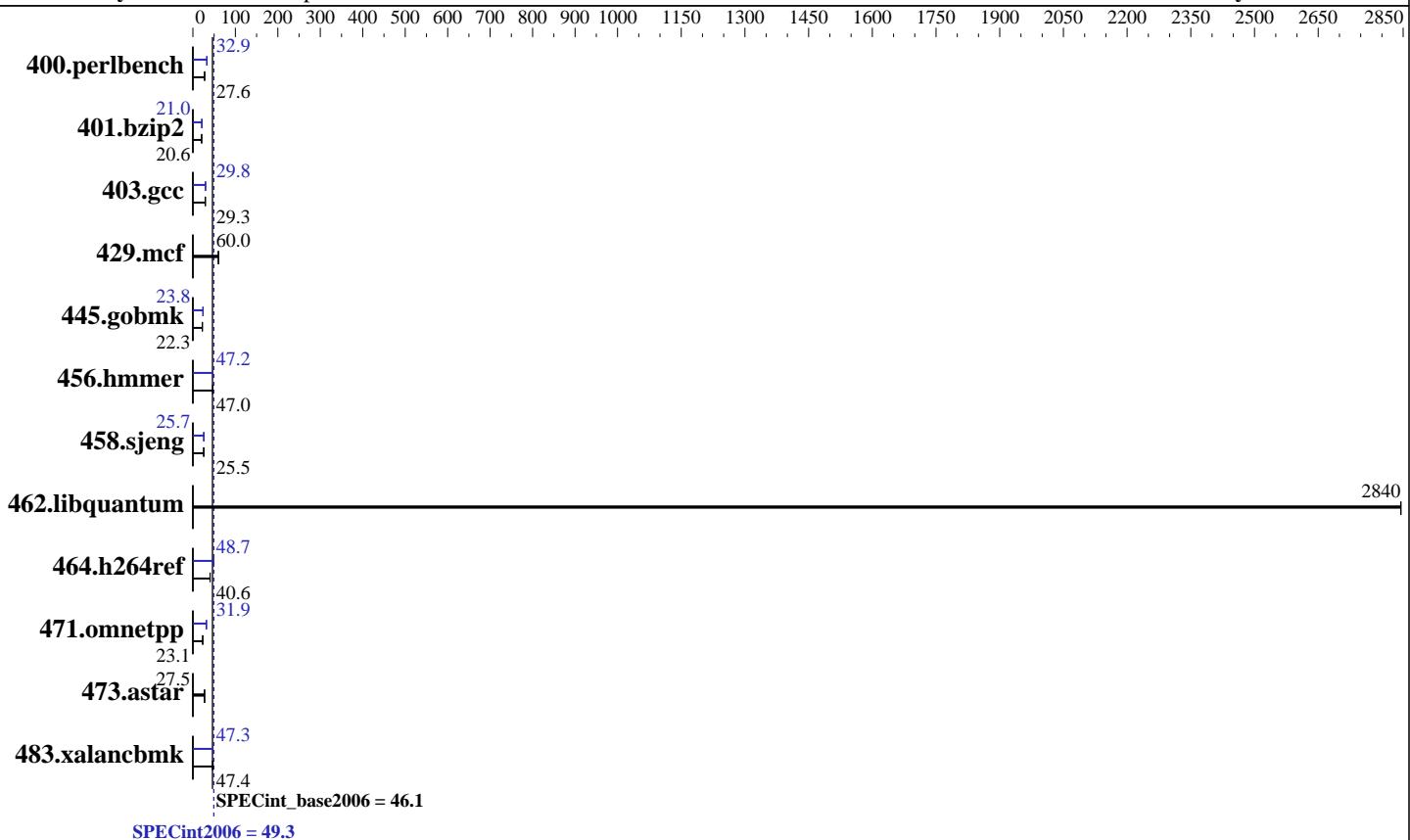
Test date: May-2012

Test sponsor: IBM Corporation

Hardware Availability: Jun-2012

Tested by: IBM Corporation

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2470
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 Compiler: 2.6.32-220.el6.x86_64
 Auto Parallel: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	354	27.6	355	27.6	354	27.6	297	32.9	297	32.9	297	32.9
401.bzip2	469	20.6	470	20.5	469	20.6	459	21.0	460	21.0	459	21.0
403.gcc	275	29.3	274	29.3	275	29.3	271	29.7	271	29.8	270	29.8
429.mcf	152	60.1	152	60.0	152	60.0	152	60.1	152	60.0	152	60.0
445.gobmk	471	22.3	471	22.3	471	22.3	442	23.8	442	23.8	442	23.8
456.hmmer	198	47.1	199	46.8	198	47.0	198	47.2	198	47.2	198	47.2
458.sjeng	474	25.5	474	25.5	474	25.5	472	25.7	471	25.7	472	25.6
462.libquantum	7.28	2840	7.28	2840	7.28	2840	7.28	2840	7.28	2840	7.28	2840
464.h264ref	550	40.2	545	40.6	545	40.6	454	48.8	454	48.7	463	47.8
471.omnetpp	270	23.1	270	23.1	270	23.1	196	31.9	196	31.9	197	31.7
473.astar	256	27.5	256	27.5	256	27.5	256	27.5	256	27.5	256	27.5
483.xalancbmk	151	45.8	146	47.4	145	47.7	146	47.4	146	47.2	146	47.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS Settings:

Operating Mode set to Maximum Performance

Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3

running on x3630m4-rhel62 Wed May 9 21:12:02 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2470 0 @ 2.30GHz
        2 "physical id"s (chips)
        32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 8
        siblings : 16
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
```

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Platform Notes (Continued)

```
cache size : 20480 kB

From /proc/meminfo
  MemTotal:      99042548 kB
  HugePages_Total:       0
  Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux x3630m4-rhel62 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 9 08:17

SPEC is set to: /home/SPECcpu-v1.2
  Filesystem      Type  Size  Used Avail Use% Mounted on
  /dev/mapper/vg_x3630m4rhe162-lv_home
            ext4   383G   7.8G  356G    3%  /home

Additional information from dmidecode:
  Memory:
    12x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m64`

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hammer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`445.gobmk: icc -m32`

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Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
              -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block           -ansi-alias
              -Wl,-z,muldefs -L/smartheap -lsmartheap
```

473.astar: basepeak = yes

```
483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
                -Wl,-z,muldefs -L/smartheap -lsmartheap
```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact webmaster@spec.org.

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