SPEC® CFP2006 Result

Supermicro

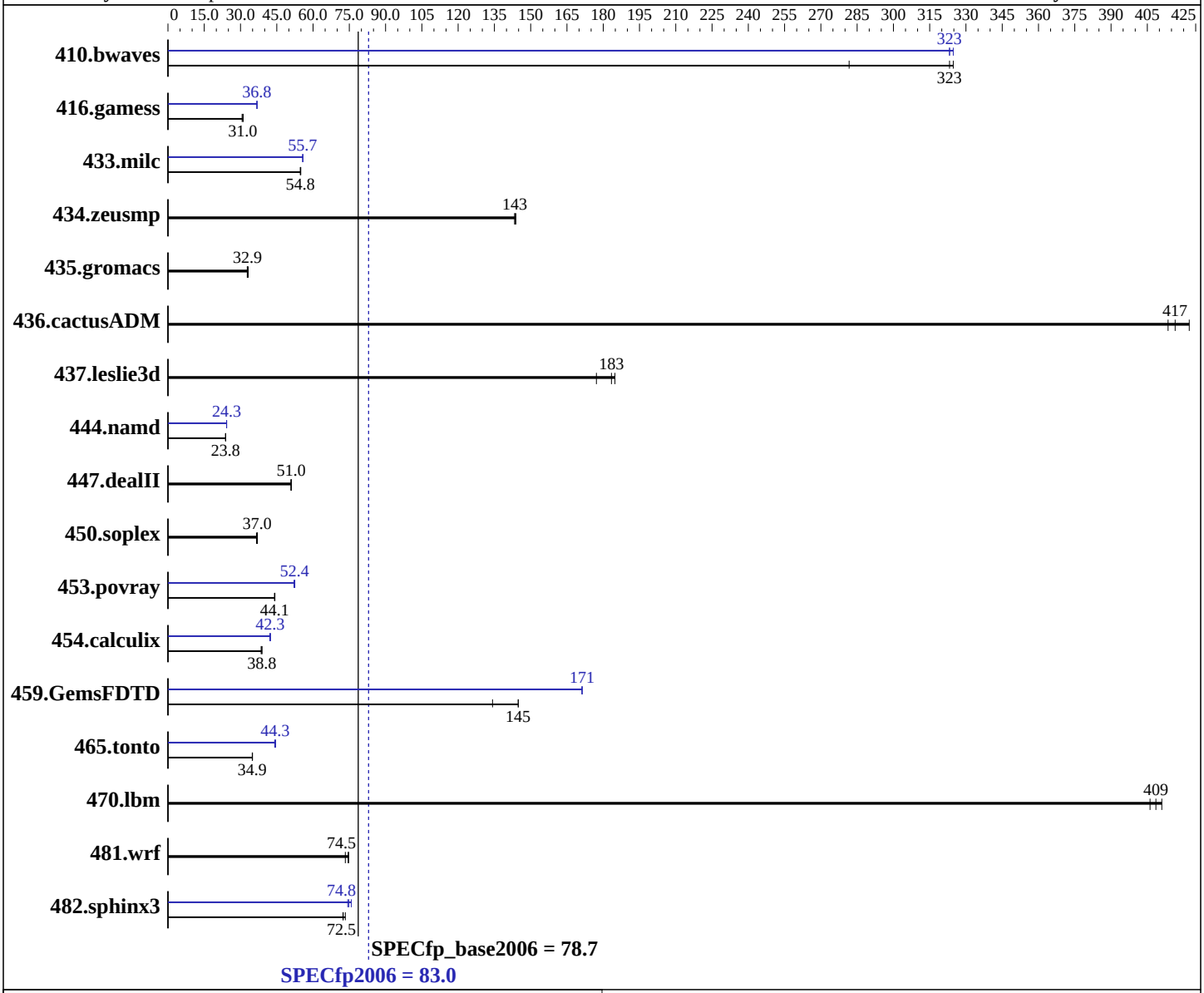
SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECfp®2006 = 83.0

SPECfp_base2006 = 78.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2012
Hardware Availability: Mar-2012
Software Availability: Oct-2011



Hardware

CPU Name:	Intel Xeon E5-2667
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 (Santiago) 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	15 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	64 GB (8 x 8 GB 2Rx8 PC3-12800R-11, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 1 TB SATA II, 7200 RPM	Other Software:	none
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48.2	282	**42.0**	**323**	41.8	325	41.8	325	**42.0**	**323**	42.0	323
416.gamess	639	30.7	630	31.1	**632**	**31.0**	532	36.8	**532**	**36.8**	531	36.9
433.milc	**167**	**54.8**	167	54.9	168	54.7	**165**	**55.7**	165	55.7	164	55.8
434.zeusmp	**63.5**	**143**	63.5	143	63.3	144	**63.5**	**143**	63.5	143	63.3	144
435.gromacs	215	33.2	**217**	**32.9**	218	32.8	215	33.2	**217**	**32.9**	218	32.8
436.cactusADM	28.3	422	28.9	414	**28.7**	**417**	28.3	422	28.9	414	**28.7**	**417**
437.leslie3d	50.9	185	**51.3**	**183**	53.1	177	50.9	185	**51.3**	**183**	53.1	177
444.namd	336	23.8	336	23.9	**336**	**23.8**	331	24.2	**331**	**24.3**	330	24.3
447.dealII	224	51.0	225	50.8	**224**	**51.0**	224	51.0	225	50.8	**224**	**51.0**
450.soplex	**226**	**37.0**	225	37.0	228	36.6	**226**	**37.0**	225	37.0	228	36.6
453.povray	**121**	**44.1**	121	44.1	120	44.2	102	52.1	**102**	**52.4**	101	52.5
454.calculix	214	38.5	**213**	**38.8**	212	38.9	**195**	**42.3**	196	42.1	195	42.4
459.GemsFDTD	79.0	134	73.2	145	**73.2**	**145**	62.0	171	61.9	171	**62.0**	**171**
465.tonto	**282**	**34.9**	281	35.0	282	34.9	221	44.5	223	44.1	**222**	**44.3**
470.lbm	33.4	411	33.8	406	**33.6**	**409**	33.4	411	33.8	406	**33.6**	**409**
481.wrf	149	74.7	**150**	**74.5**	152	73.3	149	74.7	**150**	**74.5**	152	73.3
482.sphinx3	**269**	**72.5**	269	72.4	266	73.3	**261**	**74.8**	262	74.4	257	75.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sat Apr 28 07:46:26 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2667 0 @ 2.90GHz
          2 "physical id"s (chips)
          24 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 6
          siblings  : 12
          physical 0: cores 0 1 2 3 4 5
          physical 1: cores 0 1 2 3 4 5
       cache size : 15360 KB

    From /proc/meminfo
       MemTotal:       65949756 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
       Red Hat Enterprise Linux Server release 6.1 (Santiago)

    From /etc/*release* /etc/*version*
       redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
       system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

    uname -a:
       Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
       15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Apr 27 02:13

    SPEC is set to: /home/cpu2006
       Filesystem     Type    Size  Used Avail Use% Mounted on
       /dev/mapper/VolGroup-lv_home
                      ext4    162G  104G   50G  68% /home

    Additional information from dmidecode:
      Memory:
       8x Hynix Semiconducto HMT31GR7CFR4C 8 GB 1600 MHz 1 rank

    (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "12"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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