



SPEC[®] CFP2006 Result

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Hewlett-Packard Company

SPECfp[®]_rate2006 = 183

ProLiant BL460c Gen8
(3.00 GHz, Intel Xeon E5-2637)

SPECfp_rate_base2006 = 180

CPU2006 license: 3

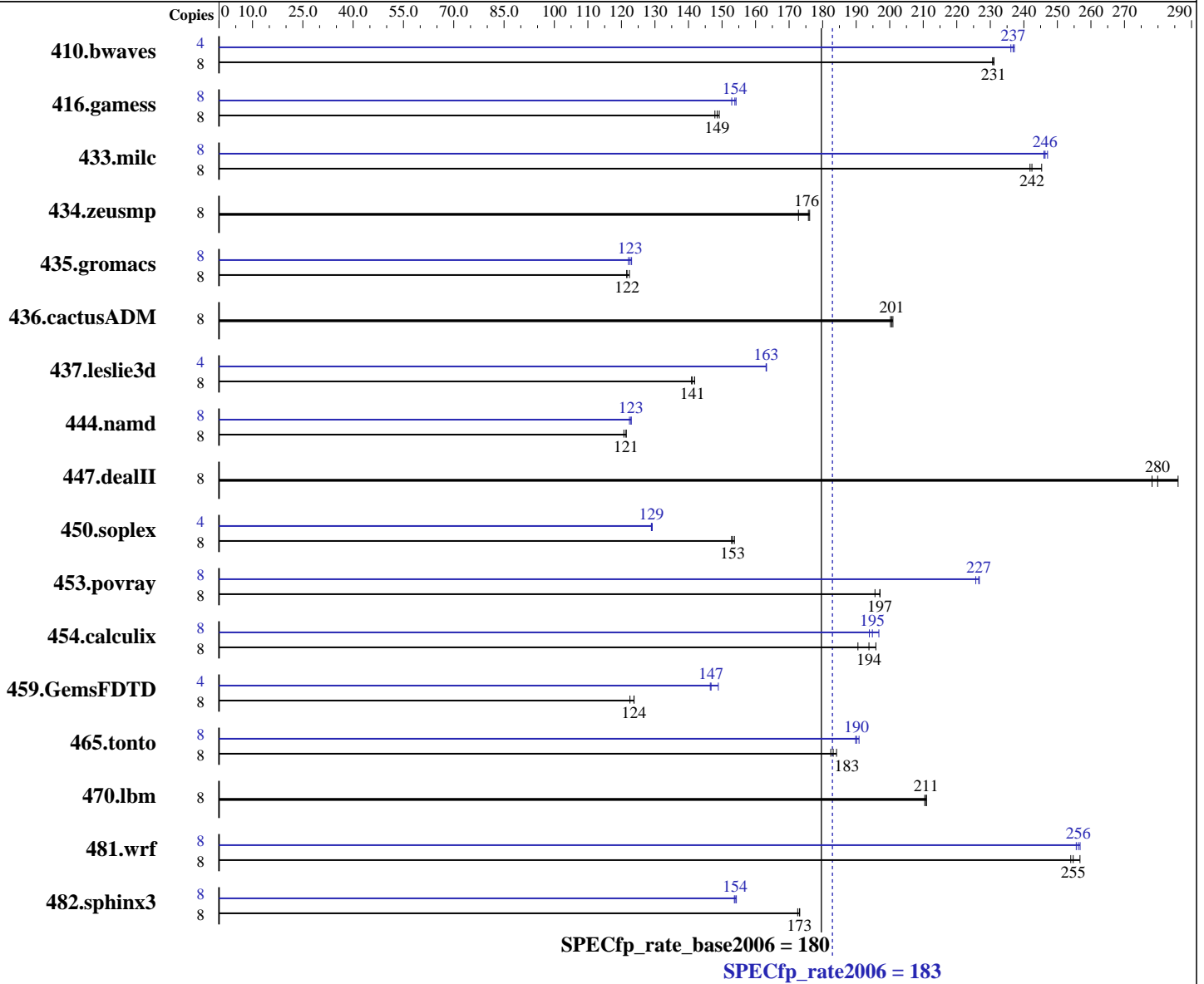
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2012

Hardware Availability: Jun-2012

Software Availability: Mar-2012



Hardware

CPU Name: Intel Xeon E5-2637
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 5 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 146 GB 15 K SAS
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: HP Array Configuration Utility, CLI version

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	470	231	471	231	<u>471</u>	<u>231</u>	4	230	236	<u>230</u>	<u>237</u>	229	237
416.gamess	8	1050	149	1059	148	<u>1054</u>	<u>149</u>	8	1024	153	<u>1018</u>	<u>154</u>	1016	154
433.milc	8	299	245	304	242	<u>303</u>	<u>242</u>	8	<u>298</u>	<u>246</u>	299	246	297	247
434.zeusmp	8	<u>414</u>	<u>176</u>	413	176	421	173	8	<u>414</u>	<u>176</u>	413	176	421	173
435.gromacs	8	467	122	<u>470</u>	<u>122</u>	470	122	8	468	122	464	123	<u>466</u>	<u>123</u>
436.cactusADM	8	<u>477</u>	<u>201</u>	476	201	477	200	8	<u>477</u>	<u>201</u>	476	201	477	200
437.leslie3d	8	<u>533</u>	<u>141</u>	534	141	530	142	4	<u>230</u>	<u>163</u>	230	163	230	163
444.namd	8	531	121	<u>529</u>	<u>121</u>	528	121	8	<u>522</u>	<u>123</u>	522	123	524	122
447.dealII	8	320	286	329	278	<u>327</u>	<u>280</u>	8	320	286	329	278	<u>327</u>	<u>280</u>
450.soplex	8	437	153	<u>436</u>	<u>153</u>	434	154	4	259	129	<u>259</u>	<u>129</u>	258	129
453.povray	8	216	197	<u>216</u>	<u>197</u>	218	196	8	189	226	<u>188</u>	<u>227</u>	188	227
454.calculix	8	346	191	337	196	<u>341</u>	<u>194</u>	8	340	194	<u>339</u>	<u>195</u>	335	197
459.GemsFDTD	8	693	123	<u>686</u>	<u>124</u>	686	124	4	285	149	<u>289</u>	<u>147</u>	290	147
465.tonto	8	<u>430</u>	<u>183</u>	427	184	431	182	8	<u>414</u>	<u>190</u>	415	190	412	191
470.lbm	8	<u>521</u>	<u>211</u>	522	210	521	211	8	<u>521</u>	<u>211</u>	522	210	521	211
481.wrf	8	348	257	352	254	<u>351</u>	<u>255</u>	8	<u>349</u>	<u>256</u>	350	256	348	257
482.sphinx3	8	900	173	<u>901</u>	<u>173</u>	904	173	8	<u>1013</u>	<u>154</u>	1015	154	1011	154

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version
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Operating System Notes (Continued)

Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version

Platform Notes

BIOS configuration:

HP Power Profile set to Custom

Energy/Performance Bias is set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Processor Power and Utilization Monitoring set to Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/cpu2006/libs2/32:/cpu2006/libs2/64"

Binaries compiled on a system with 2x Xeon E5-2667 CPU + 256GB memory using SLES11SP2RC3

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
         -opt-mem-layout-trans=3

```

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

```

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -static

```

```

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
         -inline-level=0 -scalar-rep- -static

```

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120425.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120425.xml>

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