



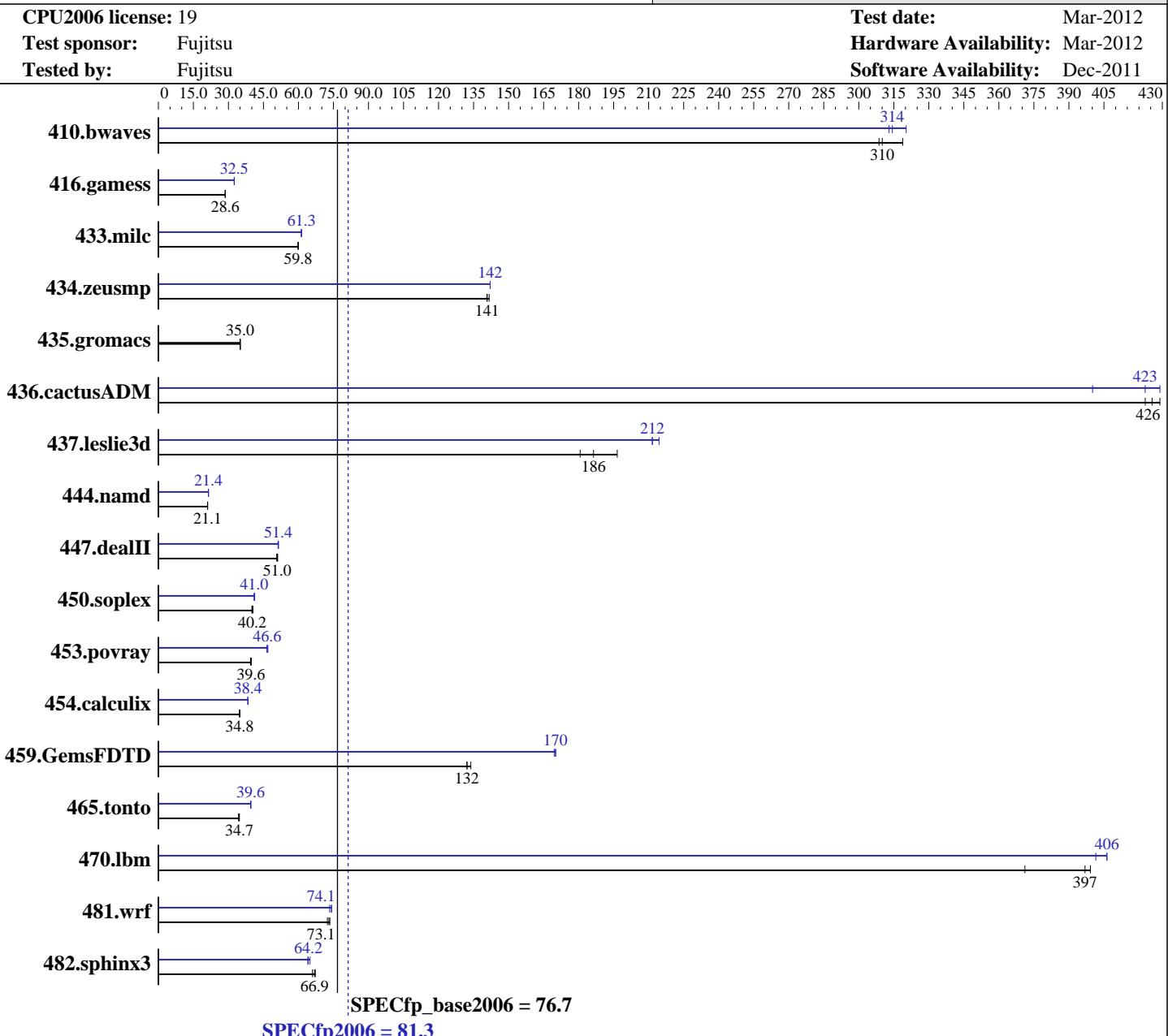
SPEC® CFP2006 Result

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Fujitsu

CELSIUS R920 (Intel Xeon E5-2665)

SPECfp®2006 = 81.3



Hardware

CPU Name: Intel Xeon E5-2665
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi - user)

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CPU2006 license: 19	Test date: Mar-2012
Test sponsor: Fujitsu	Hardware Availability: Mar-2012
Tested by: Fujitsu	Software Availability: Dec-2011

L3 Cache:	20 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)	Other Software:	None
Disk Subsystem:	1 x SATA III, 500 GB, 7200 rpm		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	42.6	319	44.0	309	43.8	310	42.4	320	43.4	313	43.2	314
416.gamess	685	28.6	684	28.6	682	28.7	603	32.5	602	32.5	602	32.5
433.milc	153	59.9	153	59.8	153	59.8	150	61.3	150	61.2	150	61.3
434.zeusmp	64.2	142	64.6	141	64.6	141	64.0	142	64.0	142	64.0	142
435.gromacs	203	35.1	204	35.0	204	35.0	203	35.1	204	35.0	204	35.0
436.cactusADM	28.1	426	27.9	429	28.3	423	29.9	400	28.3	423	27.9	429
437.leslie3d	50.4	186	52.0	181	47.8	197	43.8	214	44.4	212	44.4	212
444.namd	380	21.1	380	21.1	381	21.1	374	21.4	374	21.4	374	21.4
447.dealII	224	51.1	224	51.0	226	50.7	223	51.4	223	51.3	223	51.4
450.soplex	208	40.0	206	40.5	207	40.2	202	41.3	203	41.0	203	41.0
453.povray	134	39.8	135	39.5	134	39.6	114	46.5	113	46.9	114	46.6
454.calculix	236	34.9	237	34.8	238	34.7	215	38.4	215	38.4	215	38.4
459.GemsFDTD	79.3	134	80.3	132	80.3	132	62.4	170	62.4	170	62.6	170
465.tonto	284	34.7	284	34.7	287	34.3	249	39.6	249	39.5	248	39.6
470.lbm	37.0	371	34.4	399	34.6	397	33.8	406	33.8	406	34.2	402
481.wrf	152	73.4	153	73.1	154	72.4	151	74.1	152	73.4	151	74.1
482.sphinx3	292	66.9	290	67.2	295	66.0	305	64.0	304	64.2	300	64.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file. For full details on using numactl, please refer to your Linux documentation, 'man numactl'

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

BIOS settings:

Hyper-Threading Technology = Disabled

Frequency Floor Override = Enabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with

2x Xeon E5-2690 CPU + 64 GB memory using

Red Hat Enterprise Linux Server release 6.1 (Santiago)

Added glibc-static-2.12-1.25.el6.x86_64.rpm

to enable static linking

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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CELSIUS R920 (Intel Xeon E5-2665)

SPECfp2006 = 81.3

SPECfp_base2006 = 76.7

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: -xAVX -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: -xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-static

450.soplex: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
-ansi-alias

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: -xAVX -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch

437.leslie3d: Same as 434.zeusmp

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

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Software Availability: Dec-2011

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -xAVX -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: Same as 436.cactusADM

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>

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