# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

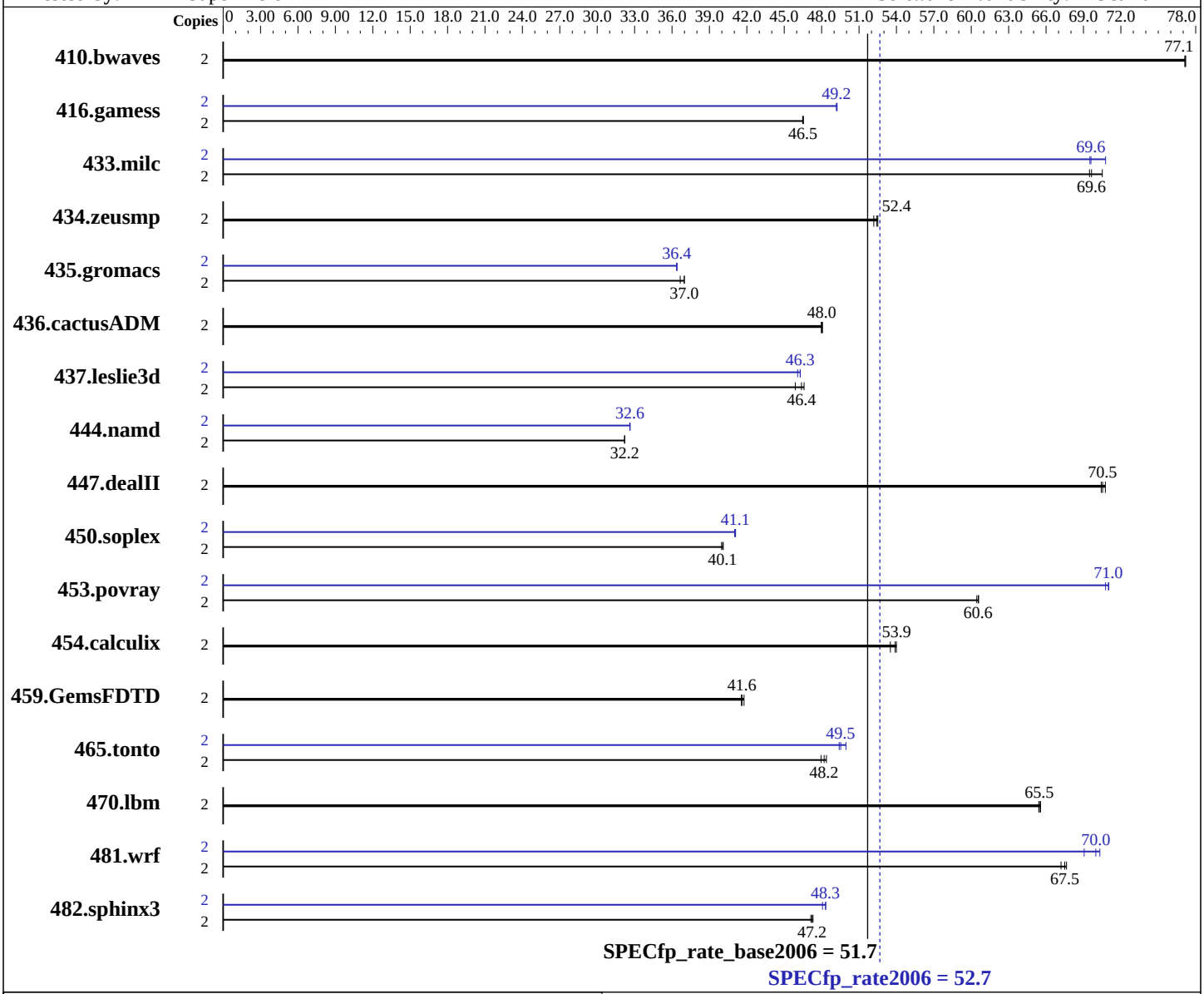
SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel Celeron G530)

**SPECfp®_rate2006 = 52.7**

**SPECfp_rate_base2006 = 51.7**

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** Mar-2012
**Hardware Availability:** Aug-2011
**Software Availability:** Oct-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Celeron G530 |
| CPU Characteristics: | |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel Celeron G530)

SPECfp_rate2006 = 52.7

SPECfp_rate_base2006 = 51.7

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** Mar-2012
**Hardware Availability:** Aug-2011
**Software Availability:** Oct-2011

L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 1 x 500 GB SATA III, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 2 | **352** | **77.1** | 352 | 77.2 | 352 | 77.1 | 2 | **352** | **77.1** | 352 | 77.2 | 352 | 77.1 |
| 416.gamess | 2 | 843 | 46.5 | 841 | 46.5 | **842** | **46.5** | 2 | 796 | 49.2 | 795 | 49.2 | **796** | **49.2** |
| 433.milc | 2 | 260 | 70.5 | **264** | **69.6** | 264 | 69.5 | 2 | 259 | 70.8 | 264 | 69.5 | **264** | **69.6** |
| 434.zeusmp | 2 | 349 | 52.2 | 347 | 52.5 | **347** | **52.4** | 2 | 349 | 52.2 | 347 | 52.5 | **347** | **52.4** |
| 435.gromacs | 2 | 386 | 37.0 | 390 | 36.7 | **386** | **37.0** | 2 | **392** | **36.4** | 392 | 36.4 | 393 | 36.4 |
| 436.cactusADM | 2 | 497 | 48.1 | 498 | 48.0 | **498** | **48.0** | 2 | 497 | 48.1 | 498 | 48.0 | **498** | **48.0** |
| 437.leslie3d | 2 | 410 | 45.9 | 403 | 46.6 | **405** | **46.4** | 2 | **406** | **46.3** | 408 | 46.1 | 406 | 46.3 |
| 444.namd | 2 | 498 | 32.2 | 498 | 32.2 | **498** | **32.2** | 2 | 492 | 32.6 | **492** | **32.6** | 491 | 32.6 |
| 447.dealII | 2 | 323 | 70.8 | **324** | **70.5** | 325 | 70.4 | 2 | 323 | 70.8 | **324** | **70.5** | 325 | 70.4 |
| 450.soplex | 2 | 416 | 40.1 | 417 | 40.0 | **416** | **40.1** | 2 | 406 | 41.1 | **406** | **41.1** | 407 | 41.0 |
| 453.povray | 2 | 176 | 60.6 | **176** | **60.6** | 176 | 60.4 | 2 | **150** | **71.0** | 150 | 70.8 | 150 | 71.0 |
| 454.calculix | 2 | 308 | 53.5 | 306 | 54.0 | **306** | **53.9** | 2 | 308 | 53.5 | 306 | 54.0 | **306** | **53.9** |
| 459.GemsFDTD | 2 | **510** | **41.6** | 508 | 41.8 | 511 | 41.6 | 2 | **510** | **41.6** | 508 | 41.8 | 511 | 41.6 |
| 465.tonto | 2 | 410 | 48.0 | 407 | 48.4 | **408** | **48.2** | 2 | 398 | 49.4 | **397** | **49.5** | 394 | 50.0 |
| 470.lbm | 2 | 420 | 65.4 | 419 | 65.6 | **420** | **65.5** | 2 | 420 | 65.4 | 419 | 65.6 | **420** | **65.5** |
| 481.wrf | 2 | **331** | **67.5** | 330 | 67.6 | 332 | 67.2 | 2 | 318 | 70.3 | **319** | **70.0** | 323 | 69.1 |
| 482.sphinx3 | 2 | 827 | 47.1 | **825** | **47.2** | 824 | 47.3 | 2 | **807** | **48.3** | 807 | 48.3 | 811 | 48.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
```

Continued on next page

## General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Continued on next page

## Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`

Continued on next page

## Peak Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3 -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page

# Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-opt-mem-layout-trans=3`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 07:24:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 April 2012.