SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX300 S7, Intel Xeon E5-2690, 2.90 GHz

SPECint®_rate2006 =   353

SPECint_rate_base2006 = 339

CPU2006 license: 19	**Test date:** Dec-2011
Test sponsor: Fujitsu	**Hardware Availability:** Mar-2012
Tested by: Fujitsu	**Software Availability:** Dec-2011

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Hardware

CPU Name:	Intel Xeon E5-2690
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	8 cores, 1 chip, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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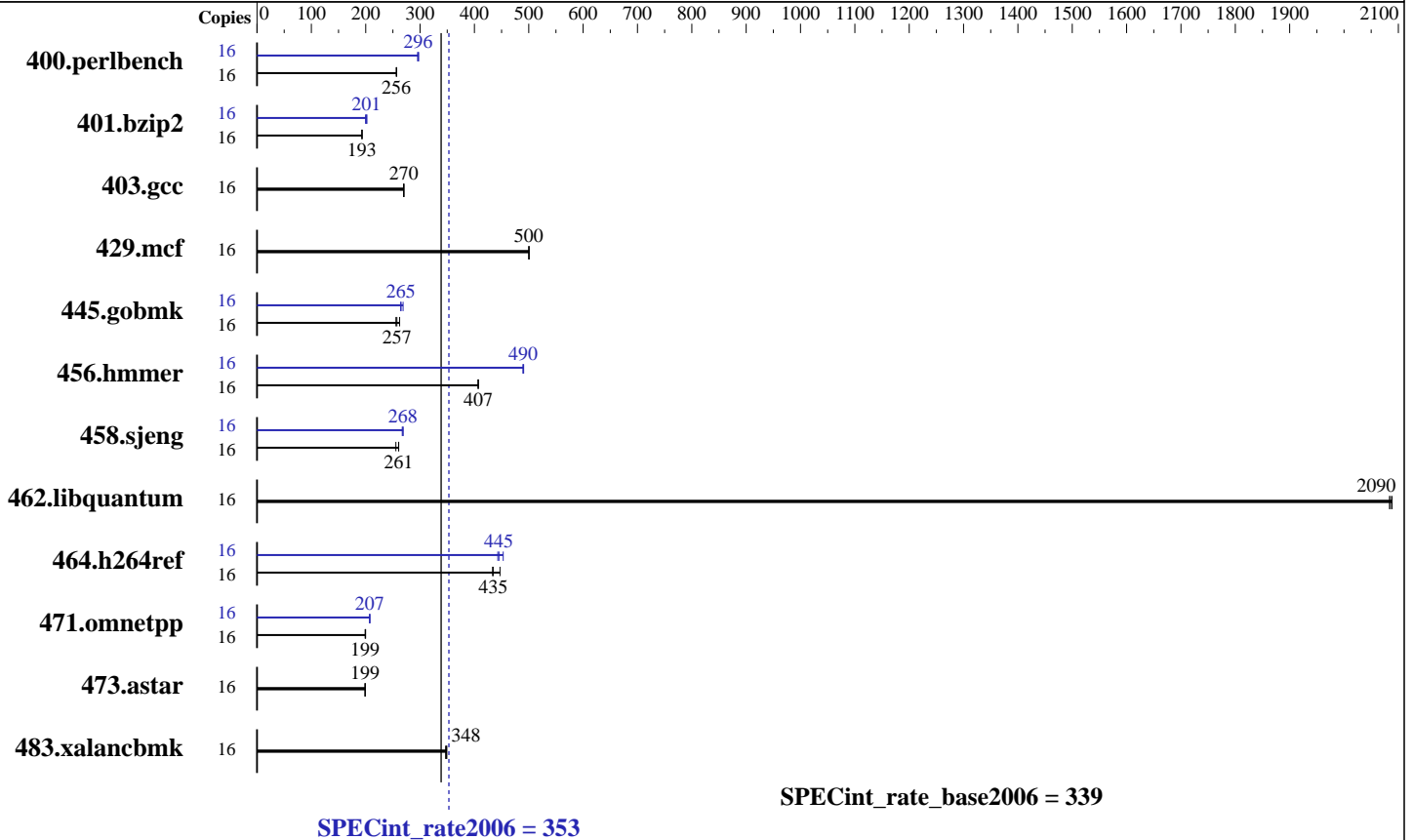SPECint_rate_base2006 = 339

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**610**	**256**	610	256	608	257	16	525	297	**527**	**296**	529	296
401.bzip2	16	**799**	**193**	798	193	800	193	16	764	202	773	200	**766**	**201**
403.gcc	16	**477**	**270**	476	270	478	270	16	**477**	**270**	476	270	478	270
429.mcf	16	**292**	**500**	292	500	291	501	16	**292**	**500**	292	500	291	501
445.gobmk	16	657	256	640	262	**652**	**257**	16	635	264	624	269	**633**	**265**
456.hmmer	16	**367**	**407**	367	407	367	407	16	304	491	**305**	**490**	305	490
458.sjeng	16	758	256	**743**	**261**	743	261	16	725	267	720	269	**721**	**268**
462.libquantum	16	**159**	**2090**	159	2090	159	2080	16	**159**	**2090**	159	2090	159	2080
464.h264ref	16	792	447	817	433	**815**	**435**	16	799	443	**795**	**445**	782	453
471.omnetpp	16	500	200	**502**	**199**	502	199	16	482	208	483	207	**482**	**207**
473.astar	16	564	199	**565**	**199**	565	199	16	564	199	**565**	**199**	565	199
483.xalancbmk	16	318	347	316	349	**317**	**348**	16	318	347	316	349	**317**	**348**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7
and the PRIMERGY TX300 S7 are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com

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Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

```
    400.perlbench: icc -m64
```

```
    401.bzip2: icc -m64
```

```
    456.hmmer: icc -m64
```

```
    458.sjeng: icc -m64
```

C++ benchmarks:
```
    icpc -m32
```

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Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
           458.sjeng: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

        400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

          401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                -auto-ilp32 -ansi-alias

           403.gcc: basepeak = yes

           429.mcf: basepeak = yes

         445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                -ansi-alias -opt-mem-layout-trans=3

          456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

           458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
                -auto-ilp32

     462.libquantum: basepeak = yes

         464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -ansi-alias

C++ benchmarks:

         471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/smartheap -lsmartheap

           473.astar: basepeak = yes

      483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For other inquiries, please contact webmaster@spec.org.

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