



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

SPECfp<sup>®</sup>\_rate2006 = 432

PRIMERGY TX300 S7, Intel Xeon E5-2650, 2.0 GHz

SPECfp\_rate\_base2006 = 420

CPU2006 license: 19

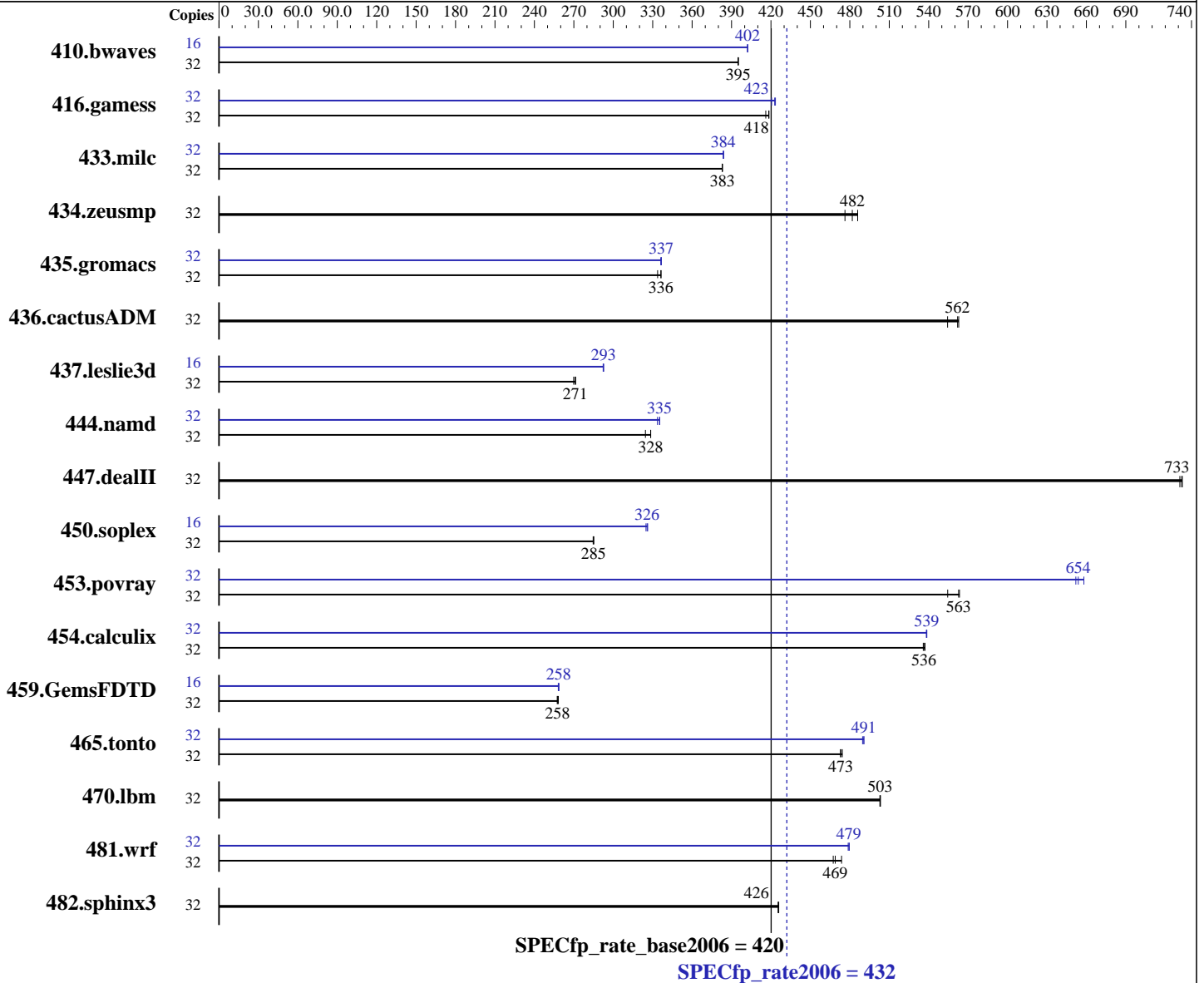
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2650  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	32	1101	395	<b><u>1101</u></b>	<b><u>395</u></b>	1101	395	16	<b><u>541</u></b>	<b><u>402</u></b>	541	402	541	402		
416.gamess	32	1497	418	1506	416	<b><u>1498</u></b>	<b><u>418</u></b>	32	<b><u>1482</u></b>	<b><u>423</u></b>	1490	420	1480	423		
433.milc	32	<b><u>767</u></b>	<b><u>383</u></b>	767	383	767	383	32	765	384	<b><u>765</u></b>	<b><u>384</u></b>	765	384		
434.zeusmp	32	<b><u>604</u></b>	<b><u>482</u></b>	599	486	611	476	32	<b><u>604</u></b>	<b><u>482</u></b>	599	486	611	476		
435.gromacs	32	<b><u>680</u></b>	<b><u>336</u></b>	685	334	679	336	32	680	336	679	337	<b><u>679</u></b>	<b><u>337</u></b>		
436.cactusADM	32	<b><u>680</u></b>	<b><u>562</u></b>	679	563	690	554	32	<b><u>680</u></b>	<b><u>562</u></b>	679	563	690	554		
437.leslie3d	32	<b><u>1110</u></b>	<b><u>271</u></b>	1109	271	1115	270	16	<b><u>514</u></b>	<b><u>293</u></b>	514	293	515	292		
444.namd	32	<b><u>781</u></b>	<b><u>328</u></b>	791	324	781	328	32	769	334	<b><u>766</u></b>	<b><u>335</u></b>	765	335		
447.dealII	32	<b><u>500</u></b>	<b><u>733</u></b>	501	731	499	733	32	<b><u>500</u></b>	<b><u>733</u></b>	501	731	499	733		
450.soplex	32	<b><u>936</u></b>	<b><u>285</u></b>	937	285	936	285	16	409	326	411	325	<b><u>410</u></b>	<b><u>326</u></b>		
453.povray	32	302	563	307	554	<b><u>303</u></b>	<b><u>563</u></b>	32	259	658	<b><u>260</u></b>	<b><u>654</u></b>	261	652		
454.calculix	32	493	536	491	537	<b><u>492</u></b>	<b><u>536</u></b>	32	490	539	491	538	<b><u>490</u></b>	<b><u>539</u></b>		
459.GemsFDTD	32	<b><u>1318</u></b>	<b><u>258</u></b>	1319	257	1315	258	16	656	259	658	258	<b><u>657</u></b>	<b><u>258</u></b>		
465.tonto	32	666	473	<b><u>665</u></b>	<b><u>473</u></b>	664	474	32	<b><u>642</u></b>	<b><u>491</u></b>	642	491	643	490		
470.lbm	32	874	503	<b><u>874</u></b>	<b><u>503</u></b>	874	503	32	874	503	<b><u>874</u></b>	<b><u>503</u></b>	874	503		
481.wrf	32	765	467	755	474	<b><u>762</u></b>	<b><u>469</u></b>	32	745	480	<b><u>746</u></b>	<b><u>479</u></b>	747	479		
482.sphinx3	32	1464	426	<b><u>1466</u></b>	<b><u>426</u></b>	1466	426	32	1464	426	<b><u>1466</u></b>	<b><u>426</u></b>	1466	426		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

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### General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7 and the PRIMERGY TX300 S7 are electronically equivalent.  
For information about Fujitsu please visit: <http://www.fujitsu.com>

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex: icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
416.gamess: `-DSPEC_CPU_LP64`  
433.milc: `-DSPEC_CPU_LP64`  
434.zeusmp: `-DSPEC_CPU_LP64`  
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
437.leslie3d: `-DSPEC_CPU_LP64`  
444.namd: `-DSPEC_CPU_LP64`  
447.dealII: `-DSPEC_CPU_LP64`  
453.povray: `-DSPEC_CPU_LP64`  
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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