



# SPEC® CINT2006 Result

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## NEC Corporation

### SPECint®\_rate2006 = 62.8

### Express5800/GT110d-S (Intel Pentium G630)

### SPECint\_rate\_base2006 = 60.4

CPU2006 license: 9006

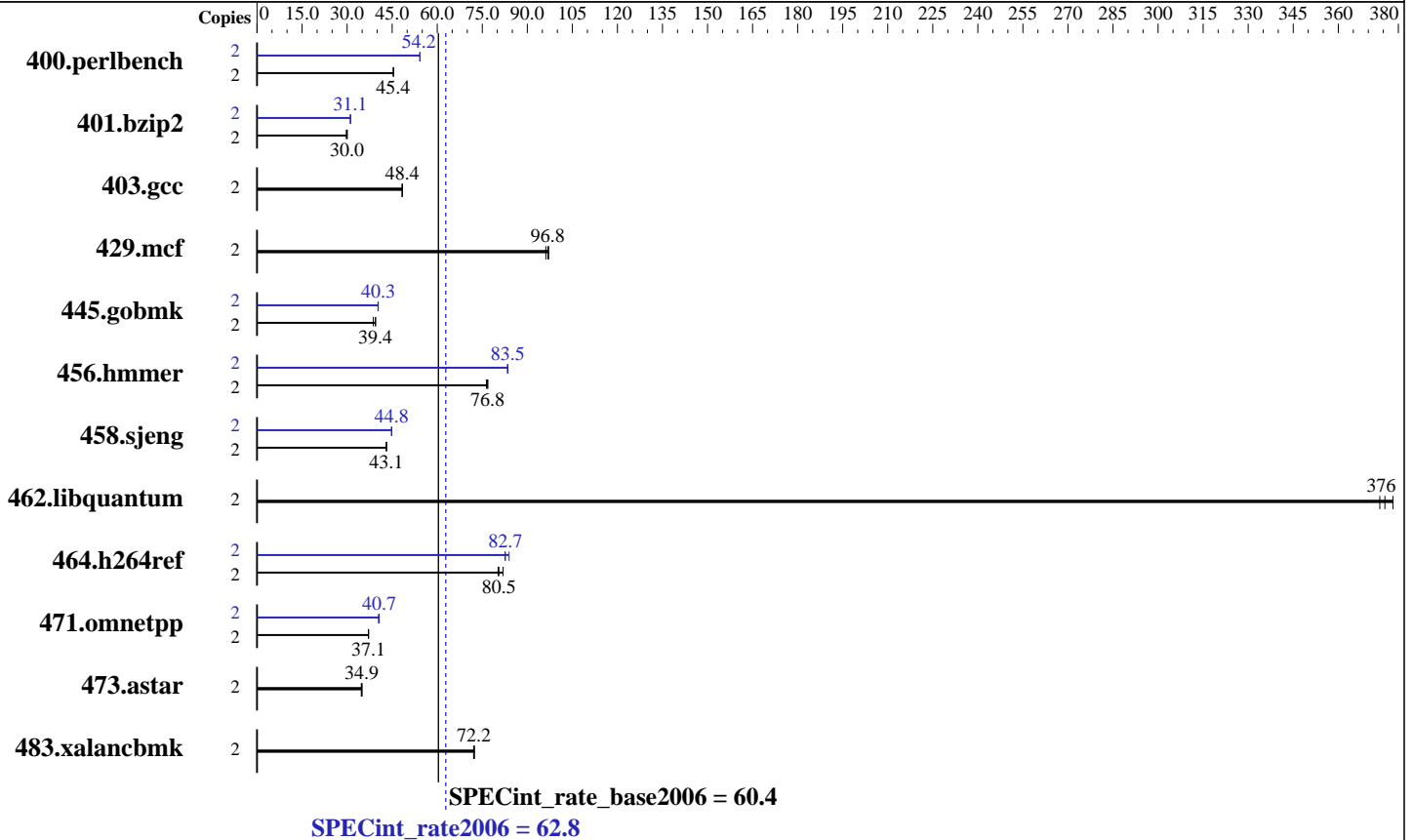
Test date: Mar-2012

Test sponsor: NEC Corporation

Hardware Availability: Jan-2012

Tested by: NEC Corporation

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Pentium G630  
 CPU Characteristics:  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)  
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>430</b>	<b>45.4</b>	430	45.4	430	45.4	2	360	54.3	<b>360</b>	<b>54.2</b>	360	54.2
401.bzip2	2	<b>643</b>	<b>30.0</b>	650	29.7	643	30.0	2	621	31.1	<b>621</b>	<b>31.1</b>	620	31.1
403.gcc	2	<b>332</b>	<b>48.4</b>	332	48.4	334	48.3	2	<b>332</b>	<b>48.4</b>	332	48.4	334	48.3
429.mcf	2	<b>188</b>	<b>96.8</b>	188	97.1	190	96.2	2	<b>188</b>	<b>96.8</b>	188	97.1	190	96.2
445.gobmk	2	542	38.7	<b>532</b>	<b>39.4</b>	532	39.5	2	<b>520</b>	<b>40.3</b>	521	40.3	520	40.4
456.hammer	2	243	76.8	244	76.4	<b>243</b>	<b>76.8</b>	2	<b>224</b>	<b>83.5</b>	224	83.4	223	83.5
458.sjeng	2	562	43.1	<b>562</b>	<b>43.1</b>	561	43.1	2	<b>540</b>	<b>44.8</b>	539	44.9	540	44.8
462.libquantum	2	111	374	<b>110</b>	<b>376</b>	110	378	2	111	374	<b>110</b>	<b>376</b>	110	378
464.h264ref	2	540	81.9	<b>550</b>	<b>80.5</b>	551	80.3	2	<b>535</b>	<b>82.7</b>	536	82.6	527	83.9
471.omnetpp	2	337	37.1	337	37.1	<b>337</b>	<b>37.1</b>	2	<b>307</b>	<b>40.7</b>	307	40.7	309	40.4
473.astar	2	<b>403</b>	<b>34.9</b>	401	35.0	404	34.7	2	<b>403</b>	<b>34.9</b>	401	35.0	404	34.7
483.xalancbmk	2	191	72.4	191	72.1	<b>191</b>	<b>72.2</b>	2	191	72.4	191	72.1	<b>191</b>	<b>72.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Default BIOS settings were used.

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

The Express5800/GT110d-S and the Express5800/GT110d models are electronically equivalent. The results have been measured on the Express5800/GT110d-S model.

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:

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## General Notes (Continued)

numactl --interleave=all runspec <etc>

Added glibc-static-2.12-1.25.el6.x86\_64.rpm  
to enable static linking

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

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## Peak Compiler Invocation (Continued)

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-R110d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-R110d-RevA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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