



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 381

Servidor Itaotec MX214 (Intel Xeon X5650)

SPECint\_rate\_base2006 = 365

CPU2006 license: 9001

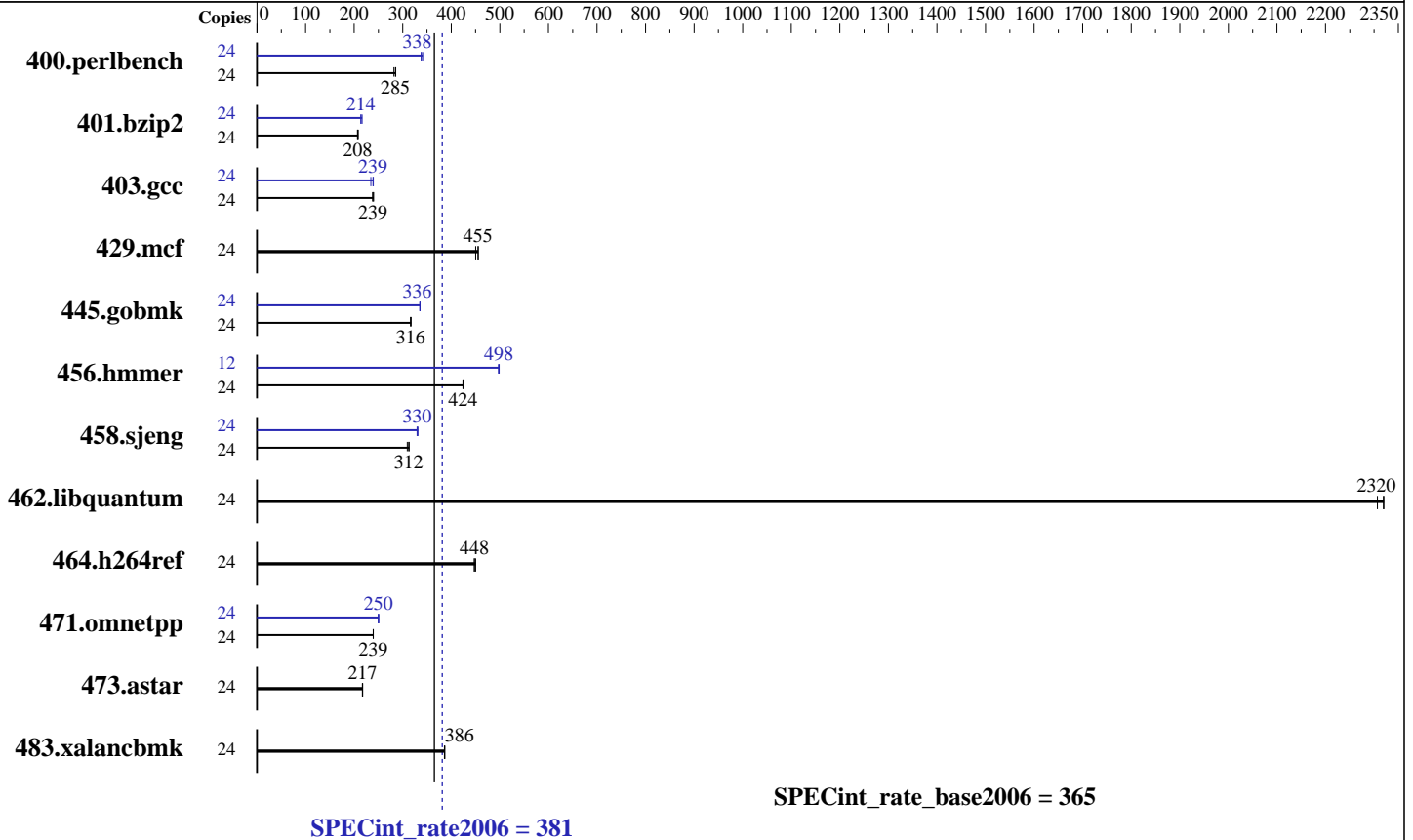
Test date: Jan-2012

Test sponsor: Itaotec

Hardware Availability: Jul-2011

Tested by: Itaotec

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 2 x 146 GB, SAS, 15000 RPM, RAID 0  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>823</b>	<b>285</b>	832	282	821	286	24	687	341	694	338	<b>693</b>	<b>338</b>
401.bzip2	24	1120	207	1112	208	<b>1116</b>	<b>208</b>	24	1070	216	1083	214	<b>1082</b>	<b>214</b>
403.gcc	24	812	238	<b>809</b>	<b>239</b>	805	240	24	807	239	<b>809</b>	<b>239</b>	824	234
429.mcf	24	486	450	<b>481</b>	<b>455</b>	480	456	24	486	450	<b>481</b>	<b>455</b>	480	456
445.gobmk	24	794	317	<b>796</b>	<b>316</b>	796	316	24	750	336	<b>750</b>	<b>336</b>	751	335
456.hammer	24	527	425	528	424	<b>528</b>	<b>424</b>	12	225	499	225	497	<b>225</b>	<b>498</b>
458.sjeng	24	939	309	<b>930</b>	<b>312</b>	927	313	24	881	330	877	331	<b>879</b>	<b>330</b>
462.libquantum	24	216	2310	<b>214</b>	<b>2320</b>	214	2320	24	216	2310	<b>214</b>	<b>2320</b>	214	2320
464.h264ref	24	1187	448	1180	450	<b>1187</b>	<b>448</b>	24	1187	448	1180	450	<b>1187</b>	<b>448</b>
471.omnetpp	24	<b>627</b>	<b>239</b>	626	239	627	239	24	<b>599</b>	<b>250</b>	600	250	599	250
473.astar	24	<b>775</b>	<b>217</b>	777	217	775	217	24	<b>775</b>	<b>217</b>	777	217	775	217
483.xalancbmk	24	<b>429</b>	<b>386</b>	428	387	429	386	24	<b>429</b>	<b>386</b>	428	387	429	386

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.  
DCU Prefetcher disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itautec-Servidor\\_Itautec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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