



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL580 G7
(2.40 GHz, Intel Xeon E7-4870)

SPECfp®2006 = 58.7

SPECfp_base2006 = 55.7

CPU2006 license: 3

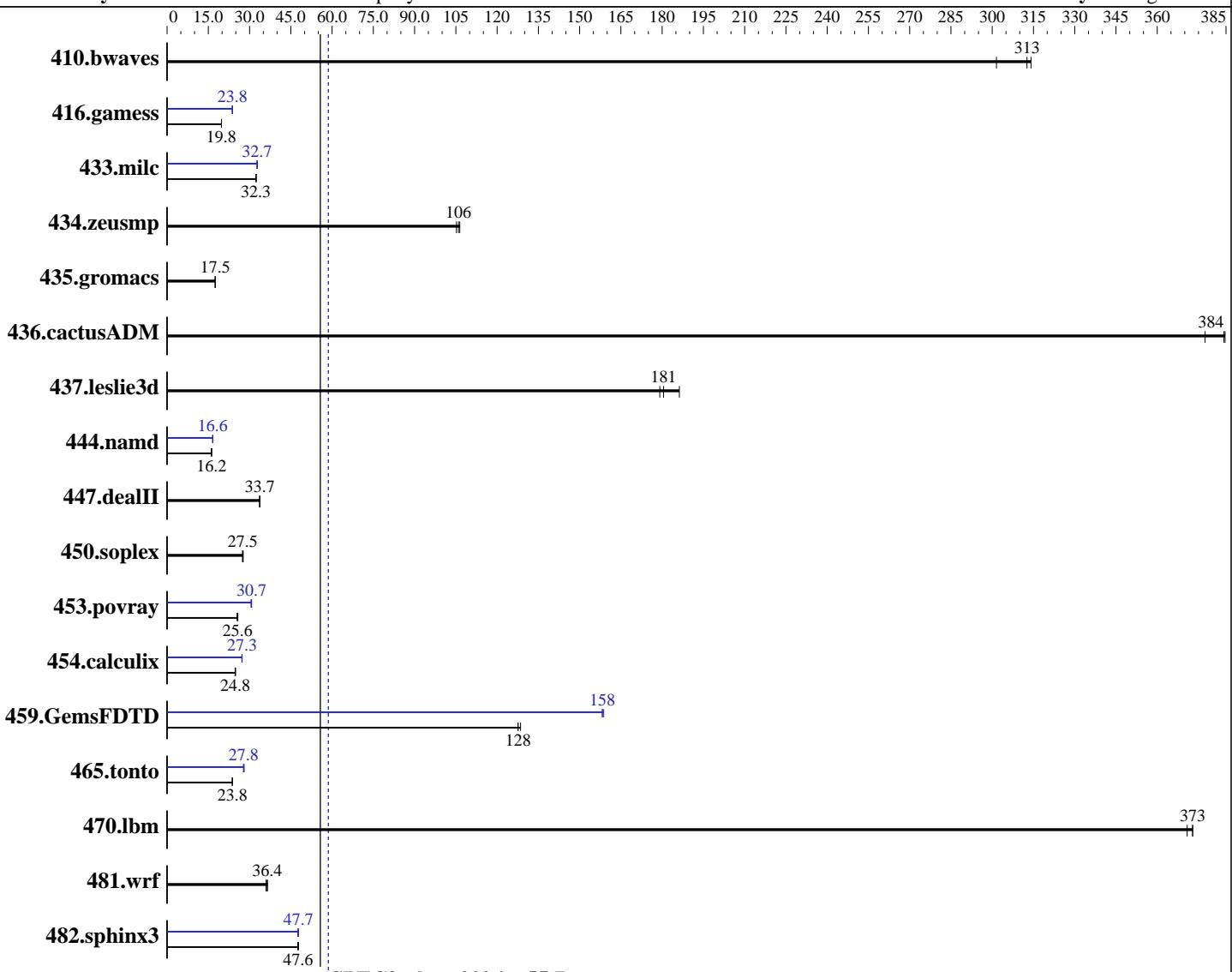
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2011

Hardware Availability: Apr-2011

Software Availability: Aug-2011



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Hardware

CPU Name: Intel Xeon E7-4870
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (64 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 2 x 146 GB 10 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	43.3	314	45.1	302	43.5	313	43.3	314	45.1	302	43.5	313
416.gamess	990	19.8	989	19.8	990	19.8	827	23.7	824	23.8	823	23.8
433.milc	284	32.3	283	32.4	284	32.3	281	32.6	279	32.9	280	32.7
434.zeusmp	85.5	106	85.9	106	86.5	105	85.5	106	85.9	106	86.5	105
435.gromacs	408	17.5	408	17.5	407	17.5	408	17.5	408	17.5	407	17.5
436.cactusADM	31.7	377	31.1	385	31.1	384	31.7	377	31.1	385	31.1	384
437.leslie3d	52.5	179	50.5	186	52.1	181	52.5	179	50.5	186	52.1	181
444.namd	496	16.2	496	16.2	496	16.2	483	16.6	482	16.6	483	16.6
447.dealII	340	33.6	339	33.8	339	33.7	340	33.6	339	33.8	339	33.7
450.soplex	301	27.7	303	27.5	304	27.4	301	27.7	303	27.5	304	27.4
453.povray	207	25.6	209	25.5	207	25.7	173	30.7	173	30.7	174	30.5
454.calculix	333	24.7	332	24.9	332	24.8	302	27.3	303	27.2	303	27.3
459.GemsFDTD	83.1	128	82.6	129	83.1	128	67.0	158	66.8	159	67.1	158
465.tonto	414	23.8	414	23.8	415	23.7	353	27.8	354	27.8	351	28.0
470.lbm	36.8	373	36.8	373	37.0	371	36.8	373	36.8	373	37.0	371
481.wrf	310	36.0	307	36.4	306	36.5	310	36.0	307	36.4	306	36.5
482.sphinx3	410	47.5	408	47.8	409	47.6	408	47.8	408	47.7	410	47.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS configuration:
Intel Hyper-Threading set to Disabled

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32:/cpu2006/ic12.1-libs/intel64"
OMP_NUM_THREADS = "20"

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html>
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

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For other inquiries, please contact webmaster@spec.org.

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