



SPEC® CINT2006 Result

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Hewlett-Packard Company

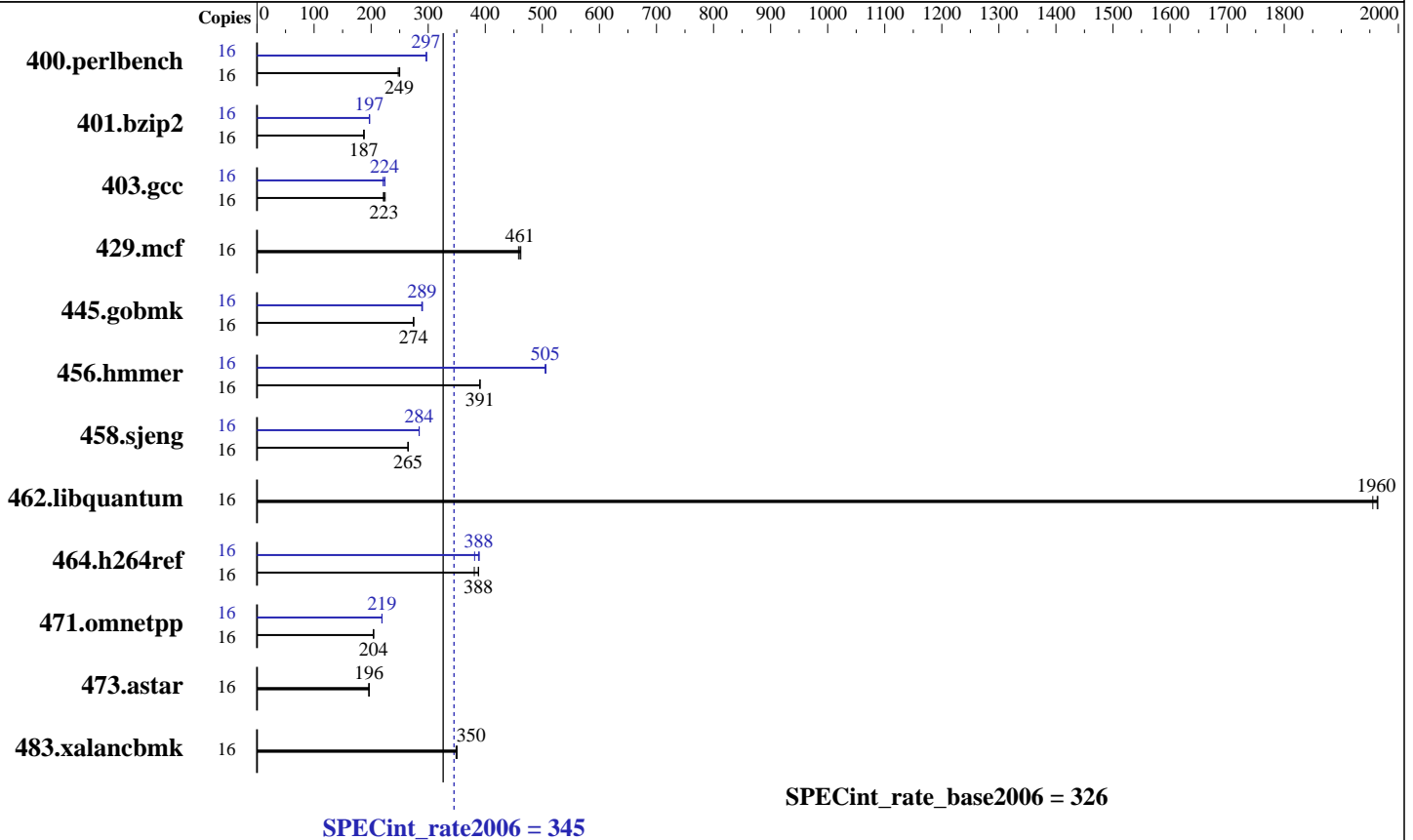
SPECint®_rate2006 = 345

ProLiant DL380 G7
(3.46 GHz, Intel Xeon X5677)

SPECint_rate_base2006 = 326

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2011
Hardware Availability: Mar-2010
Software Availability: Sep-2011



Hardware

CPU Name: Intel Xeon X5677
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 15 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	625	250	<u>627</u>	<u>249</u>	632	247	16	<u>527</u>	<u>297</u>	527	297	526	297
401.bzip2	16	822	188	<u>824</u>	<u>187</u>	826	187	16	785	197	782	197	<u>784</u>	<u>197</u>
403.gcc	16	582	221	<u>579</u>	<u>223</u>	573	225	16	<u>576</u>	<u>224</u>	583	221	575	224
429.mcf	16	316	462	318	458	<u>317</u>	<u>461</u>	16	316	462	318	458	<u>317</u>	<u>461</u>
445.gobmk	16	<u>612</u>	<u>274</u>	611	275	612	274	16	581	289	579	290	<u>581</u>	<u>289</u>
456.hammer	16	382	390	382	391	<u>382</u>	<u>391</u>	16	295	506	295	505	<u>295</u>	<u>505</u>
458.sjeng	16	<u>731</u>	<u>265</u>	732	264	731	265	16	681	284	<u>682</u>	<u>284</u>	682	284
462.libquantum	16	170	1960	<u>169</u>	<u>1960</u>	169	1960	16	170	1960	<u>169</u>	<u>1960</u>	169	1960
464.h264ref	16	912	388	<u>913</u>	<u>388</u>	930	381	16	<u>912</u>	<u>388</u>	929	381	909	389
471.omnetpp	16	<u>490</u>	<u>204</u>	489	204	491	204	16	457	219	<u>457</u>	<u>219</u>	456	219
473.astar	16	<u>573</u>	<u>196</u>	573	196	571	197	16	<u>573</u>	<u>196</u>	573	196	571	197
483.xalancbmk	16	<u>315</u>	<u>350</u>	315	350	317	349	16	<u>315</u>	<u>350</u>	315	350	317	349

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/smartheap/cpu2006/ic12.1-libs/ia32/:/cpu2006/ic12.1-libs/intel64"



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 01:03:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 December 2011.