



# SPEC<sup>®</sup> CFP2006 Result

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## Hewlett-Packard Company

ProLiant DL380 G7  
(2.26 GHz, Intel Xeon E5607)

SPECfp<sup>®</sup>\_rate2006 = **NC**

SPECfp\_rate\_base2006 = **NC**

CPU2006 license: 3  
Test sponsor: Hewlett-Packard Company  
Tested by: Hewlett-Packard Company

Test date: Oct-2011  
Hardware Availability: Feb-2011  
Software Availability: Sep-2011

**SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.**

### Hardware

CPU Name: Intel Xeon E5607  
CPU Characteristics:  
CPU MHz: 2267  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx4 PC3L-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15 K SAS  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86\_64  
Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803  
Auto Parallel: No  
File System: ext3  
System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

**Non-Compliant**



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
416.gamess	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
433.milc	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
434.zeusmp	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
435.gromacs	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
436.cactusADM	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
437.leslie3d	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
444.namd	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
447.dealII	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
450.soplex	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
453.povray	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
454.calculix	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
459.GemsFDTD	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
465.tonto	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
470.lbm	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
481.wrf	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		
482.spm	8	NC	NC	NC	NC	NC	NC	8	NC	NC	NC	NC	NC	NC		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores



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## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_page_cache
runspec command invoked through numactl file:
numactl --interleave=all runspec <etc>
```

## Platform Notes

```
BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH="/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32:/cpu2006/ic12.1-libs/intel64"
```

## Base Compiler Invocation

C benchmarks:

C++ benchmarks:

```
icc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -noFor_main
436.cactusADM: -DSPEC_CPU_LP64 -noFor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -noFor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64  
482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64  
450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
445.gamess: -DSPEC\_CPU\_LP64  
453.milc: -DSPEC\_CPU\_LP64  
434.zetmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64 -nofor\_main  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.nvray: -DSPEC\_CPU\_LP64  
454.cactus: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.cwv: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

Continued on next page



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## Peak Optimization Flags (Continued)

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto  
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flag files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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