



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY TX120 S3, Intel Celeron G530, 2.40 GHz

SPECfp®_rate2006 = 51.9

SPECfp_rate_base2006 = 50.5

CPU2006 license: 19

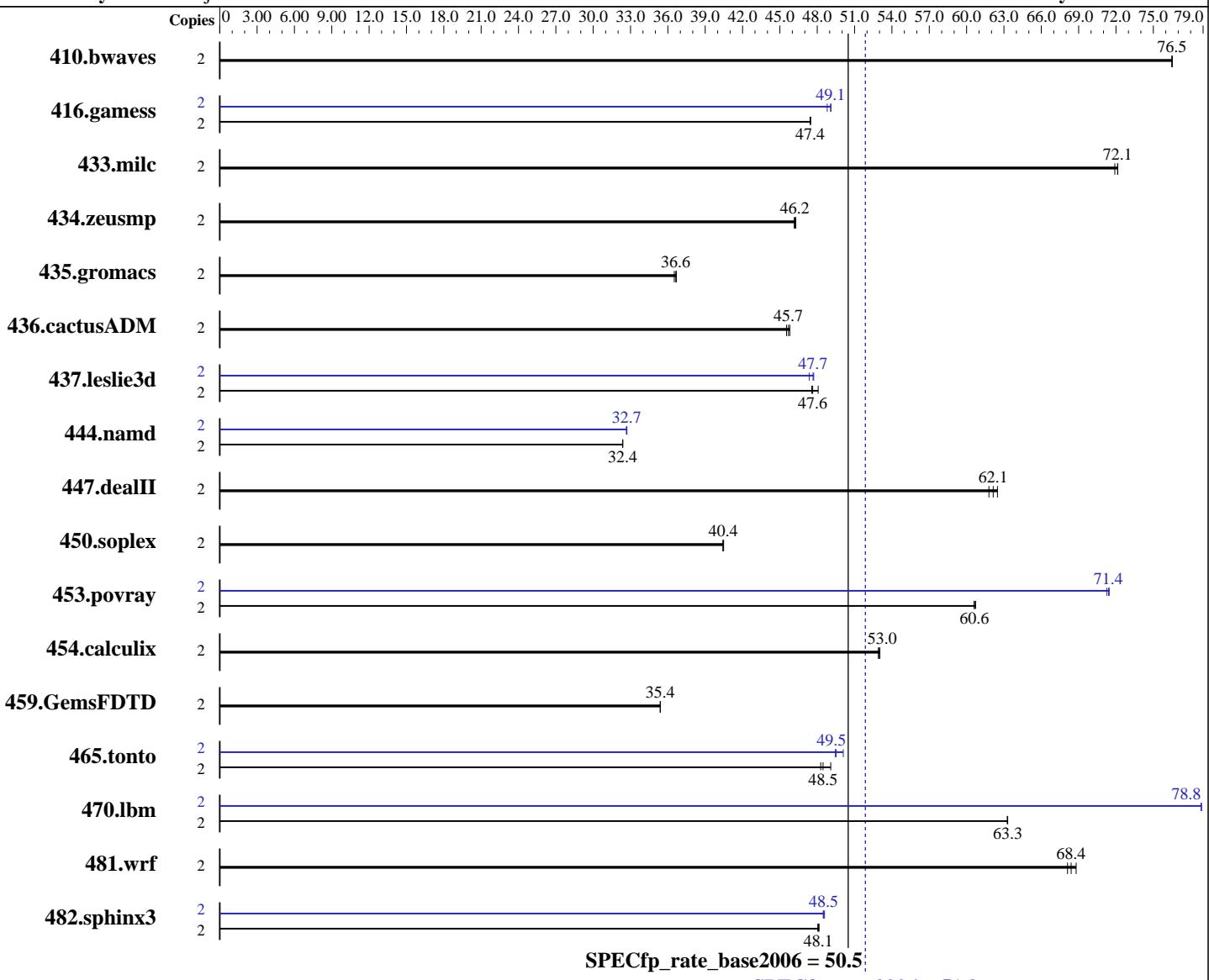
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Celeron G530
CPU Characteristics:
CPU MHz:
FPU:
CPU(s) enabled:
CPU(s) orderable:
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1067 MHz and CL7)
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM
 Other Hardware: --

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	355	76.5	<u>355</u>	<u>76.5</u>	355	76.5	2	355	76.5	<u>355</u>	<u>76.5</u>	355	76.5	355	76.5
416.gamess	2	<u>826</u>	<u>47.4</u>	825	47.5	826	47.4	2	<u>798</u>	<u>49.1</u>	802	48.8	<u>797</u>	<u>49.1</u>		
433.milc	2	<u>255</u>	<u>72.1</u>	255	71.9	254	72.1	2	<u>255</u>	<u>72.1</u>	255	71.9	254	72.1		
434.zeusmp	2	394	46.2	393	46.3	<u>394</u>	<u>46.2</u>	2	394	46.2	393	46.3	<u>394</u>	<u>46.2</u>		
435.gromacs	2	<u>390</u>	<u>36.6</u>	389	36.7	391	36.5	2	<u>390</u>	<u>36.6</u>	389	36.7	391	36.5		
436.cactusADM	2	525	45.5	522	45.8	<u>523</u>	<u>45.7</u>	2	525	45.5	522	45.8	<u>523</u>	<u>45.7</u>		
437.leslie3d	2	391	48.1	<u>395</u>	<u>47.6</u>	395	47.6	2	394	47.7	397	47.4	<u>394</u>	<u>47.7</u>		
444.namd	2	495	32.4	496	32.4	<u>496</u>	<u>32.4</u>	2	491	32.7	<u>491</u>	<u>32.7</u>	490	32.7		
447.dealII	2	370	61.8	366	62.5	<u>368</u>	<u>62.1</u>	2	370	61.8	366	62.5	<u>368</u>	<u>62.1</u>		
450.soplex	2	<u>412</u>	<u>40.4</u>	412	40.5	413	40.4	2	<u>412</u>	<u>40.4</u>	412	40.5	413	40.4		
453.povray	2	176	60.6	<u>175</u>	<u>60.6</u>	175	60.7	2	149	71.3	149	71.5	<u>149</u>	<u>71.4</u>		
454.calculix	2	<u>311</u>	<u>53.0</u>	311	53.0	312	52.9	2	<u>311</u>	<u>53.0</u>	311	53.0	312	52.9		
459.GemsFDTD	2	600	35.4	600	35.4	<u>600</u>	<u>35.4</u>	2	600	35.4	600	35.4	<u>600</u>	<u>35.4</u>		
465.tonto	2	<u>406</u>	<u>48.5</u>	401	49.1	408	48.3	2	393	50.1	<u>397</u>	<u>49.5</u>	398	49.5		
470.lbm	2	434	63.3	434	63.3	<u>434</u>	<u>63.3</u>	2	348	78.9	349	78.8	<u>349</u>	<u>78.8</u>		
481.wrf	2	325	68.8	<u>327</u>	<u>68.4</u>	328	68.1	2	325	68.8	<u>327</u>	<u>68.4</u>	328	68.1		
482.sphinx3	2	<u>811</u>	<u>48.1</u>	811	48.0	810	48.1	2	804	48.5	802	48.6	<u>803</u>	<u>48.5</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Large pages were not enabled for this run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
 Binaries were compiled on RHEL5.5



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xsse4.2 -ipo -O3 -no-prec-div -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
            -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep -static
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

```
459.GemsFDTD: basepeak = yes
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
            -inline-calloc -opt-malloc-options=3
            -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: basepeak = yes
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html>



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You can also download the XML flags sources by saving the following links:

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<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml>

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